What is peb? Printed Circuit Board

> PCB is a integral part of any Electrical and

> PCB ? s mad up of copper, Dienectric material, Solder mask and Silkeren.

PAD: It is a conductive area on the board surface cohere electronic components are soldered.

- (1) Through Hole pads: Thise pads are designed for companie that have leads or pins passing through the pcb.
- (2) Surface Mount Pads: Sm pads are designed for Components that are mounted directly onto the surface of the board.

Solder mask: It is process of covering the peb to protect against corrosion, rost Prevent soider bridges from forming between Closery spaced solar pads.

-> Otherwise it may lead to short circuit.

These masking should be in many caars but green

Silkscreen: It : 1 used as a reference indicator for placing components on a PCB Board, marks, 1090s,

> For soldering purpose it is very useful to identify the palarity of the component which is going to soldered ont · Single sided · Double sided

· Mult? - layer

· Rigid

· Flexible

· Rigid - Frex

Layer stack up: Top Silkscrun Top soldermark pre-preg COVE Pri-prig 4 Bottom Soldermark Bottom Silkscrum Via: It amous a conductive connection between different layers. Types of Via: > Through - Hore via :- It extends through the entire thickness of the PCB, connecting the top and bottom layers > Brind via:- It connects an outer layer of the PCB to one or more inner layers but does not go through the entire board. > Buried via: - A burried via connects two or more inner layers of the PCB without reaching the outerlayers Blind Buried layer 2

PCB Performance classes.

> crass 1: General Electronic Products.

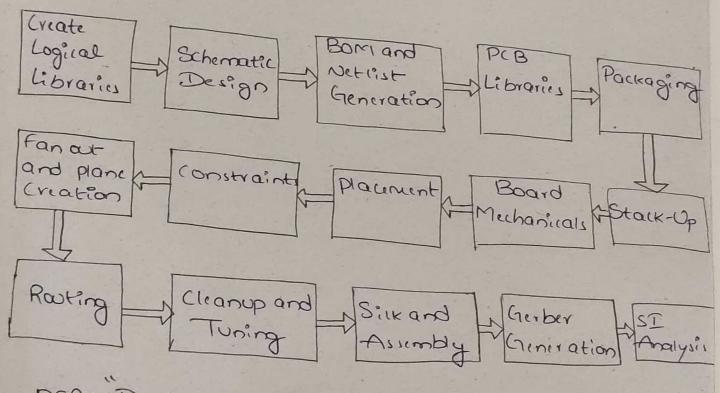
Consumer electronics , Toys etc...

> Class 2 : Dedicated Service Products

Professional Systems, Computers, Communication system -> class 3: High Recability Products

Radaris, Weapon systems, Satericte Communication

PCB Design Flow Basic:



DFA: Design for Assembly "have set of principles and their goal is to create a per layout that is not only functional, electrically sound, efficient and cost-effective-to assemble.

DET: "Design for Testability" have set of Principles and their goal is to enhance the efficiency and accoraty of testing procedures, identifying and isolating fault or defects in PCB.