

# Verification Quiz 1

تم حفظ المسئدة. 

\* Indicates that question is required

Email address\*

- \* interfaces may contain modports, clockingblocks, functions, tasks, always/initial blocks, but can we define interfaces inside modules

yes

No

\* interfaces may be used for

reducing simulation run time

decrease the design critical path delay

do some communication functionalities.

all of the above

\* Assertions are used to

check the design behavior

check the VAR for offside goals

check in Dahab for a vacation

check the crush profile every clk cycle and being a synchronized stalker

\* which of the following operators can be used to describe the repetition of Boolean  
.expressions in an assertion for n times with a non-consecutive manner

[\*n]

[1:\$]

[\$:1]

[->n]

\* ?if a=0 but b=1, is the following assertion going to fail

```
) assert property  
 disable iff (c) ( posedge clk)@  
 c |=> a ###1 b  
 ;(
```

yes

No

depends on c

can't tell

\* the following expression can be replaced by  
`b ##[1:$] c ##[1:$] b`

`b[->3]`

`b[=3]`

`b[*3]`

none of the above

\* the following expression can be replaced by  
`b ##[1:$] c ##[1:$] b ##[1:$]`

`b [->3]`

`b [=3]`

`b[*3]`

none of the above

\* cover points can exist in

covergroups

modules

classes

initial/always blocks

all of the above if they have random variables

- \* to declare the direction of signals with respect to each module connected to an interface, we use

Modport

podcast

airpods

crop-top

- \* what is the difference between assertions and usual if-statements when taking about checking the design behavior

assertions are easier to write/read/debug

assertions causes simulation to stop when it fails

both answers are correct

Page 1 of 1

Erase form

Submit