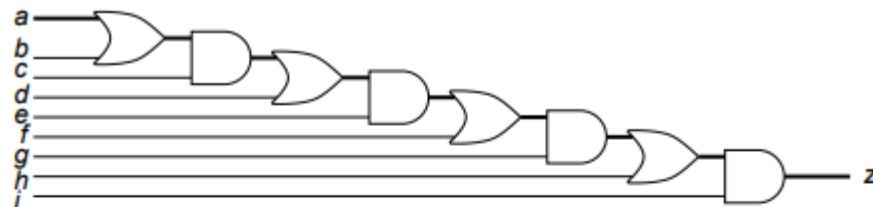
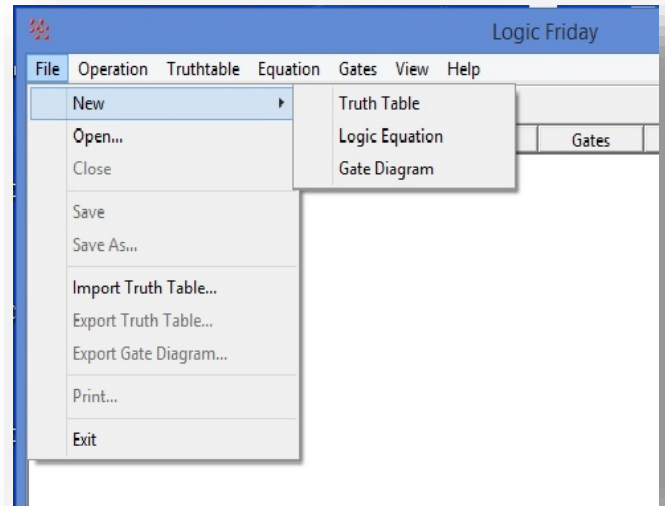
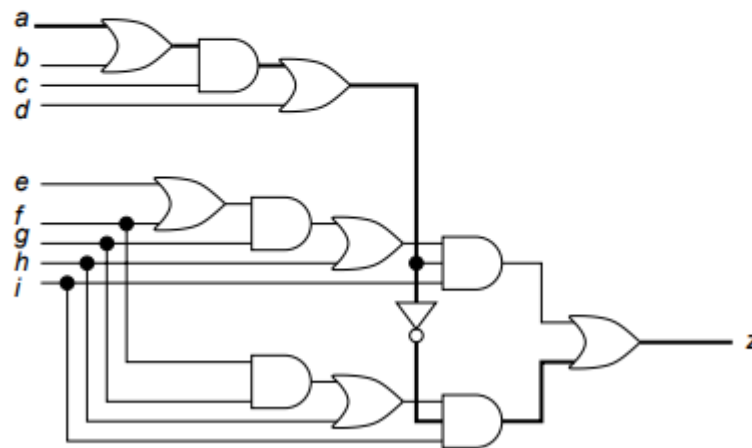


CSE 313s: Digital Design Verification ASSIGNMENT 1

1. Visit the following site: https://download.cnet.com/Logic-Friday/3000-20415_4-75848245.html
2. Download the Logic Friday tool (file: lf114_setup.exe (859 KB))
3. Get acquainted with the tool.
4. From the file menu, select a new to enter a design in the form of your choice.
5. After you enter the function press on the minimize icon, to minimize your design.
6. The figure below shows two versions of the same design. The second version was designed to reduce the critical path delay of the first version. You are required to prove that both versions are equivalent, by constructing a miter circuit, and showing that its output is 0.



(i) Original circuit.



(ii) Redesigned circuit.