

Timer

count

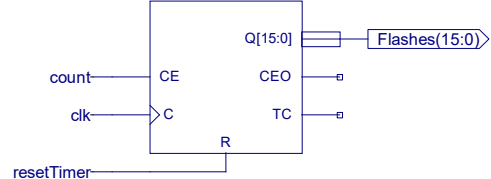
clk

resetTimer

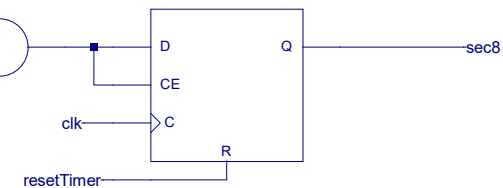
sec8

sec4

CB16RE



FDRE



FDRE

