



Top Project Status			
<b>Project File:</b>	Lab5.xise	<b>Parser Errors:</b>	No Errors
<b>Module Name:</b>	Top	<b>Implementation State:</b>	New
<b>Target Device:</b>	xc3s100e-5cp132	• <b>Errors:</b>	
<b>Product Version:</b>	ISE 14.7	• <b>Warnings:</b>	
<b>Design Goal:</b>	Balanced	• <b>Routing Results:</b>	
<b>Design Strategy:</b>	<a href="#">Xilinx Default (unlocked)</a>	• <b>Timing Constraints:</b>	
<b>Environment:</b>		• <b>Final Timing Score:</b>	

Detailed Reports 					
Report Name	Status	Generated	Errors	Warnings	Infos
Synthesis Report					
Translation Report					
Map Report					
Place and Route Report					
Power Report					
Post-PAR Static Timing Report					
Bitgen Report					

Secondary Reports 		
Report Name	Status	Generated
<a href="#">ISIM_Simulator_Log</a>	Out of Date	Wed May 11 20:33:40 2016

**Date Generated:** 05/11/2016 - 22:05:07