# Use of Operational Amplifier as an Integrator and Differentiator

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June 21, 2022

## 1 Abstract

Our group measured the frequency response of two similar circuits, that , while using the same components, show opposing behavior, not only in the resulting waveform, but also in the frequency Response.

## 2 Introduction

## 2.1 Operational Amplifier

The circuits are composed of a resistor (R), a capacitor (C) and an operational amplifier. To analyze their behavior we need to know the functioning of an ideal operational amplifier, herein known as "OPAMP".

The ideal OPAMP is a voltage-controlled voltage source, with a voltage gain  $E = \frac{V_o}{V_i}$ , where  $V_o$  is the output voltage and  $V_i$  is the input voltage, in this case  $V_o$  is referenced to ground, while  $V_i$  is the voltage difference between two poles, that are known as inverting  $(V^+)$  and not inverting  $(V^-)$  inputs, so  $V_i = V^+ - V^-$ . In the ideal OPAMP  $E \to \infty$ .

To run the circuit in the desired configuration we have to use a negative feedback loop, in this way we can use virtual short circuit principle, this is related to the stability of negative feedback and  $E \to \infty$ , due to this factor  $V^+ - V^- = 0$ , it is also named 'Virtual Ground' because often the not-inverting input is at ground potential, so  $V^+ = 0 = V^-$ .

#### 2.2 Integrator Circuit

As in 2.2, in the feedback loop we have a capacitor and a resistor, so writing the Ohm's Law, Capacitor So to ob Law  $(q = Cv \implies \frac{\mathrm{d}q}{\mathrm{d}t} = i = C\frac{\mathrm{d}v}{\mathrm{d}t})$  and KCL we over (1):

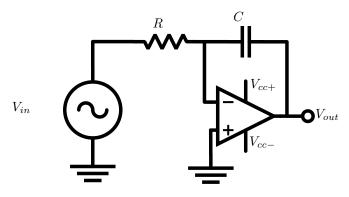


Figure 1: Schematic of an Integrator Circuit

obtain:

$$\begin{cases} v_{IN} - R \cdot i_R = v^- \\ i_R = i_C \\ v^+ - v^- = 0 \\ v^+ = 0 \\ i_C = C \frac{\mathrm{d}v^- - v_{OUT}}{\mathrm{d}t} \\ i = \frac{v_{IN}}{R} = -C \frac{\mathrm{d}v_{OUT}}{\mathrm{d}t} \end{cases}$$

From last equation, resolving for  $v_{OUT}$ :

$$v_{OUT} = -\frac{1}{CR} \int_0^t v_{IN} \, dt + v(0) \tag{1}$$

It is visible from (1), that the behaviour of this circuit in time domain is to produce the integral of the input voltage function.

The frequency response is defined as

$$H = \frac{v_{OUT}}{v_{IN}}(f)$$

So to obtain H, we apply Fourier Transform (FT) over (1):

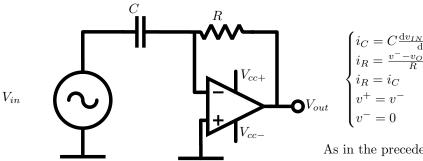
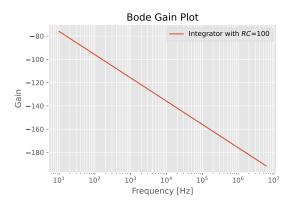


Figure 2: Schematic of a Differentiator Circuit

$$V_{OUT} = -\frac{1}{CR(j2\pi f)}V_{IN}$$
 (2)

$$H = \frac{V_{OUT}}{V_{IN}} = \frac{1}{RC(j2\pi f)}$$
 (3)



Plotting the Bode Gain Diagramm shows a constant negative monotony of -20 dB/dec, this must kept in consideration while using the circuit because it means the output waveform is attenuated with an higher frequency.

#### 2.3 Differentiator Circuit

As shown in 2.3, the circuit is similar to the previous case, but in this case the capacitor and the resistor are swapped. This change produces a completely different response to the input waveform, it can be understood writing KCL, the Ohm's Law and the capacitor law.

$$\begin{cases} i_C = C \frac{dv_{IN} - v^-}{dt} \\ i_R = \frac{v^- - v_{OUT}}{R} \\ i_R = i_C \\ v^+ = v^- \\ v^- = 0 \end{cases} \qquad C \frac{dv_{IN}}{dt} = \frac{-v_{OUT}}{R}$$

As in the precedent case resolving for  $v_{OUT}$ 

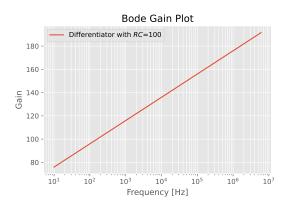
$$v_{OUT} = RC \frac{\mathrm{d}v_{IN}}{\mathrm{d}t} \tag{4}$$

(4) shows that  $v_{OUT}$  is a waveform proportional to the derivative of the input waveform.

Calculating H produces:

$$V_{OUT} = RC \cdot (j2\pi f)V_{IN} \tag{5}$$

$$H = RC \cdot (j2\pi f) \tag{6}$$



In this case the circuit has a constant positive monotony of 20 dB/dec, so with an higher frequency the output signal will be amplified.

## 3 Measurement Methods

#### 3.1 Materials

#### 3.1.1 Instruments

- Oscilloscope "Tektronix TDS 1012B";
- Function Generator "Agilent 3322A";
- Power Supply.

#### 3.1.2 Equipment

- OPAMP "LT081";
- Breadboard;
- Wires:
- Resistors;
- Capacitors;

#### 3.2 Procedure

The two circuits are built as shown in the previous pictures, the Oscilloscope probe was connected in the point named  $V_{OUT}$ , grounded to ground. The function generator was connected to  $V_{IN}$  and ground. A dual power supply powered the OPAMP, supplying the necessary  $V_{CC+}$  and  $V_{CC-}$ . The measure procedure was: set a frequency of the sine wave on the function generator, using the oscilloscope it was measured the voltage peak to peak of the input and the output signal. After that was used the cursor mode on oscilloscope to measure the phase between the two waveforms, for this the cursor was set on time domain and moved in corrispondence of a peak of the input waveform and the nearest peak of the output waveform, at the measure was assigned a positive sign if the output is to the right of input, negative if the output is to the left of input. The frequencies were choosen using an approximate log scale (like 1, 2, 3, 5, 8, 10).

## 4 Analysis

## 4.1 Computation

During the measuring process datas were saved on a csv file. To analyse it was used a Python enviroment, with the libraries "Numpy, Pandas and Matplotlib", this environment was inserted in a Jupyter Notebook to visualize the results of computation in real time. The code computed and plotted from the data the Bode Diagram and the Nyquist Diagram. At this point it was used another software, "NGSpice", to simulate the expected behavior in frequency of the circuits. Then point datas from experiment and simulation are plotted and compared in the Python environment.

- 4.2 Integrator
- 4.3 Differentiator
- 5 Conclusion