

Parallel & Concurrent Programming

Theory Assignment 4: Locks

Autumn 2023

1. Exercise 7.1 on page 181 of the 2nd edition of the book (page 194 in the pdf).
2. Exercise 8.6 on page 198 of the 2nd edition of the book (page 211 in the pdf).
3. Develop a abortable lock based on MCS lock. In other words, each thread should spin on its local memory location and not on a memory location belonging to another processor/thread (that could potentially be far away).

Due Date: 8th November 2023 (Wednesday), 3:00 pm before the class.

The answers will be discussed in the class. So, no late submission will be accepted.