



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.



CONSOLIDATED MEMO OF MARKS/GRADES AND CREDITS



C5109101 B.Tech. ELECTRICAL & ELECTRONICS ENGINEERING

Name : PAGADALA PRANITHA

Hall Ticket No : 19U15A0227

Serial. No. : 21600000984

Year of Admission : 2019-2020

Name of the College : U1-SCET, GHATKESAR

Month & Year of Final Exam : September, 2022

Class Awarded : FIRST CLASS

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S.No.	SUBJECT TITLE	GRADE	POINT	GRADE	CREDITS	S.No.	SUBJECT TITLE	GRADE	POINT	GRADE	CREDITS
I SEMESTER						II SEMESTER					
DIRECT ADMISSION INTO II-YEAR UNDER LATERAL ENTRY SCHEME											
I SEMESTER						II SEMESTER					
1	ENGINEERING MECHANICS	5	C	4.0		1	CONTROL SYSTEMS	6	B	4.0	
2	ELECTRICAL MACHINES - I	6	B	4.0		2	LAPLACE TRANSFORMS, NUMERICAL METHODS & COMP	7	B+	4.0	
3	ELECTRICAL MACHINES LAB - I	10	O	1.0		3	CONTROL SYSTEMS LAB	10	O	1.0	
4	ELECTRICAL CIRCUIT ANALYSIS	5	C	4.0		4	DIGITAL ELECTRONICS LAB	10	O	1.0	
5	ELECTRICAL CIRCUITS LAB	10	O	1.0		5	ELECTRICAL MACHINES - II	6	B	4.0	
6	ANALOG ELECTRONICS LAB	10	O	1.0		6	DIGITAL ELECTRONICS	6	B	3.0	
7	ANALOG ELECTRONICS	6	B	3.0		7	ELECTRICAL MACHINES LAB - II	10	O	1.0	
8	ELECTROMAGNETIC FIELDS	7	B+	3.0		8	POWER SYSTEM - I	6	B	3.0	
9	GENDER SENSITIZATION LAB ^	-	-	0.0		9	CONSTITUTION OF INDIA ^	-	-	0.0	
I SEMESTER						II SEMESTER					
1	POWER SYSTEM - II	5	C	4.0		1	POWER SYSTEM LAB	10	O	1.0	
2	HIGH VOLTAGE ENGINEERING	5	C	3.0		2	SIGNALS AND SYSTEMS	6	B	3.0	
3	MEASUREMENTS AND INSTRUMENTATION	5	C	4.0		3	MICROPROCESSORS & MICROCONTROLLERS LAB	10	O	1.0	
4	POWER ELECTRONICS	6	B	4.0		4	SIGNALS AND SYSTEMS LAB	10	O	1.0	
5	POWER SYSTEM SIMULATION LAB	9	A+	1.0		5	POWER SEMICONDUCTOR DRIVES	6	B	3.0	
6	MEASUREMENTS AND INSTRUMENTATION LAB	10	O	1.0		6	MICROPROCESSORS & MICROCONTROLLERS	6	B	3.0	
7	ADVANCED COMMUNICATION SKILLS LAB	10	O	1.0		7	POWER SYSTEM OPERATION AND CONTROL	6	B	3.0	
8	POWER ELECTRONICS LAB	10	O	1.0		8	POWER SYSTEM PROTECTION	6	B	4.0	
9	BUSINESS ECONOMICS AND FINANCIAL ANALYSIS	6	B	3.0		9	INDUSTRIAL MANAGEMENT	6	B	3.0	
10	ARTIFICIAL INTELLIGENCE ^	-	-	0.0		10	ENVIRONMENTAL SCIENCE ^	-	-	0.0	
11	INTELLECTUAL PROPERTY RIGHTS ^	-	-	0.0		11	CYBER SECURITY ^	-	-	0.0	
I SEMESTER						II SEMESTER					
1	INDUSTRIAL ORIENTED MINI PROJECT / SUMMER INTERNSHIP	9	A+	2.0		1	POWER QUALITY & FACTS	6	B	3.0	
2	DIGITAL SIGNAL PROCESSING	6	B	3.0		2	ELECTRICAL DISTRIBUTION SYSTEMS	7	B+	3.0	
3	PRINCIPLES OF ENTREPRENEURSHIP	6	B	3.0		3	PROJECT STAGE - II	10	O	7.0	
4	HVDC TRANSMISSION	6	B	3.0		4	ENVIRONMENTAL IMPACT ASSESSMENT	6	B	3.0	
5	ELECTRICAL & ELECTRONICS DESIGN LAB	10	O	3.0							
6	PROJECT STAGE - I	10	O	3.0							
7	FUNDAMENTALS OF MANAGEMENT FOR ENGINEERS	6	B	3.0							
8	SEMINAR	10	O	1.0							

Number of Credits registered and secured are: 123

Aggregate Marks/CGPA Secured : 6.85

Date of Issue : October 29, 2022

(see overleaf for rules concerned to award of class)

Ab indicates 'ABSENT'



(^ Mandatory course passed for award of degree)

(*Courses registered but not counted for calculation of aggregate)

K. Venkateswara Rao
CONTROLLER OF EXAMINATIONS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 1832394



H.T No.: 19U15A0227

College: U1 - Samskruti College of Engineering and Technology,
GHATKESAR

Ms. Pagadala Pranitha

D/o. Pagadala Narasimha Rao

having fulfilled the academic requirements and passed the examination
held during *September - 2022* in *First Class*

has been admitted by the Executive Council to the Degree of

Bachelor of Technology

Electrical & Electronics Engineering

Given under the Seal of the University

Date: 18-03-2023



K. Venkateswara Rao

[Signature]

[Signature]

DIRECTOR OF EVALUATION



VICE-CHANCELLOR

CMM No
21600000984

PC No
31600000986

