Verilog

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-tags:: Note:HardwareWiki *[2021-05-16 Sun 20:15]*

1 Resources

1.1 Blogs to read

1. ☐ https://alchitry.com/pages/verilog-fpga-tutorials

   Only read the Helpful basics stuff

   Yes I repeat, *Only read the Helpful basics stuff*

2. ☐ Numato Blog

   Ignore stuff about XILINX, too complicated for now. We don't need Vivado, we have Verilator

3. ☐ UPC Secrets of Hardware

   Detailed explaination, another big book to read :)

4. ☐ Chip Verify

   Boss level manual. Read this, *then you Verilog pro.*

   Now Verilog should be understandable.

UMD Verilog CheatSheet

Quick go through, because you'll forget this in a day.

1.2 Some courses to take a look at:

[ ] MIT course:

MIT OCW: Intro to Digital Systems (Lab)
Lecture notes should be enough

[ ] NPTEL course:

NPTEL Hardware Modelling
Can't complain that you aren't studying in an ITT now :)

1.3 What's learning without writing code

1. ☐ 1c,2,3 ONLY MIT OCW

   I won't check the code, but try writing them, you get extra points. *:0*

1.4 Ready to the extra mile?

1. ☐ `https://www.youtube.com/watch?v=gUsHwi4M4xE`

Video's about FPGA, but talks about HDLs as well. Good video covering parts of how FPGA and HDLs work.