THEETI RANJITH KUMAR

Electronics and Communication Engineering Student

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GitHub Profile LinkedIn Profile Portfolio Website

Summary

Highly motivated Electronics and Communication Engineering student with strong academic performance (CGPA: 8.04). Passionate about Embedded Systems, IoT, and VLSI concepts with hands-on experience in hardware prototyping and web application development. Seeking internship opportunities to apply technical knowledge in real-world engineering and software development projects.

Education

B.Tech in Electronics and Communication Engineering 2026 – Present

Maharaj Vijayaram Gajapathi Raj College of Engineering, Andhra Pradesh

CGPA: 8.04 / 10

Technical Skills

Programming: C, Python (Basic), SQL (Basics), HTML, CSS, JavaScript

Hardware & HDL: Verilog, VHDL (Xilinx Vivado), Arduino, ESP32

Tools: GNU Octave, Multisim, Xilinx Vivado, Arduino IDE, Tinkercad, LTSpice

Core Areas: Digital Electronics, Analog Circuits, IoT Systems, VLSI Concepts, Data Structures

Projects

ESP32-CAM Vehicle Number Plate Recognition (IOI-BASED PROJECT)

- Developed an embedded vision-based system using ESP32-CAM for automated vehicle number plate detection.
- Integrated camera module with microcontroller for image acquisition and processing.
- Designed system architecture for real-time monitoring and surveillance applications.

Smart Waste Management System (IoT-Based Community Project)

- Designed and implemented an IoT-enabled waste monitoring system to detect garbage fill levels.
- Utilized sensors and microcontroller modules to trigger alerts for timely waste collection.
- Focused on improving urban sanitation efficiency and resource optimization.

Portfolio Generator Web Application

- Built a dynamic web-based portfolio generator for students using modern web technologies.
- Designed responsive UI and structured forms for user data input.
- Improved usability by automating portfolio page creation with clean layout design.

EAPCET Guide Web Application

- Developed an academic guidance platform providing structured exam resources and navigation.
- Implemented responsive interface and organized content structure for enhanced accessibility.

Certifications

- NPTEL – System Design through Verilog (52%)
- NPTEL – Introduction to Industry 4.0 and Industrial Internet of Things (80%)
- NPTEL – Fundamentals of Artificial Intelligence (63%)
- edX – Basic Circuit Analysis
- edX - Introduction to Internet of Things

Achievements

- Participated in Sankalp Hackathon conducted at MVGR College of Engineering.
- Participated in Hackerrank Hackathon conducted at MVGR College of Engineering.