

# Assignment – Caches

due date: June 14<sup>th</sup> 11:59pm

## 1. [40 points]

A small 16-bit address space special-purpose processor may be equipped with one of two tiny direct-mapped caches, C1 or C2, each of which has a total capacity of 64 bytes. C1 has a blocksize of 4 bytes (which corresponds to the size of an integer on this system) while C2 has a blocksize of 16 bytes.

(a) What are the cache parameters (m, C, B, E, S, t, s, b) for C1 and C2?

(b) Consider that the following sequence of addresses are read, (where the size of the data path equals the blocksize): BA00, BA04, AA08, BA05, AA14, AA11, AA13, AA38, AA09, AA0B, BA04, AA2B, BA05, BA06, AA09, AA11.

For each cache option, specify which references are hits and which are misses, and show the final data content of the cache.

## 2. [40 points]

Given a cache with parameters little m = 32, little b = 8, little s = 8 and E = 4.

Assume that:

sizeof(element)	= 8 bytes
@x[256]	= AAAA0000
@y[256]	= AAB0000
@a[512]	= AAAA8000
@b[512]	= AAB8000
value1 and value2 are in registers	

Use the code segment:

```
for i = 0 to 255
  value1 = x[ i ] × y[ i ]
  for j = 0 to 1
    value2 = a[ i×2+j ] + b[ i×2+j ]
```

(a) Fully describe the cache in terms of the cache parameters discussed in class.

(b) What is the hit rate for each of x, y, a and b?

(c) What is the overall hit rate?

(d) What are the cache contents after 1 iteration of the “i” loop?

(e) What are the cache contents after the completion of the “i” loop?

## 3. [20 points]

Which of the following cache hierarchies will provide better AMAT?

Hierarchy A	hit time (cycles)	hit rate	Hierarchy B	hit time (cycles)	hit rate
L1 cache	2	25%	L1 cache	2	50%
L2 cache	10	80%	L2 cache	5	65%
L3 cache	80	95%	L3 cache	20	80%
Main memory	500		Main memory	500	

(a)

m C B E S t s b

 $C_1 = 16 \quad 64 \quad 4 \quad 1 \quad 16 \quad 10 \quad 4 \quad 2$  $C_2 = 16 \quad 64 \quad 16 \quad 1 \quad 4 \quad 10 \quad 2 \quad 4$ 

(b)

\* BA = 1011 1010 00

\* AA = 1010 1010 00

$C_1$	tag	set	ask	get	M/H
1	BA	0	0	[0-3]	M
2	BA	1	0	[0-3]	M
3	AA	2	0	[0-3]	M
4	BA	1	1	[0-3]	H
5	AA	5	0	[0-3]	M
6	AA	4	1	[0-3]	M
7	AA	4	3	[0-3]	H
8	AA	14	0	[0-3]	M
9	AA	2	1	[0-3]	H
10	AA	2	3	[0-3]	H
11	BA	1	0	[0-3]	H
12	AA	10	3	[0-3]	M
13	BA	1	1	[0-3]	H
14	BA	1	2	[0-3]	H
15	AA	2	1	[0-3]	H
16	AA	4	1	[0-3]	H

$C_2$	tag	set	ask	get	M/H
1	BA	0	0	[0-15]	M
2	BA	0	4	[0-15]	H
3	AA	0	8	[0-15]	M
4	BA	0	5	[0-15]	M
5	AA	1	4	[0-15]	M
6	AA	1	1	[0-15]	H
7	AA	1	3	[0-15]	H
8	AA	3	8	[0-15]	M
9	AA	0	9	[0-15]	M
10	AA	0	11	[0-15]	H
11	BA	0	4	[0-15]	M
12	AA	2	11	[0-15]	M
13	BA	0	5	[0-15]	H
14	BA	0	6	[0-15]	H
15	AA	0	9	[0-15]	M
16	AA	1	1	[0-15]	H

•  $C_1$

tag	block
Set 0	BA [0-3]
Set 1	BA [0-3]
Set 2	AA [0-3]
Set 4	AA [0-3]
Set 5	AA [0-3]
Set 10	AA [0-3]
Set 14	AA [0-3]

•  $C_2$

tag	block
set 0	AA [0-15]
set 1	AA [0-15]
set 2	AA [0-15]
set 3	AA [0-15]

2. (a) m C B E S t s b  
 32 262144 256 4 256 16 8 8

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(b) x, y, a, b Hit rate :  $\frac{31}{32} = 96.875\%$

(c) overall Hit rate :  $1 - \frac{6}{192} = \frac{186}{192} = 96.875\%$

(d)

set 0 { tag block  
 AAAA x[0~31]  
 AABB y[0~31]

set 128 { tag block  
 AAAA a[0~31]  
 AABB b[0~31]

(e)

set 0 { tag block  
 AAAA x[0~31]  
 AABB y[0~31]  
 set 1 { tag block  
 AAAA x[32~63]  
 AABB y[32~63]  
 set 2 { tag block  
 AAAA x[64~95]  
 AABB y[64~95]  
 set 3 { tag block  
 AAAA x[96~127]  
 AABB y[96~127]  
 set 4 { tag block  
 AAAA x[128~159]  
 AABB y[128~159]  
 set 5 { tag block  
 AAAA x[160~191]  
 AABB y[160~191]  
 set 6 { tag block  
 AAAA x[192~223]  
 AABB y[192~223]  
 set 7 { tag block  
 AAAA x[224~255]  
 AABB y[224~255]

set 128 { tag block  
 AAAA a[0~31]  
 AABB b[0~31]  
 set 129 { tag block  
 AAAA a[32~63]  
 AABB b[32~63]  
 set 130 { tag block  
 AAAA a[64~95]  
 AABB b[64~95]  
 set 131 { tag block  
 AAAA a[96~127]  
 AABB b[96~127]  
 set 132 { tag block  
 AAAA a[128~159]  
 AABB b[128~159]  
 set 133 { tag block  
 AAAA a[160~191]  
 AABB b[160~191]  
 set 134 { tag block  
 AAAA a[192~223]  
 AABB b[192~223]  
 set 135 { tag block  
 AAAA a[224~255]  
 AABB b[224~255]  
 set 136 { tag block  
 AAAA a[256~287]  
 AABB b[256~287]  
 set 137 { tag block  
 AAAA a[288~319]  
 AABB b[288~319]  
 set 138 { tag block  
 AAAA a[320~351]  
 AABB b[320~351]  
 set 139 { tag block  
 AAAA a[352~383]  
 AABB b[352~383]  
 set 140 { tag block  
 AAAA a[384~415]  
 AABB b[384~415]  
 set 141 { tag block  
 AAAA a[416~447]  
 AABB b[416~447]  
 set 142 { tag block  
 AAAA a[448~479]  
 AABB b[448~479]  
 set 143 { tag block  
 AAAA a[480~511]  
 AABB b[480~511]

- Hierarchy A

$$\begin{aligned}\text{AMAT} &= 2 + (1 - 0.25) \times (10 + (1 - 0.8) \times (80 + (1 - 0.95) \times (500))) \\ &= 25.25\end{aligned}$$

- Hierarchy B

$$\begin{aligned}\text{AMAT} &= 2 + (1 - 0.5) \times (5 + (1 - 0.65) \times (20 + (1 - 0.8) \times (500))) \\ &= 25.5\end{aligned}$$

So, Hierarchy A provide better AMAT than Hierarchy B.