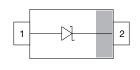


# Vishay Semiconductors

# **Small Signal Schottky Diode**





### **LINKS TO ADDITIONAL RESOURCES**











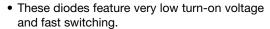
# **MECHANICAL DATA**

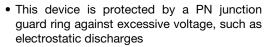
Case: SOD-123

Weight: approx. 10.6 mg Packaging codes/options:

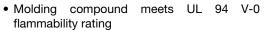
18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

### **FEATURES**











AUTOMOTIVE GRADE



- Moisture Sensitivity Level (MSL) 1
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

PARTS TABLE							
PART	ORDERING CODE	AEC-Q101 QUALIFIED	TYPE MARKING	CIRCUIT CONFIGURATION	TAPED UNITS PER REEL	MINIMUM ORDER QUANTITY	
BAT54W	BAT54W-E3-08	no	L8		3 000	15 000	
	BAT54W-HE3_A-08	yes		Single	(8 mm tape on 7" reel)	13 000	
	BAT54W-E3-18	no	Lo	Sirigle	10 000	10 000	
	BAT54W-HE3_A-18	yes			(8 mm tape on 13" reel)	10 000	

PACKAGE						
PACKAGE NAME	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
SOD-123	10.6 mg	UL 94 V-0	MSL 1 (according J-STD-020)	Peak temperature max. 260 °C		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Repetitive peak reverse voltage		$V_{RRM}$	30	V	
Forward continuous current (1)		I <sub>F</sub>	200	mA	
Repetitive peak forward current (1)	duty cycle t <sub>p</sub> / T < 0.5	I <sub>FRM</sub>	300	mA	
Surge forward current (1)	$t_p = 10 \text{ ms}$	I <sub>FSM</sub>	600	mA	
Power dissipation	on FR-4 board with recommended soldering footprint	В	230	mW	
	Infinite heatsink	P <sub>tot</sub>	350	mW	

### Note

(1) Infinite heatsink





# Vishay Semiconductors

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Thermal resistance junction to ambient air	according to JEDEC® 51-3 on FR-4 board with recommended soldering footprint	R <sub>thJA</sub>	420	K/W		
Thermal resistance junction lead	Infinite heatsink	R <sub>thJL</sub>	280	K/W		
Junction temperature		Tj	125	°C		
Storage temperature range		T <sub>stg</sub>	-65 to +150	°C		
Operating temperature range		T <sub>op</sub>	-55 to +125	°C		

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reserve breakdown voltage	Tested with 100 μA pulses	V <sub>(BR)</sub>	30			V
Leakage current (1)	V <sub>R</sub> = 25 V	I <sub>R</sub>			2	μA
	I <sub>F</sub> = 0.1 mA	V <sub>F</sub>			240	mV
	I <sub>F</sub> = 1 mA	V <sub>F</sub>			320	mV
Forward voltage (1)	I <sub>F</sub> = 10 mA	V <sub>F</sub>			400	mV
	I <sub>F</sub> = 30 mA	V <sub>F</sub>			500	mV
	I <sub>F</sub> = 100 mA	V <sub>F</sub>			800	mV
Diode capacitance	V <sub>R</sub> = 1 V, f = 1 MHz	C <sub>D</sub>			10	pF
Reserve recovery time	$I_F = 10 \text{ mA}, I_R = 10 \text{ mA}, I_R = 1 \text{ mA}, R_L = 100 \Omega$	t <sub>rr</sub>			5	ns

### Note

 $<sup>^{(1)}~</sup>$  Pulse test:  $t_p < 300~\mu s,~duty~cycle~t_p/T < 0.02$ 

# TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

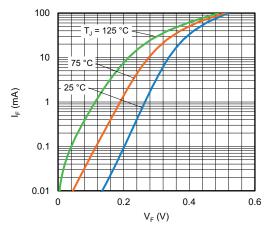


Fig. 1 - Typical Forward Current vs. Forward Voltage

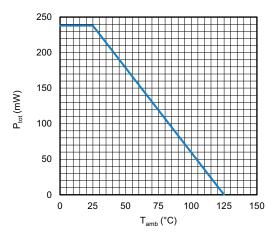


Fig. 2 - Admissible Power Dissipation vs. Ambient Temperature

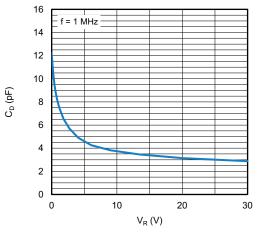


Fig. 3 - Typical Reverse Characteristics

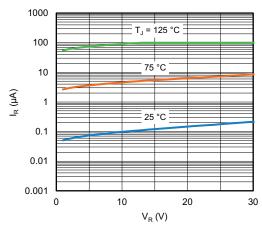
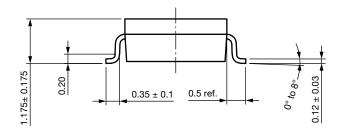


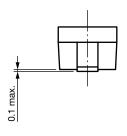
Fig. 4 - Typical Capacitance vs. Reverse Voltage

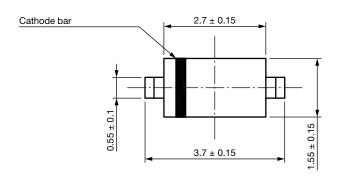


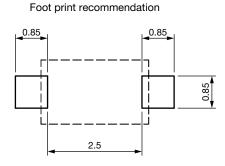
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# PACKAGE DIMENSIONS in millimeters (inches): SOD-123









Rev. 01 - Date: 18. Jan. 2022 Document no.: S8-V-3910.01-003 (4)

23223

23225



# Vishay Semiconductors

# **CARRIER TAPE SOD-123**

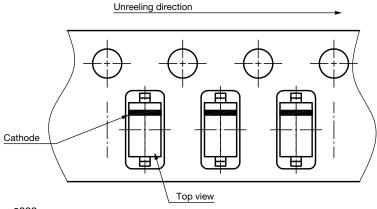
# A - A section 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013 0.203 ± 0.013

Rev. 02 - Date: 21. Jan. 2014

Document no.: S8-V-3717.10-002 (4)

# **OIRIENTATION IN CARRIER TAPE SOD-123**

 $1.85 \pm 0.1$ 



Rev. 02 - Date: 07. Nov. 2022 Document no.: S8-V-3717.10-003 (4)

Rev. 1.0, 20-Nov-2023 5 Document Number: 86408



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