

RAPHAEL SILVA DO NASCIMENTO

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Highly accomplished Senior Software Engineer with 10+ years of experience in full-stack development, system verification, and automation. Proven expertise in Node.js, Python, Java, and SystemVerilog (with embedded C), with a strong background in microservices, ETL, Android ecosystem (Java/Kotlin) and Linux. Seeking to leverage technical leadership and problem-solving skills to drive innovation and contribute to Company's success.

Professional Experience

Senior Developer / Software Engineer | Boticário

Jun. 2021 – Dec. 2025

- Design, develop, test, deploy, maintain, and improve software applications.
- Research, conceive, and develop software applications to extend and enhance Grupo Boticario's product offering.
- Collaborate on scalability issues involving data access and information flow.
- Developed and implemented ETL processes, scraping bots, and Node.js microservices, improving legacy system data retrieval efficiency and ensuring real-time updates for the help center.
- Design and create AWS Lambda functions.
- Write TypeScript/JavaScript software adhering to SOLID principles.
- Develop Python programs using Selenium for data scraping.

Mid Engineer / Software Engineer | Flex

Jan. 2020 – Jun. 2021

- Led development and design for the Automation team, contributing to the demand for software modeling web architecture and coding in Python, JavaScript, C#, C/C++, and Java.
- Managed project activities by integrating with the Planner tool, optimizing resource allocation using Kamba Methodology.
- Developed Business Intelligence (BI) solutions, including Dashboards, utilizing ETL, Data Warehouse, Data Mart, OLAP, and Data Mining techniques.
- Developed and implemented Python and C# microservices for Single Page Applications (SPA) to automate online systems.
- Integrated machines using diverse industrial communication protocols: ModBus, HTTP/HTTPS, OPC-DA, OPC-UA.
- Contributed to research and software development in Machine Learning.

- Contributed to embedded projects for industrial automation in the IoT line, supporting Linux configuration and embedded software development.

Project Specialist / SoC Verification Engineer | NXP SEMICONDUCTORS / FREESCALE

Aug. 2014 – Mar. 2019

- Verified ARM-based microcontrollers integration, ensuring IP (Intellectual Property) performance through testbench and test case creation across RTL, GLS, and Post Layout phases.
- Developed verification plans based on coverage analysis, describing test patterns and utilizing Planner for regression reporting.
- Implemented SystemVerilog verification environments (assertions, drivers, monitors) using UVM (Universal Verification Methodology).
- Identified technical implementation needs for the MMC5.0/SDIO module to achieve robust project coverage and support various DENALI specification configurations.
- Utilized Linux environments and wrote Bash scripts to optimize tasks.
- Verified uSDHC (SDCard), IpUART, EWM, LPIT, and LPTIMER modules.

Scholarship from CNPq - Centro de Formação em Projeto de Circuitos Integrados | PROGRAMA CI - Brasil - Porto Alegre/RS

Aug. 2013 – Aug. 2014

- Verified IP integration between a D.S.P. (digital signal processing) module and the CHARRUA processor, adopting UVM methodologies using SystemVerilog.
- Coded all verification environments for the C.P.S. (Carry Phase Synchronizer) module.
- Created a verification plan using the ePlanner tool.

C# Programmer | PSG Tecnologia Aplicada - Campo Grande/MS

Mar. 2013 – July. 2013

- Migrated the public billing system of Mato Grosso do Sul from Cobol to C#, SQL Server, and Visual Studio.
- Developed the core receiving program, bank text file interpreter, and report generator interfaces.

Java Programmer | TDR informática - Campo Grande/MS

Aug. 2010 – Nov. 2011

- Contributed to the implementation of the SCRUM project management methodology.
- Re-engineered hospital management software from Delphi 10 (3-layer methodology) to Java OO (MVC) for use in both public and private sectors.
- Provided Java support to a team of Delphi 10 developers.

- Developed a security module with biometric reader integration using Java Applets.

Academic Education

- **Business Intelligence (BI) MBA** - Estacio de Sa - 2021
- **ASIC Specialization** - Programa Ci-Brasil CT1 (UFRGS) - 2014
- **Computer Engineering with emphasis on Hardware** - Universidade para o Desenvolvimento do Estado e região do Pantanal UNIDERP - 2013

Languages

- **English:** Advanced
 - Experienced in communicating with people from other countries for business and personal friendships.

Skills

- **Object-Oriented Programming**
- **Design Patterns Implementing**
- **Languages:**
 - **Software Development:** Python, Typescript/JavaScript, Java/Kotlin, C#, Flutter
 - **Hardware Design & Embedded Systems:** C, C++, Embedded Development
 - **ASIC/FPGA Verification:** SystemVerilog, Verilog, Hardware Description Languages (HDL).
- **Architectures and Frameworks:** MVC (Model, View, Controller), MVVM, UVM (Universal Verification Methodology), Android, Vue.js, Node.js, React.js, TypeScript, Java Servlet, JSF
- **Database (SQL/NoSQL, XQuery):** PostgreSQL, MySQL, MongoDB, SQL Server, DynamoDB
- **Agile Methodology:** SCRUM, Kanban, Kaizen
- **Linux:** Git, Tcsh, Bash, Tmux
- **Tools:** Git, Visual Studio Code/Android Studio, CADENCE (SimVision, Irun, Eplanner), Synopsys (Verdi, Certitude), Yocto, Docker

Personal Interests

Passionate about leveraging technology for environmental improvement and community well-being. Enjoys playing guitar and practicing Budo Tai Jutsu for personal development.