

# **Study and Experimental Verification of Superposition Theorem in DC Circuits**

Course Title: Basic Electrical and Electronic Circuit  
Course Code: EEE 07131122



**Submitted by:**

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## Objectives

1. Item 1
2. Item 2
3. Item 3
4. Item 4
5. Item 5

## Introduction

Lab experiment Introduction will go here [?, ?].....

## Working Principle of the Experiment

working principle of the experiment will go here.....



Figure 1: Title of the image

## Equipment

1. Item 1
2. Item 2
3. Item 3
4. Item 4
5. Item 5

## Experimental Data

Text on experimental data and table go here.....

## Experimental Graph



Figure 2: Title of the image



Figure 3: Title of the image

## Error Calculation

Write the error analysis in this section.....

## Discussion and Conclusion

Text on discussion, your own observation and conclusion.....

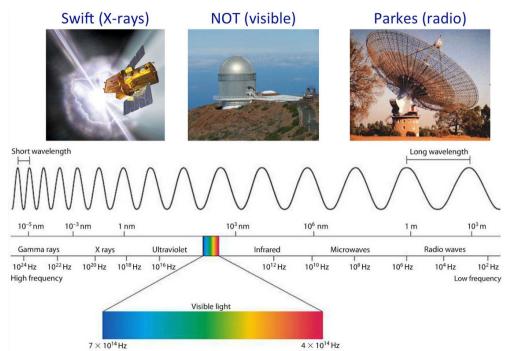


Figure 4: Ideal Graph

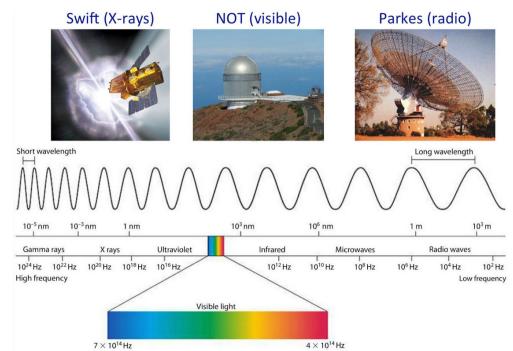


Figure 5: Actual Graph