

Study and Experimental Verification of Superposition Theorem in DC Circuits

Course Title: Basic Electrical and Electronic Circuit
Course Code: EEE 07131206B



Submitted by:

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Session: 2021-2022

Submitted to:

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Report Submission Date: **December 10, 2025**

Deceleration

I, the undersigned, **Rudro Sen**, hereby declare that the work presented in this report is my own, carried out under the scientific direction of **Name of course Teacher**, in accordance with the principles of honesty, integrity, and responsibility. This report was written entirely by myself and complies with the charter on the fight against scientific plagiarism.

Sylhet, December 10, 2025

Signature

Signature



Objectives

1. Item 1
2. Item 2
3. Item 3
4. Item 4
5. Item 5

Introduction

Lab experiment Introduction will go here [?, ?].....

Working Principle of the Experiment

working principle of the experiment will go here.....



Figure 1: Title of the image

Equipment

1. Item 1
2. Item 2
3. Item 3
4. Item 4
5. Item 5

Experimental Data

Text on experimental data and table go here.....

Experimental Graph



Figure 2: Title of the image



Figure 3: Title of the image

Error Calculation

Write the error analysis in this section.....

Discussion and Conclusion

Text on discussion, your own observation and conclusion.....

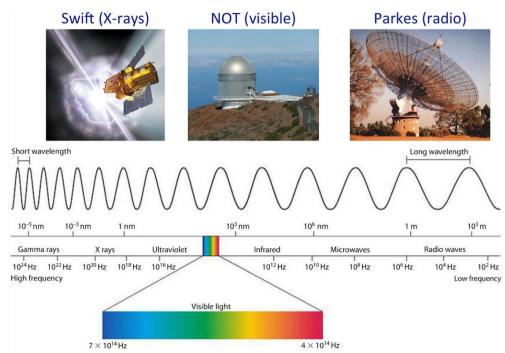


Figure 4: Ideal Graph

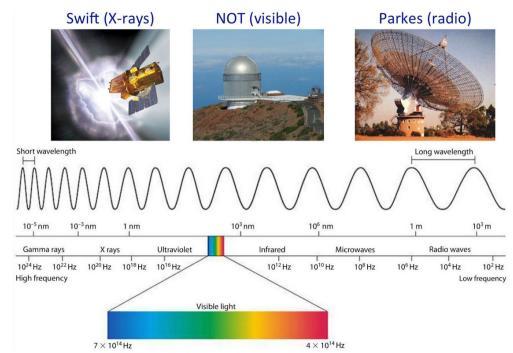


Figure 5: Actual Graph