C++ Operators Precedence Table

The following table (taken from cppreference.com) shows the precedence of C++ operators. Precedence Level 1 signifies operators of highest priority, while Precedence Level 17 signifies operators of the lowest priority.

Precedence	Operator	Description	Associativity
1	::	Scope Resolution	Left to Right
	a++	Suffix/postfix increment	
	a	Suffix/postfix decrement	
	type()	Function cast	
2	type{ }	Function cast	Left to Right
	a()	Function call	
	a[]	Subscript Member access from an object	
	->	Member access from object ptr	
	++a	Prefix increment	
	a	Prefix decrement	
	+a	Unary plus	
	-a	Unary minus	
		Logical NOT Bitwise NOT	
3	" (type)	C style cast	Right to Left
	(type) *a	Indirection (dereference)	ragili to Leit
	&a	Address-of	
	sizeof	Size-of	
	co_await	await-expression	
	new new[]	Dynamic memory allocation	
	delete delete[]	Dynamic memory deallocation	
4	.* ->*	Member object selector Member pointer selector	Left to Right
5	a*b	Multiplication Division	Loft to Direkt
	a/b a%b	Division Modulus	Left to Right
	Q 70 D	Floadias	
6	a + b	Addition	Left to Right
WY	a - b	Subtraction	Lore to Right
7	<<	Bitwise left shift	Loft to Dight
/	>>	Bitwise right shift	Left to Right
8	<=<	Three-way comparison operator	Left to Right
	<	Less than	
	<=	Less than or equal to	
9	>	Greater than	Left to Right
	>=	Greater than or equal to	
		Facility of the second	
10	== !-	Equal to	Left to Right
	!=	Not equal to	
11	&	Bitwise AND	Left to Right
12		Bitwise XOR	Left to Right
13		Bitwise OR	Left to Right
14	&&	Logical AND	Left to Right
15	II	Logical OR	Left to Right
	a?b:c	Ternary Conditional	
	throw	throw operator	
	co_yield	yield expression (C++ 20)	
	=	Assignment	
	+=	Addition Assignment	
	-=	Subtraction Assignment	
		Multiplication Assignment	Right to Left
16	*=	LIVICION Accidence and	
16	/=	Division Assignment	
16	/= %=	Modulus Assignment	
16	/=		
16	/= %= <<=	Modulus Assignment Bitwise Shift Left Assignment	
16	/= %= <<= >>=	Modulus Assignment Bitwise Shift Left Assignment Bitwise Shift Right Assignment	
16	/= %= <<= >>= &=	Modulus Assignment Bitwise Shift Left Assignment Bitwise Shift Right Assignment Bitwise AND Assignment	