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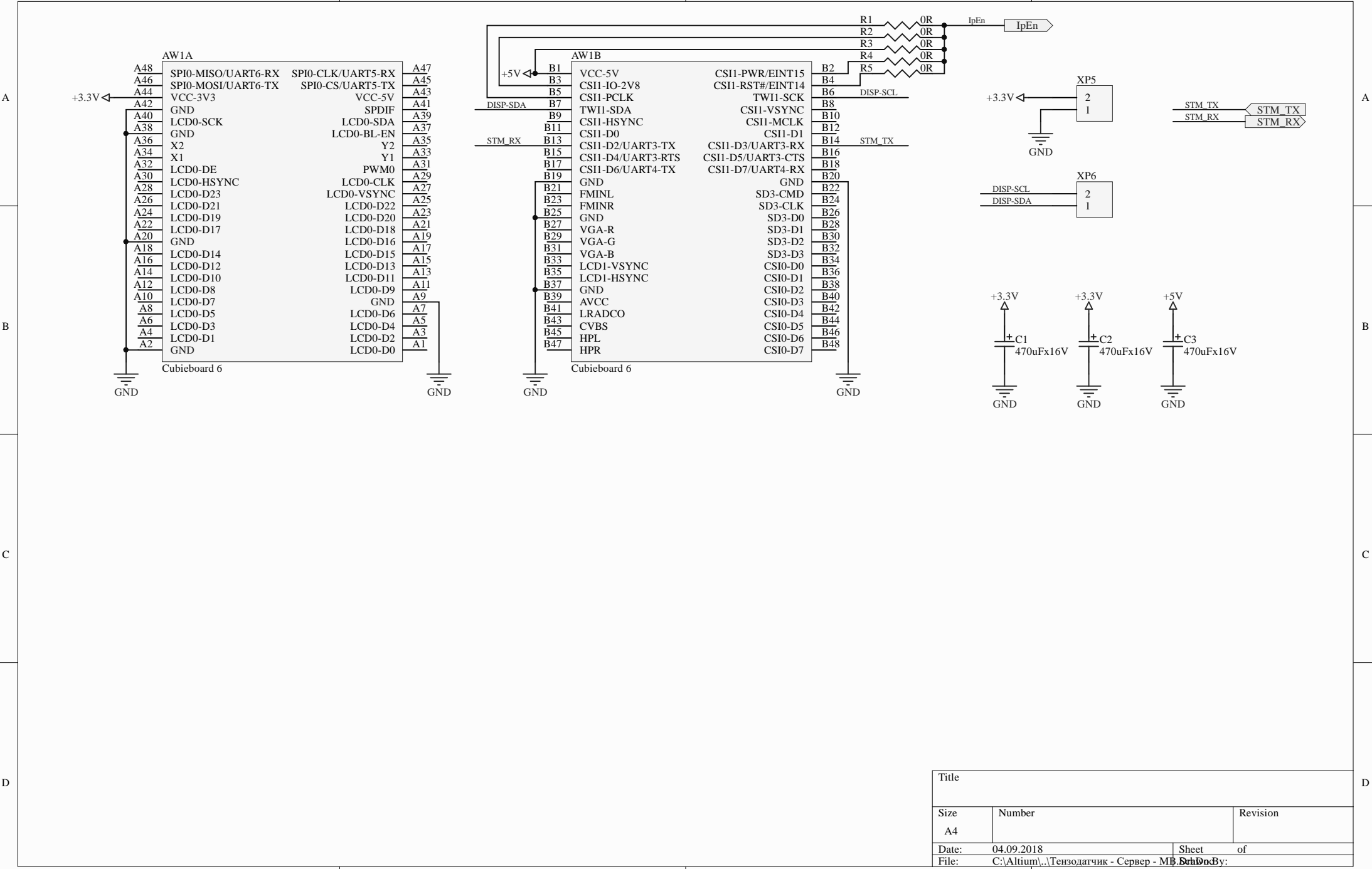
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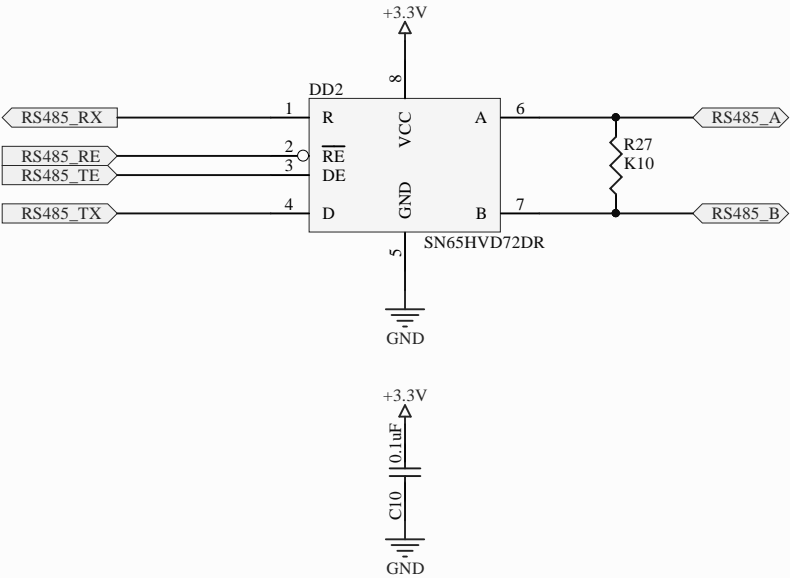
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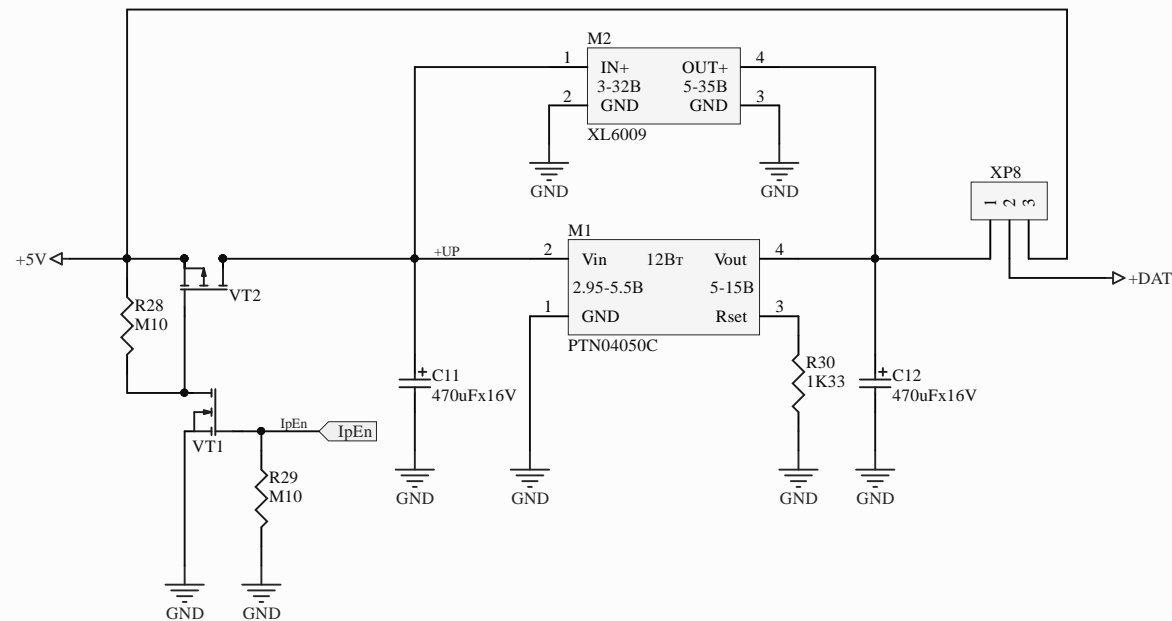
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Title			
Size	Number		Revision
A4			
Date:	04.09.2018	Sheet	of
File:	C:\Altium\...\Тензодатчик - Сервер - МБ. SchDocBy:		



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Size	Number		Revision
A4			
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File:	C:\Altium\...\Тензодатчик - Сервер - RS485.SchDoc		



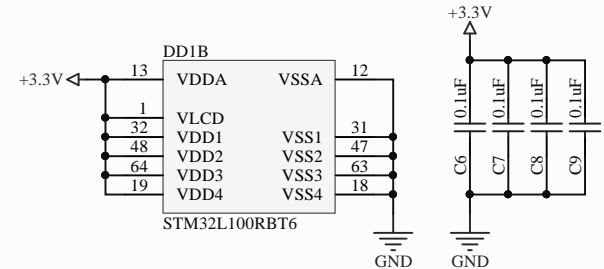
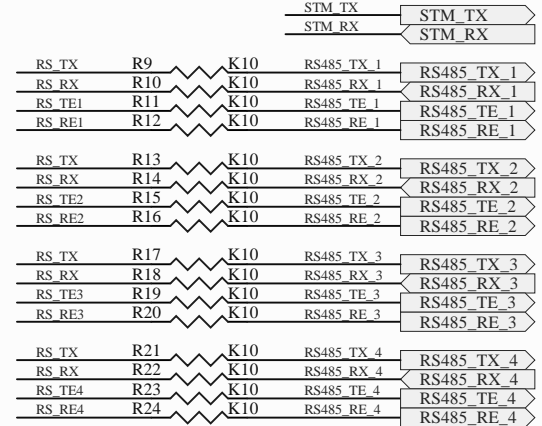
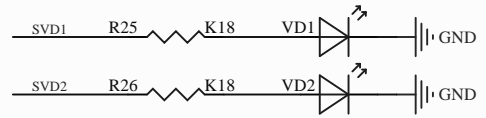
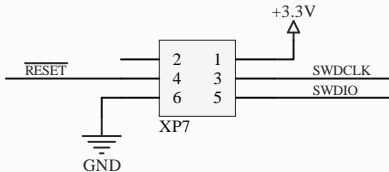
Title			
Size	Number		Revision
A4			
Date:	04.09.2018	Sheet	of
File:	C:\Altium\..\Тензодатчик - Сервер - ИП... Сделано By:		

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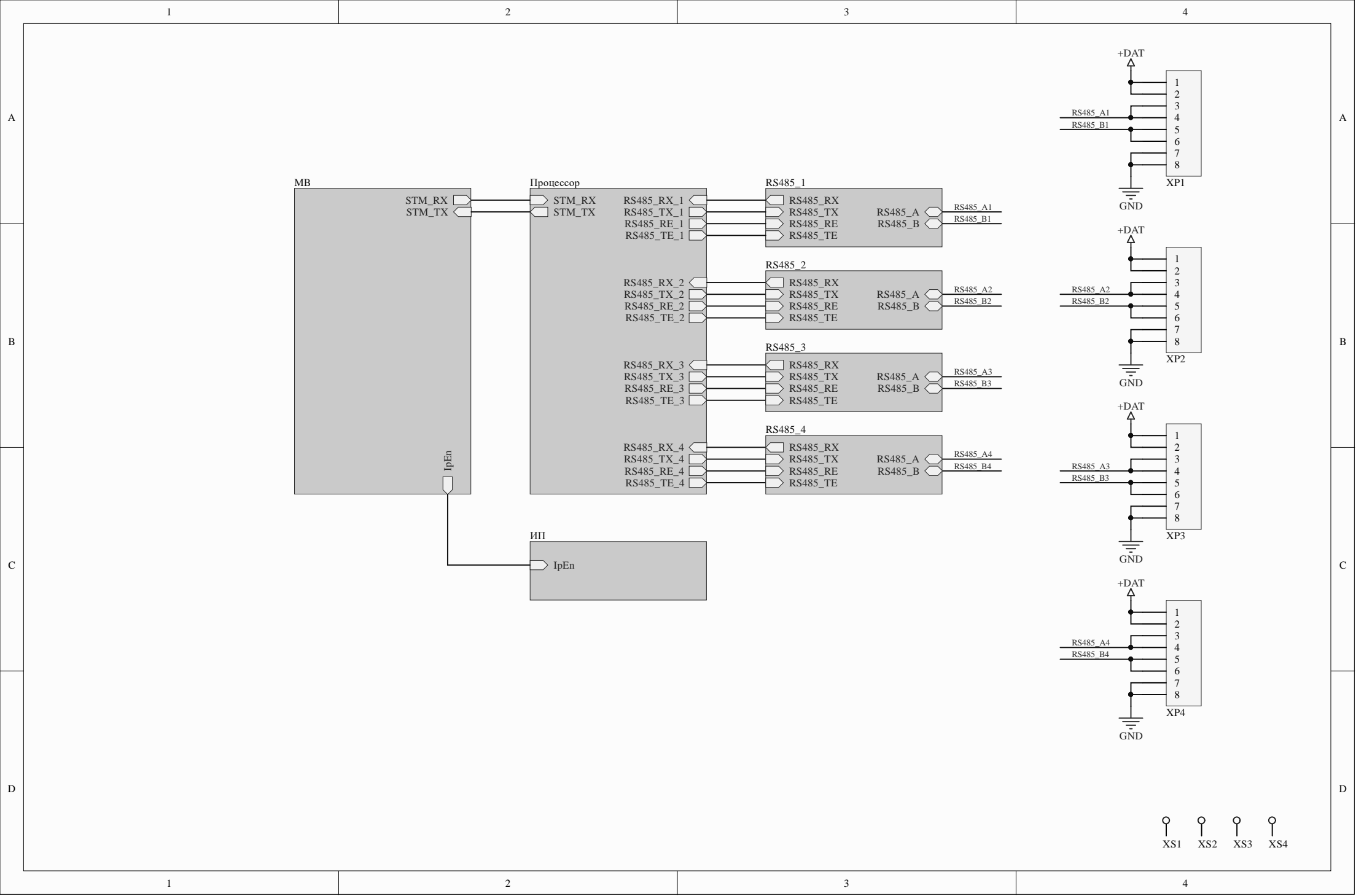
PA0/USART2_CTS/TIM2_CH1_ETR/WKUP1/ADC_IN0/COMP1_INP 14
PA1/USART2_RTS/TIM2_CH2/LCD_SEG0/ADC_IN1/COMP1_INP 15
PA2/USART2_TX/TIM2_CH3/TIM9_CH1/LCD_SEG1/ADC_IN2/COMP1_INP 16 RS_TX
PA3/USART2_RX/TIM2_CH4/TIM9_CH2/LCD_SEG2/ADC_IN3/COMP1_INP 17 RS_RX
PA4/SPI1_NSS/USART2_CK/ADC_IN4/DAC_OUT1/COMP1_INP 20 RS_TE4
PA5/SPI1_SCK/TIM2_CH1_ETR/ADC_IN5/DAC_OUT2/COMP1_INP 21 RS_RE4
PA6/SPI1_MISO/TIM3_CH1/LCD_SEG3/TIM10_CH1/ADC_IN6/COMP1_INP 22 RS_TE3
PA7/SPI1_MOSI/TIM3_CH2/LCD_SEG4/TIM11_CH1/ADC_IN7/COMP1_INP 23 RS_RE3
PA8/USART1_CK/MCO/LCD_COM0 41
PA9/USART1_TX/LCD_COM1 42 STM_TX
PA10/USART1_RX/LCD_COM2 43 STM_RX
PA11/USART1_CTS/SPI1_MISO/USB_DM 44
PA12/USART1_RTS/SPI1_MOSI/USB_DP 45
PA13/JTMS-SWDIO 46 SWDIO
PA14/JTCK-SWCLK 49 SWDCLK
PA15/TIM2_CH1_ETR/PA15/SPI1_NSS/LCD_SEG17/JTDI 50
PB0/TIM3_CH3/LCD_SEG5/ADC_IN8/COMP1_INP/VREF_OUT 26 RS_TE1
PB1/TIM3_CH4/LCD_SEG6/ADC_IN9/COMP1_INP/VREF_OUT 27 RS_RE1
PB3/TIM2_CH2/SPI1_SCK/SPI3_SCK/LCD_SEG7/JTDO/COMP2_INM 55
PB4/TIM3_CH1/SPI1_MISO/SPI3_MISO/LCD_SEG8/NTRST/COMP2_INP 56
PB5/I2C1_SMBA/TIM3_CH2/SPI1_MOSI/LCD_SEG9/COMP2_INP 57
PB6/TIM4_CH1/I2C1_SCL/USART1_TX 58
PB7/TIM4_CH2/I2C1_SDA/USART1_RX/COMP2_INP/PVD_IN 59
PB8/TIM4_CH3/I2C1_SCL/LCD_SEG16/TIM10_CH1 61
PB9/TIM4_CH4/I2C1_SDA/LCD_COM3/TIM11_CH1 62
PB10/I2C2_SCL/USART3_TX/TIM2_CH3/LCD_SEG10 29
PB11/I2C2_SDA/USART3_RX/TIM2_CH4/LCD_SEG11 30
PB12/SPI2_NSS/I2C2_SMBB/USART3_CK/LCD_SEG12/TIM10_CH1/ADC_IN18/COMP1_INP 33 SVD2
PB13/SPI2_SCK/USART3_CTS/LCD_SEG13/TIM9_CH1/ADC_IN19/COMP1_INP 34 SVD1
PB14/SPI2_MISO/USART3_RTS/LCD_SEG14/TIM9_CH2/ADC_IN20/COMP1_INP 35
PB15/SPI2_MOSI/LCD_SEG15/TIM11_CH1/ADC_IN21/COMP1_INP/RTC_REFIN 36
PC0/LCD_SEG18/ADC_IN10/COMP1_INP 8
PC1/LCD_SEG19/ADC_IN11/COMP1_INP 9
PC2/LCD_SEG20/ADC_IN12/COMP1_INP 10
PC3/LCD_SEG21/ADC_IN13/COMP1_INP 11
PC4/LCD_SEG22/ADC_IN14/COMP1_INP 24 RS_TE2
PC5/LCD_SEG23/ADC_IN15/COMP1_INP 25 RS_RE2
PC6/TIM3_CH1/LCD_SEG24 37
PC7/TIM3_CH2/LCD_SEG25 38
PC8/TIM3_CH3/LCD_SEG26 39
PC9/TIM3_CH4/LCD_SEG27 40
PC10/USART3_TX/LCD_SEG28/LCD_SEG40/LCD_COM4 51
PC11/USART3_RX/LCD_SEG29/LCD_SEG41/LCD_COM5 52
PC12/USART3_CK/LCD_SEG30/LCD_SEG42/LCD_COM6 53
PC13/WKUP2/RTC_TAMP1/RTC_TS/RTC_OUT 2
PC14/OSC32_IN 3
PC15/OSC32_OUT 4
PD2/TIM3_ETR/LCD_SEG30/LCD_SEG43/LCD_COM7 54
PH0/OSC_IN 5
PH1/OSC_OUT 6
RST 7 RESET
BOOT0 60
PB2/BOOT1 28

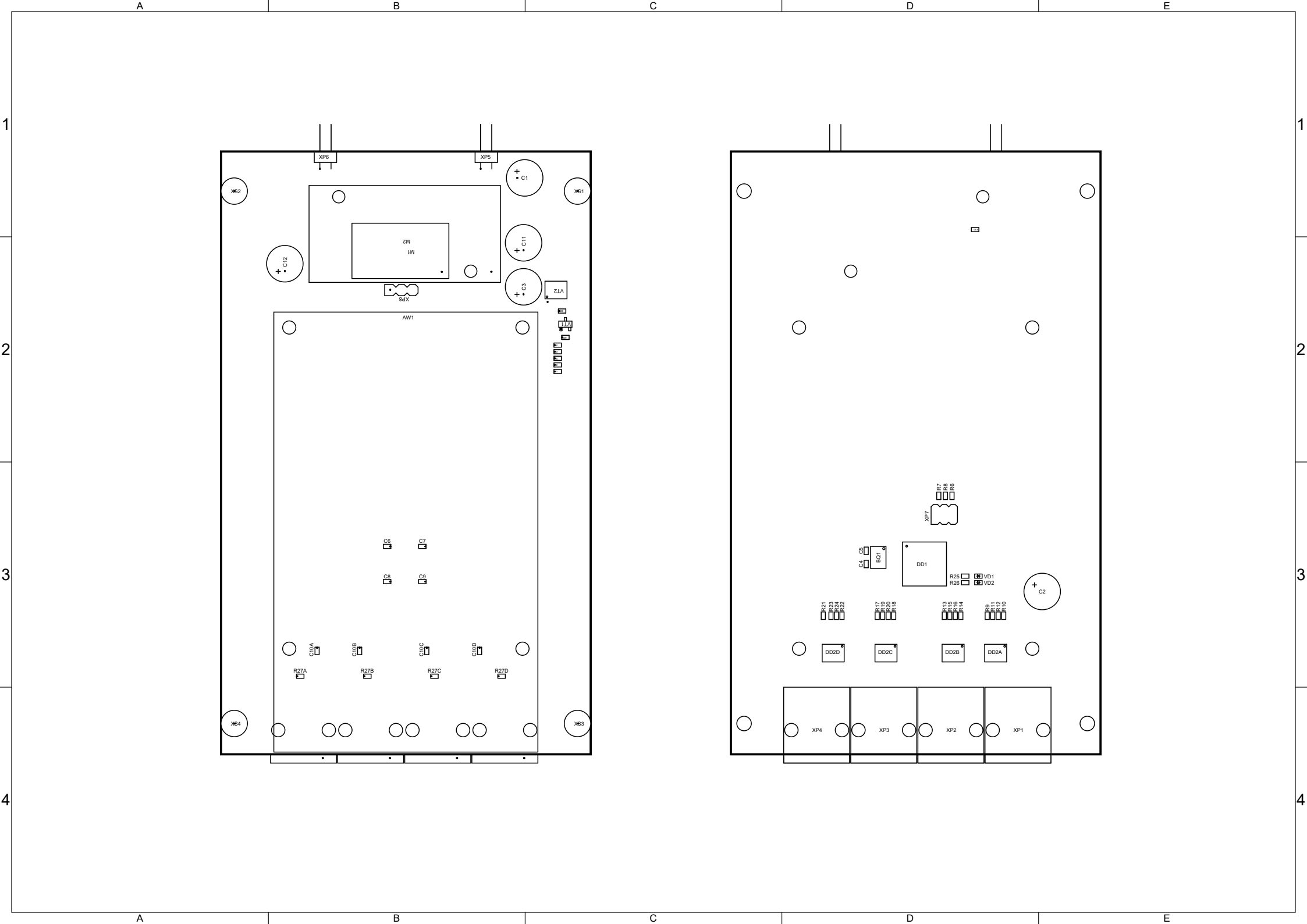
STM32L100RBT6

SWDIO R6 1K
SWDCLK R7 1K
RESET R8 1K



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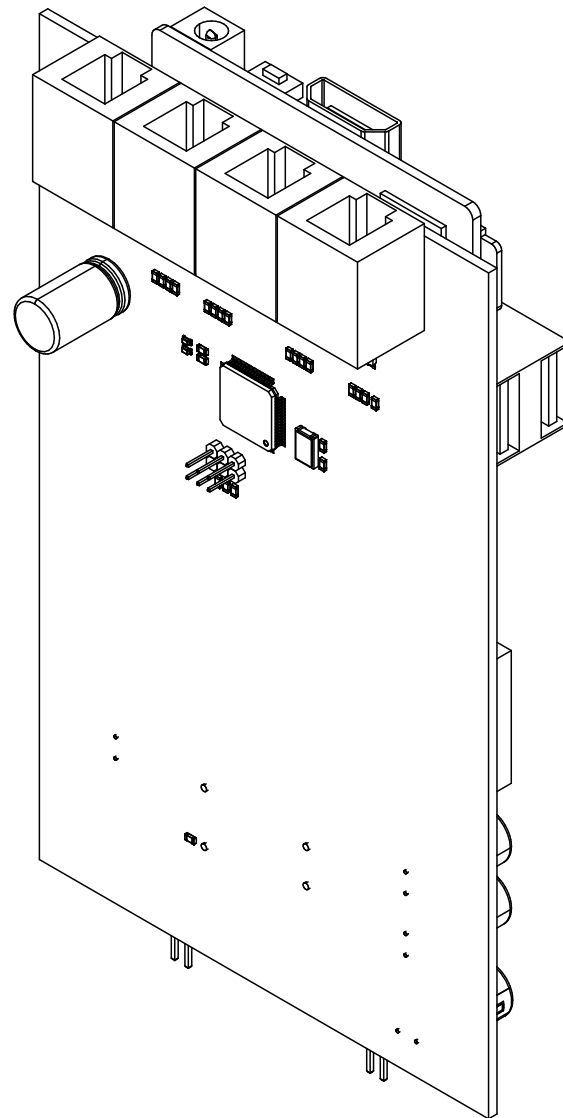
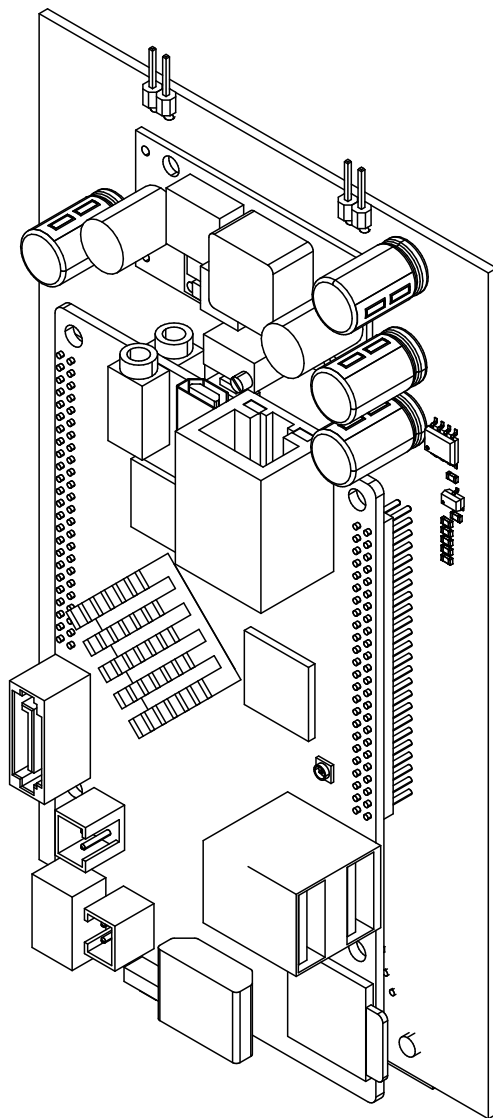
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