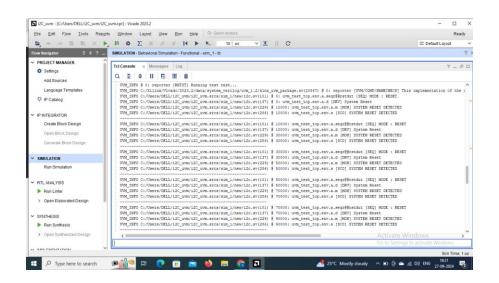
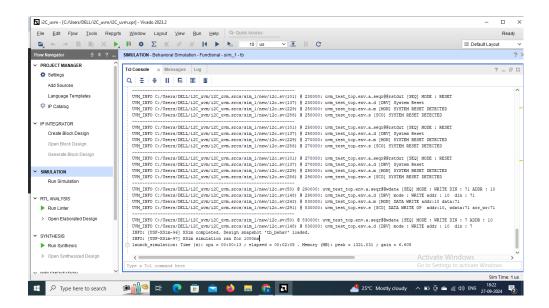
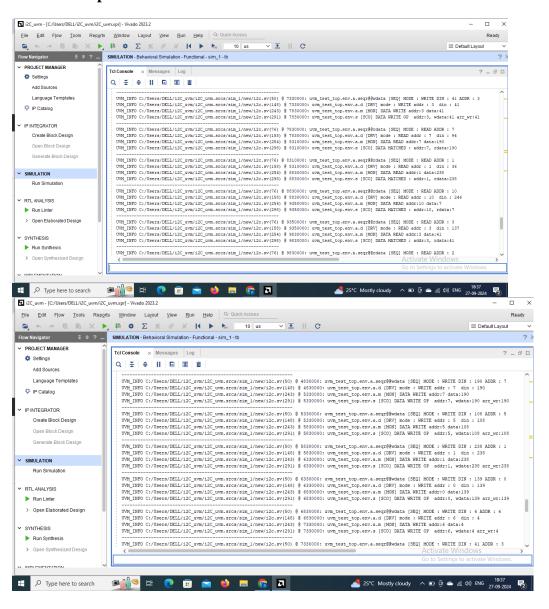
Simulation Results Of UVM Test Bench Environment For Functional Verification Of I2C Protocol in Xilinx Vivado

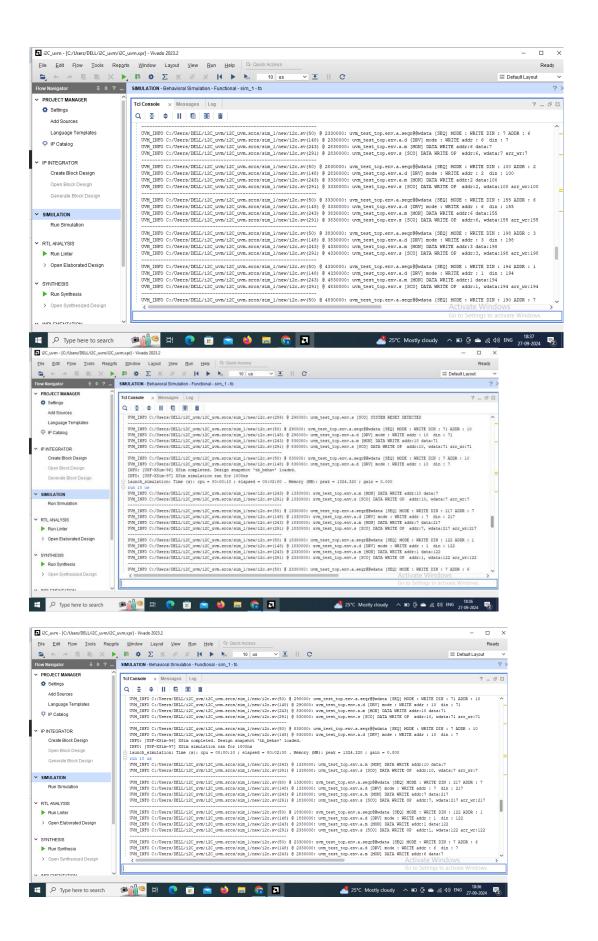
For RESET DUT Operation:





For Write Operation:





For Reading Data from Slave Memory:

