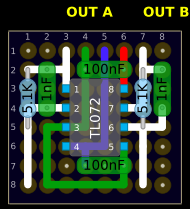
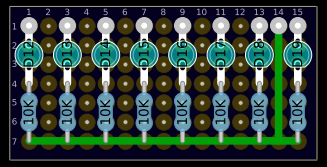
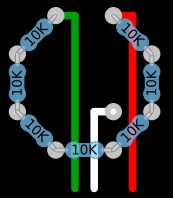
DUAL INVERTING OUTPUT BUFFER
(from Turing Machine)
R_fb = R_in * (desired gain)



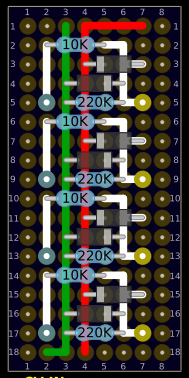
SOME LEDs



8 POSN ROTARY to ANALOG READ

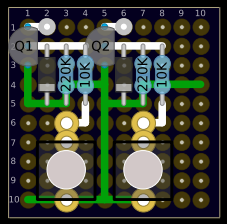


CV OUT:
CLIPPED TO 0-5V
(NO SCALING)



CV IN:
+/- 10V

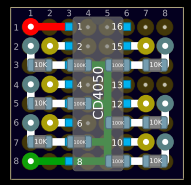
COUPLE OF SINK ON INPUT GATE/TRIGGER INS (FOR MCU W/ PULLUP INPUTS)



VDD
(of *OUTPUT SIDE* CIRCUIT)

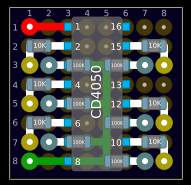
HEX GATE BUFFER
***series resistors on inputs**
if going high to low; outputs
if low to high)

HIGH TO LOW:



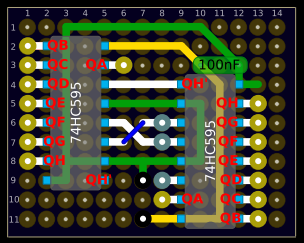
IN 1-3
OUT 1-3
OUT 4-6
IN 4-6

LOW TO HIGH:



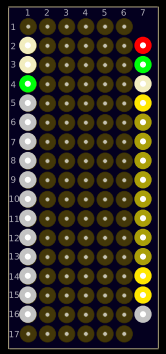
OUT 1-3
IN 1-3
IN 4-6
OUT 4-6

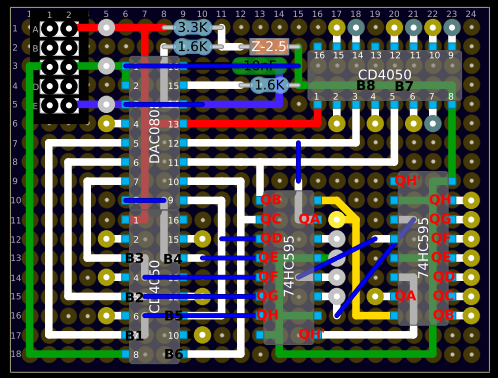
16 BIT SERIAL to PARALLEL SHIFT REGISTER



PASSIVE SR
(LOADS FIRST BYTE)

ACTIVE SR
(LOADS SECOND BYTE)





"ANALOG" DIGITAL TO ANALOG CONVERTER