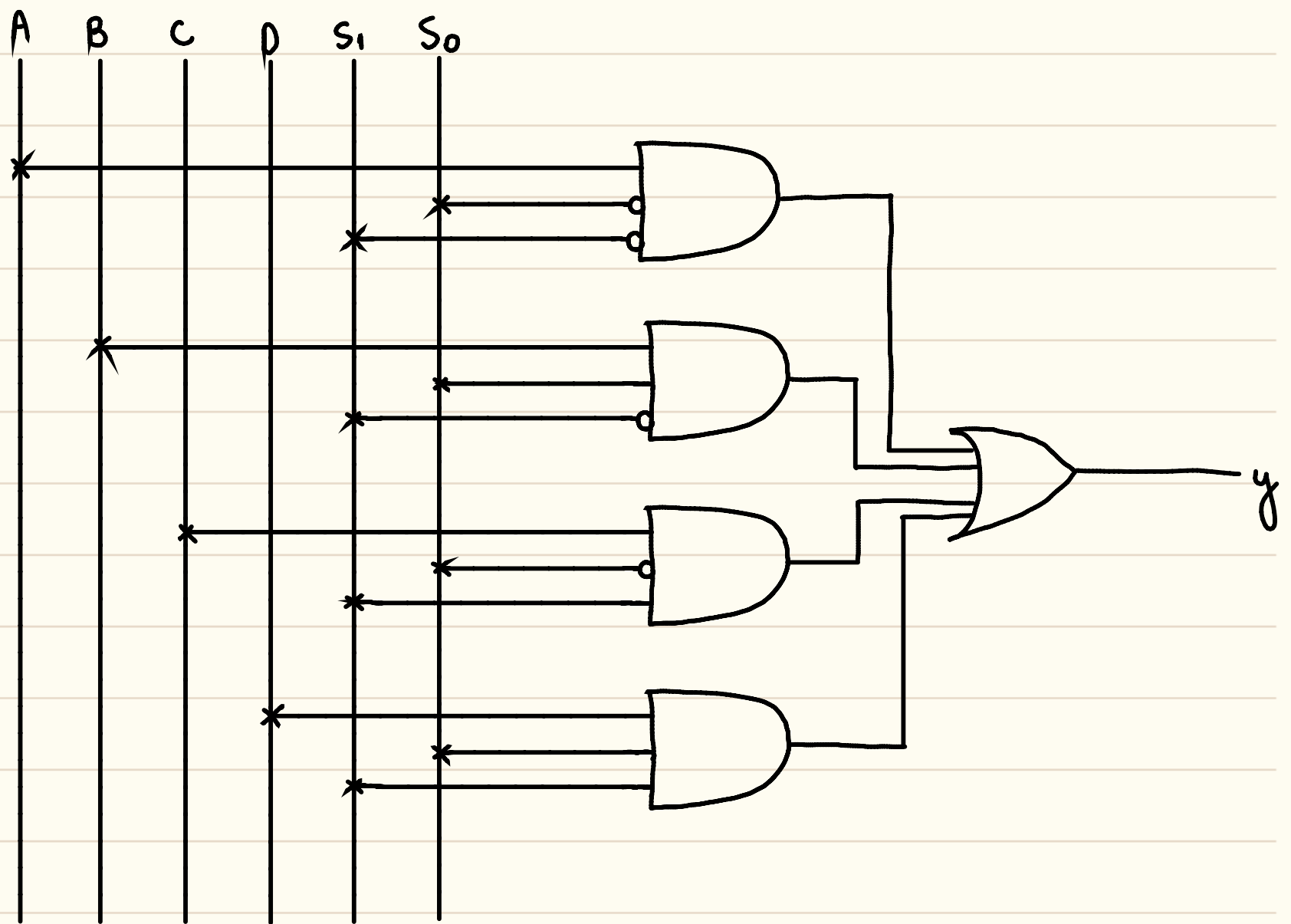


• Circuito de um MUX com input de 4 registradores de 4 bits cada



- Tabela verdade

S <sub>1</sub>	S <sub>0</sub>	y
0	0	A
0	1	B
1	0	C
1	1	D