Due: September 7, 2025

Problem 1 For the circuit below (see Figure 1), answer the following questions. You may assume zero initial conditions for the capacitor voltage.

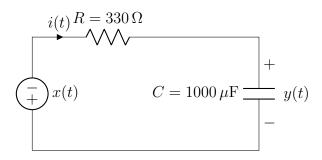


Figure 1: A series RC with input x(t), current i(t), and output y(t) across the capacitor.

- (a) [25 pts] Using your knowledge from circuit theory, derive a relationship between the input and the output of the circuit. Note that you can leave your final answer as a differential equation.
- (b) [25 pts] Assume that you are able to provide a sinusoidal input by connecting the circuit above to a signal generator that gives $x(t) = A\sin(\omega t)$. Solve for y(t) using your answer from part (a).

[use more pages if needed]

Problem 2 Apply your knowledge of computer programming to solve the circuit equation you derived in the previous problem. You may use Python jupyter notebooks or MATLAB Live Scripting to complete the problem. Make sure to attach your code notebook as a PDF to your final submission.

- (a) [25 pts] Using scipy.integrate.odeint or MATLAB's ode45 (or a different ode solver in MATLAB), simulate the ODE for $t \in [0,T]$ with $x(t) = A\sin(\omega t)$. Plot the input x(t) with time and the output y(t) with respect to time on two different axes side-by-side. Make sure to clearly label the axes and include a legend. You are allowed to choose A, ω, T but make sure to describe your choices clearly. Report the gain (the output magnitude divided by the input magnitude) at time 0 and at your last time point.
- (b) [25 pts] Solve the circuit for two additional inputs below and similarly plot the results as above:
 - 1. A step input: x(t) = A u(t), where u(t) is the unit magnitude step function.
 - 2. A pulse input: $x(t) = A[u(t-t_0) u(t-t_0-\tau)]$ for some fixed positive values of t_0, τ .

Remember to plot both inputs and outputs, and label your graphs clearly.