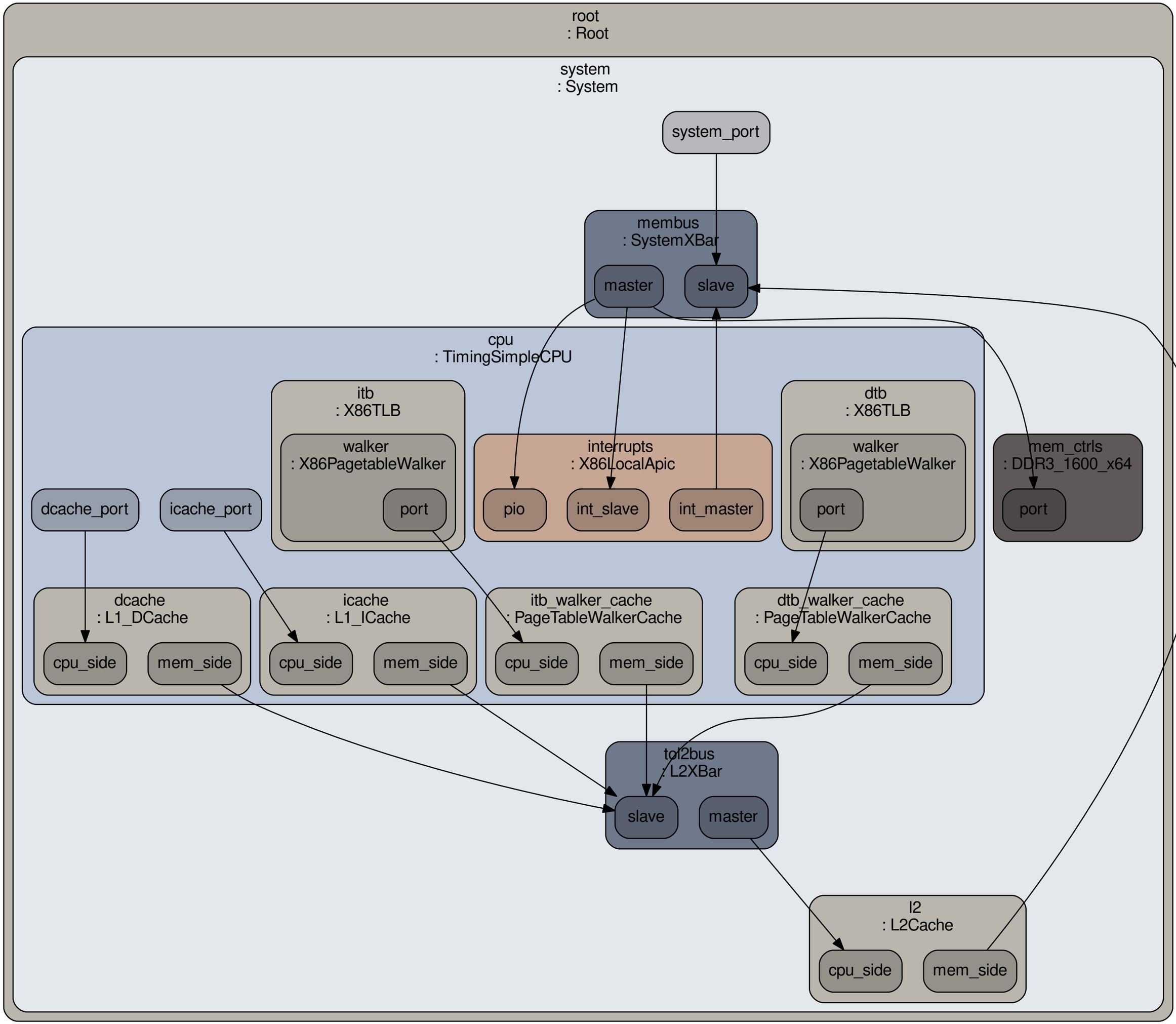
root
: Root
system
: System
system_port
membus
: SystemXBar
master
slave
cpu
: TimingSimpleCPU
itb
: X86TLB
walker
: X86PagetableWalker
port
interrupts
: X86LocalApic
pio
int_slave
int_master
dtb
: X86TLB
walker
: X86PagetableWalker
port
mem_ctrls
: DDR3_1600_x64
port
dcache_port
icache_port
dcache
: L1_DCache
cpu_side
mem_side
icache
: L1_ICache
cpu_side
mem_side
itb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
dtb_walker_cache
: PageTableWalkerCache
cpu_side
mem_side
tol2bus
: L2XBar
slave
master
l2
: L2Cache
cpu_side
mem_side