COL215: Digital Logic and System Design

Special Laboratory Semester, AY 2021-22

Department of Computer Science & Engineering

Lab Assignment 6

Stopwatch

Learning Objective:

Learn (i) use of a pre-designed circuit (4-digit display, in this case) as a building block, (ii) creation of time reference.

Specifications:

Design a stopwatch and implement it on BASYS 3 board, using its 7-segment display and push buttons. Since the display has only 4 digits, assign these as follows - 1 digit for minutes, two digits for seconds and one digit for tenths of a second. Use three push buttons as follows.

• Start/Continue • Pause • Reset

Details:

The circuit will consist of a few counters, time reference and display (from previous assignment).

A. Counters: The design will be centered around an ensemble of four counters described below.
   - A modulo 10 counter to count tenths of a second
   - A modulo 10 counter to count unit digits of seconds
   - A modulo 6 counter to count tens of seconds
   - A modulo 10 counter to count minutes

The ensemble is driven by a 10 Hz timing reference. Provide for an enable input and a reset input. The enable input comes from a flip-flop/latch that is set to '1' when Start/Continue button is pressed and set to '0' when Pause button is pressed. Reset input comes from a push button.

The counters can be synchronous or asynchronous. In asynchronous counters, various bits may not change simultaneously, but the time delays will not be perceptible to the eye.

B. Time reference: 100 MHz clock available on BASYS 3 board needs to be divided by 10^7 to get 10 Hz clock that updates tenths of a second. Note that a modulo N counter divides frequency by N. Recall that the display requires a clock in the range of 250 Hz to 4 kHz. Suppose you use 1 kHz clock for the display. Then you can first divide 100 MHz frequency by 10^5 to get 1 kHz and then divide it by 100 to get 10 Hz.

Create an appropriate constraint file for connecting the circuit to on-board resources - slide switches, 7-segment displays and clock.