

COL215: Digital Logic and System Design

Special Laboratory Semester, AY 2021-22

Department of Computer Science & Engineering

Lab Assignment - 8

Asynchronous Serial Transmitter

Learning Objective:

Learn how to send serial information without a shared clock.

Specification:

Design asynchronous serial transmitter to create a loop with the receiver developed in the previous assignment

Details:

Design and implementation of a serial asynchronous transmitter and connecting the parallel output of the receiver (designed in the previous assignment) to the parallel input of the transmitter to form a loop. This will be used to demonstrate that the data sent from the PC is echoed back to it through the receiver/transmitter pair.