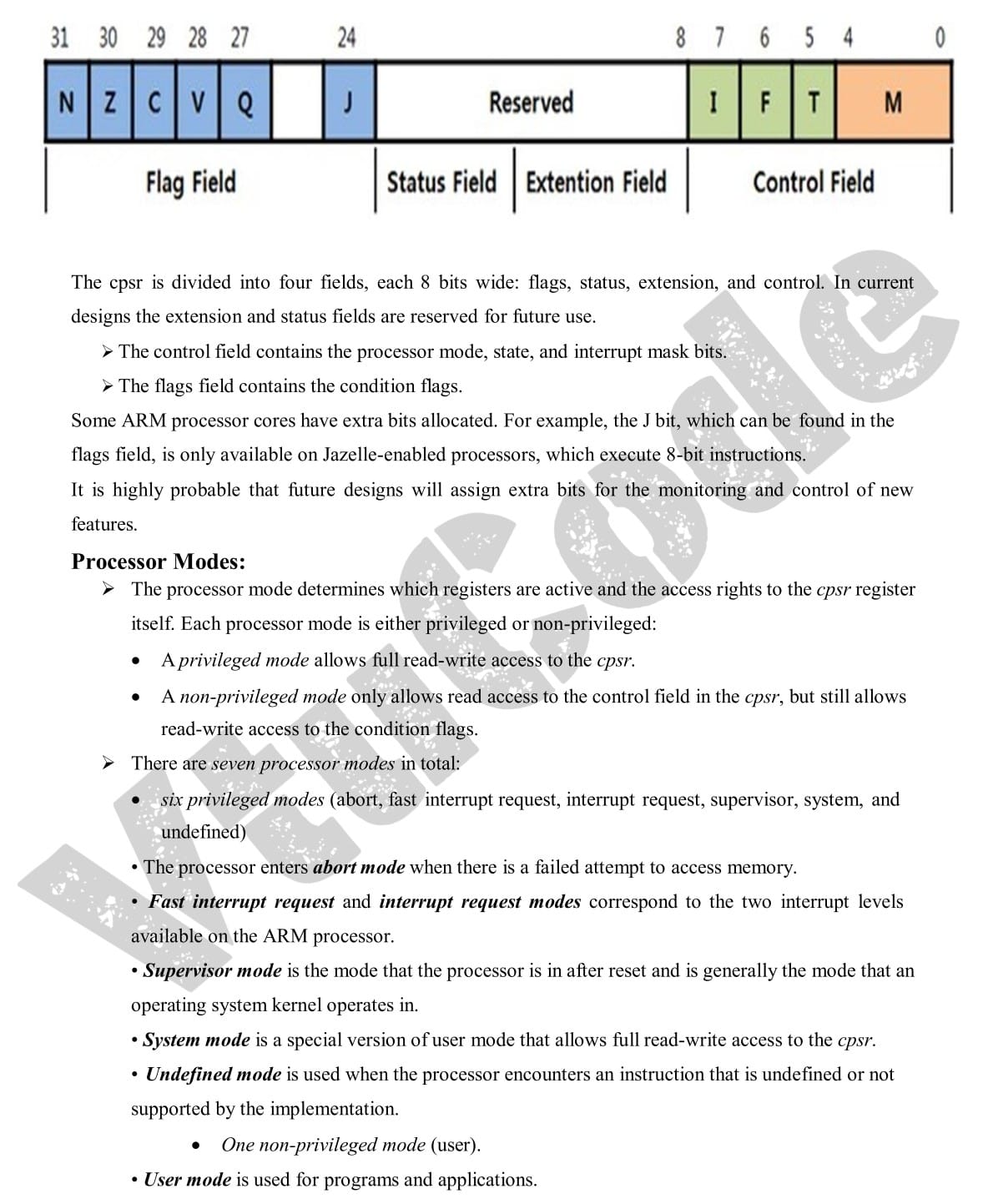
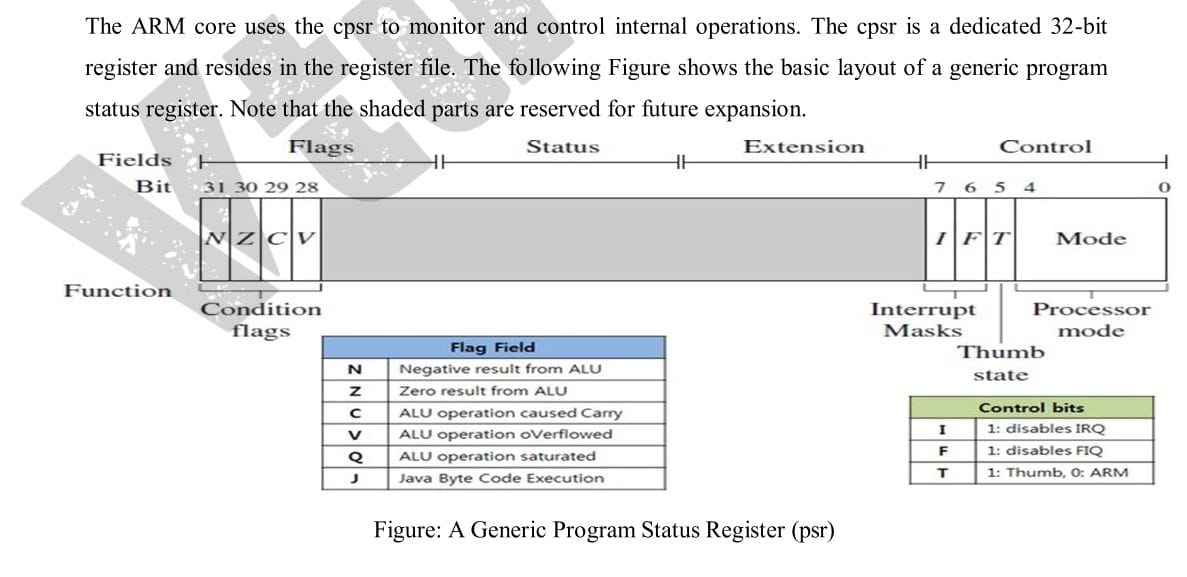
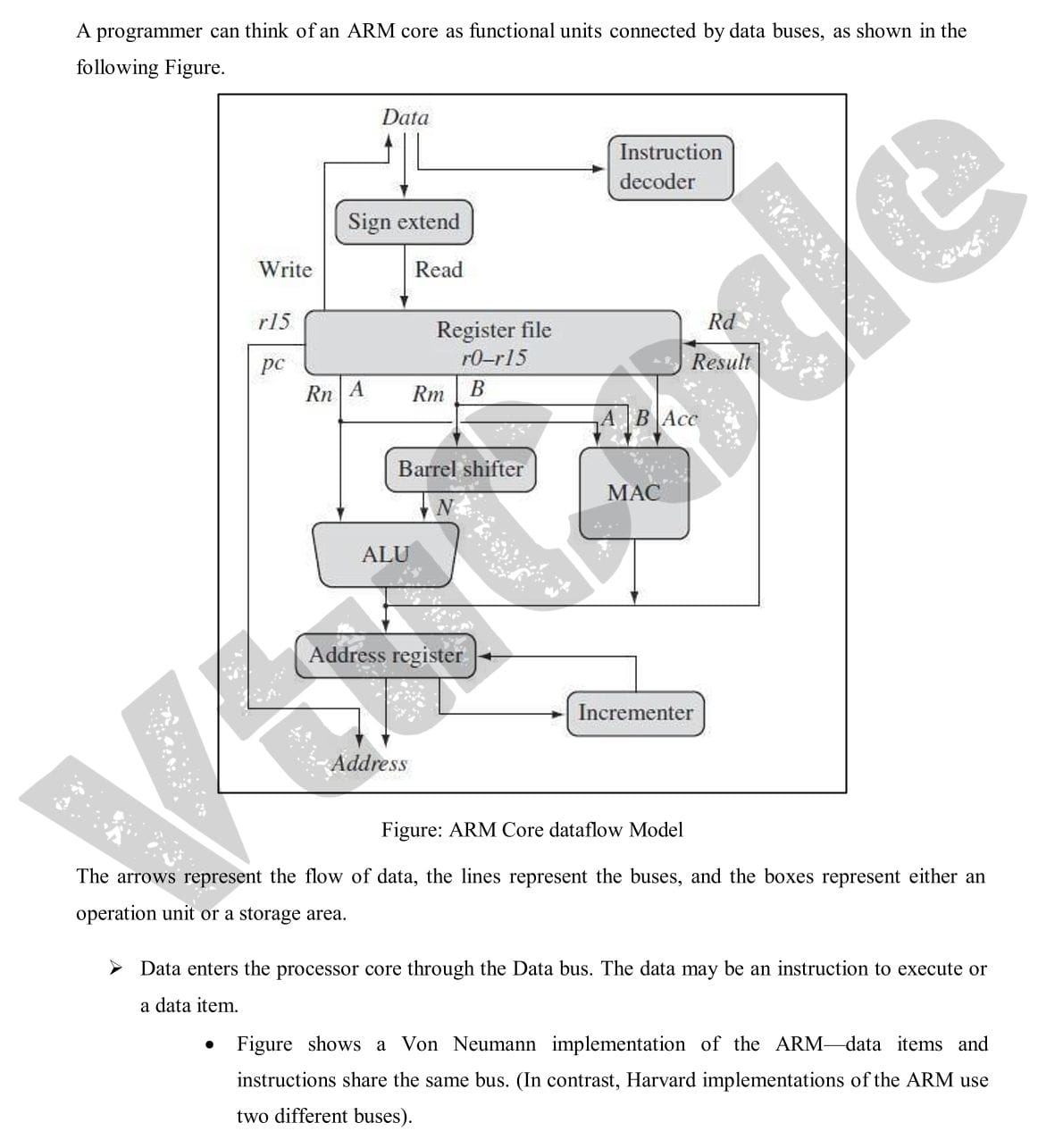
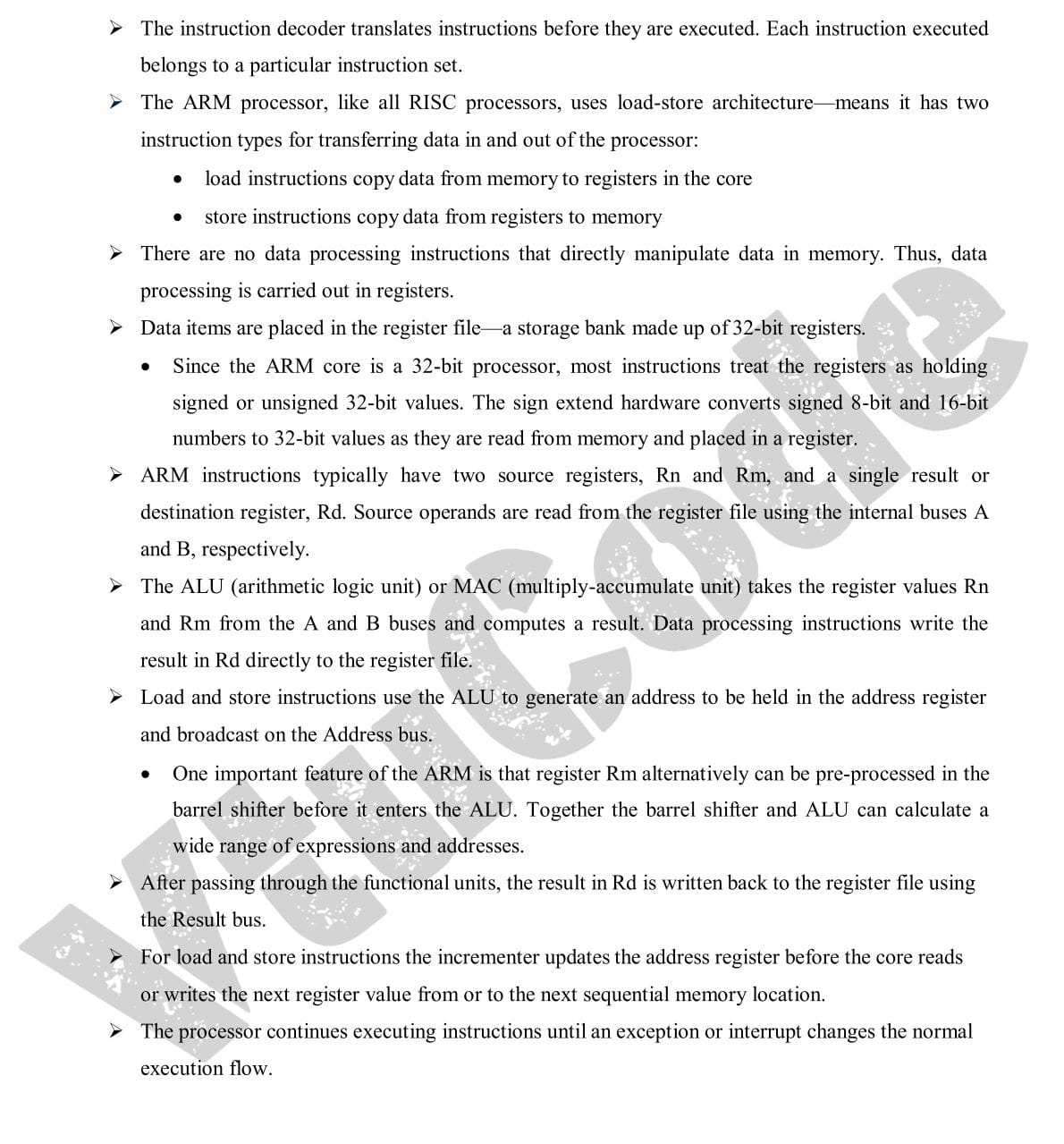
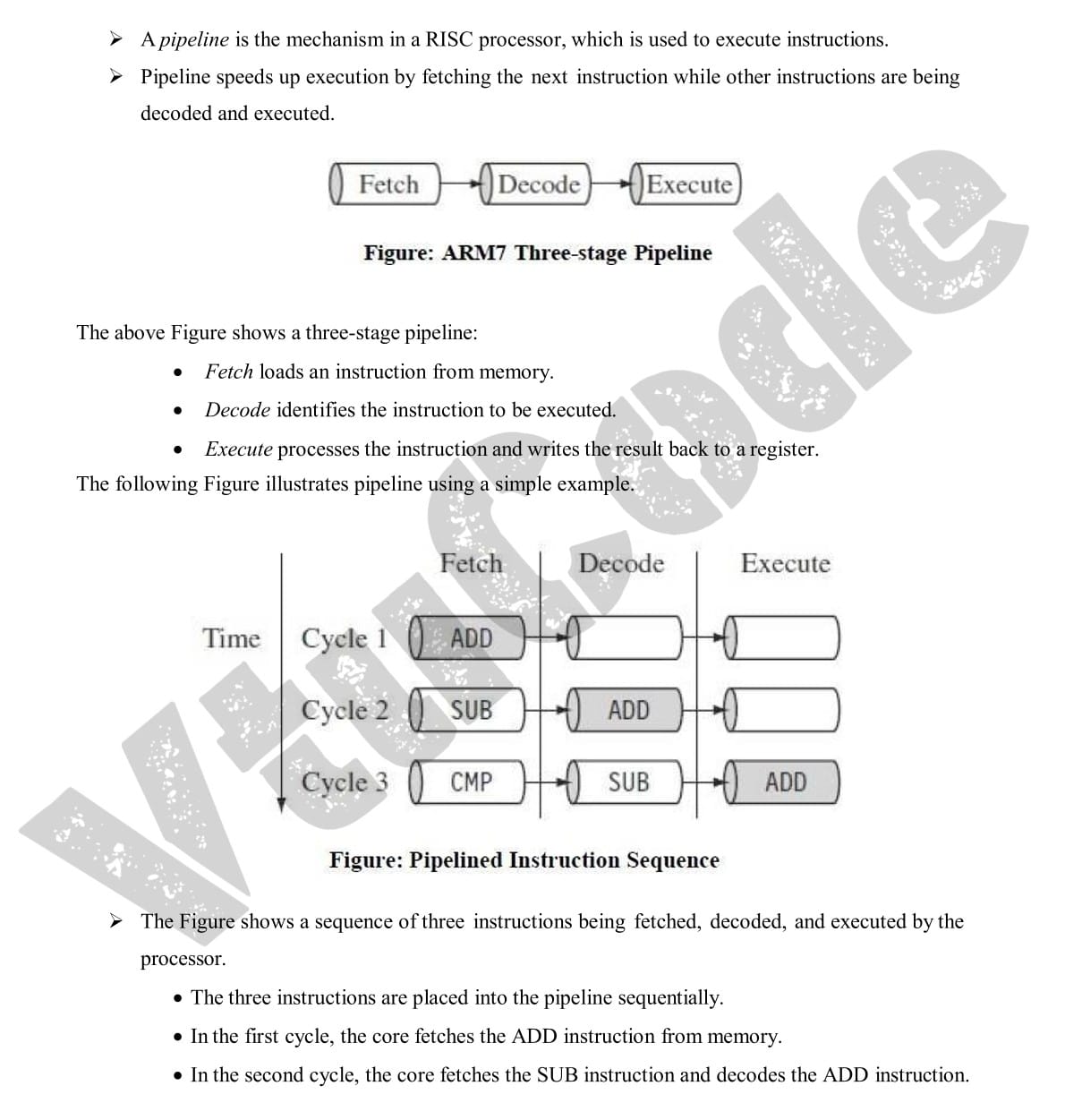
**Q.11) CURRENT PROGRAM STATUS REGISTER**

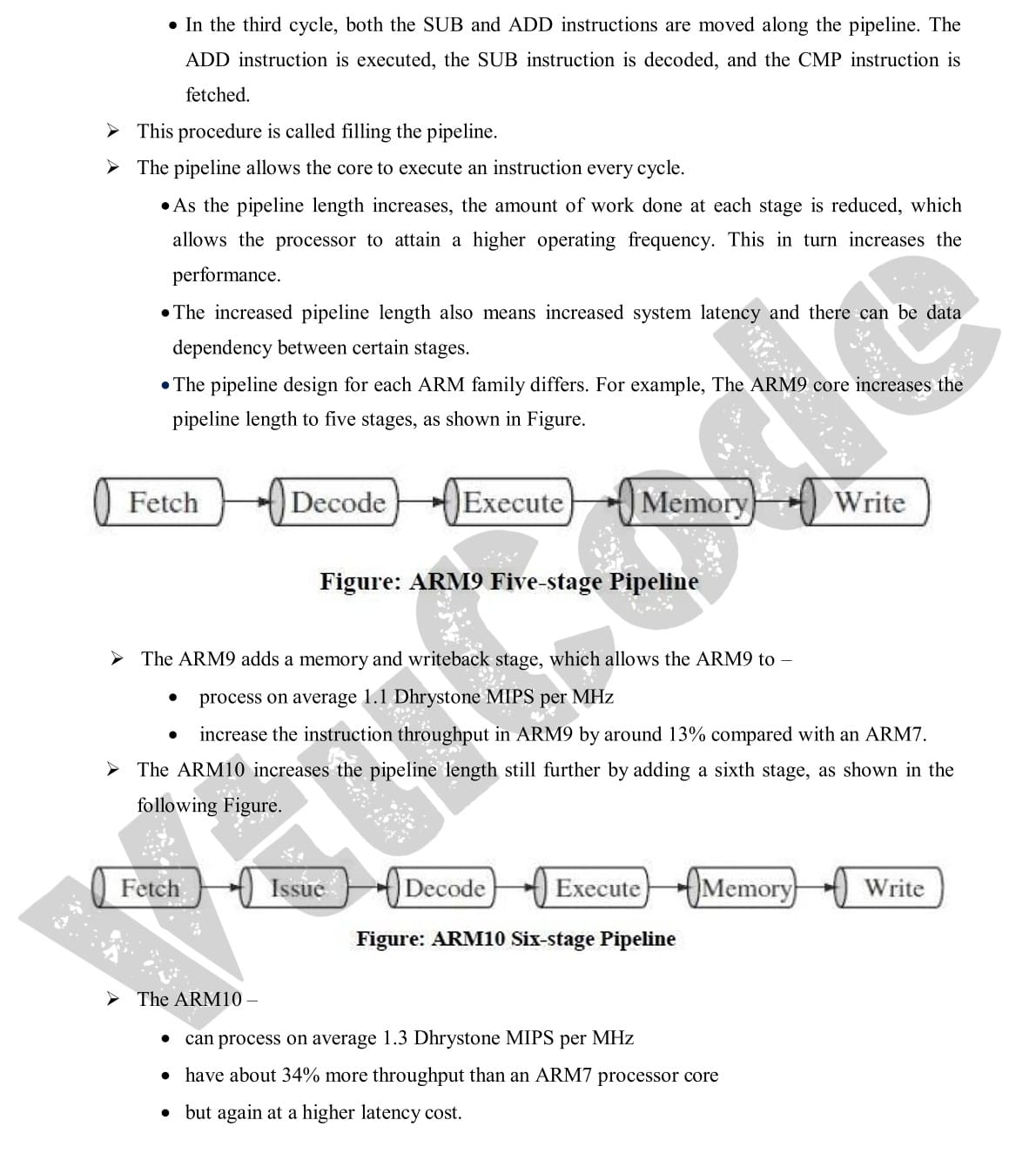
**Q.12 ) ARM CORE**

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**Q.13) PIPELINE**

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