

# Assignment-1- Canonical PoS using Assembly language

Ravi Sumanth Muppana- FWC22003

August 2022

## 1 Problem:

Derive a Canonical POS expression for a Boolean function F, represented by the following truth table:

X	Y	Z	F (X, Y, Z)
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

## 2 Solution:

### 2.1 Theory:

Canonical form is simply representing the Boolean function as sum of minimum terms (SoP) or product of maximum terms (PoS). For canonical PoS form, consider the input rows where the output is '0' and do logical AND of all those max terms in order to get the Boolean expression function. There are four cases where the output is '0'. They are  $[(0,0,1),(0,1,1),(1,1,0),(1,1,1)]$ .

### 2.2 Boolean expression:

$$F(X, Y, Z) = (X + Y + Z').(X + Y' + Z').(X' + Y' + Z).(X' + Y + Z')$$

### 2.3 Components:

Arduino UNO	1
Breadboard	1
Jumper wires	-

### 2.4 Connections:

Arduino UNO has LED connected to Pin 13. Let pins 2,3,4 be the inputs. Connect the input pin to Vcc, if it must be set to '1'. Connect the input pin to GND of arduino board, if it must be set to '0'. The LED will glow if the output is '1'.

## 3 Software:

Download this pdf and click on the below link for assembly code:  
Source code link  
or

```
wget https://raw.githubusercontent.com/Ravisumanth/FWC-1/main/Assembly/Canonical-PoS.a
```