

## RAMASAMY ALAGAR

**DEGREE** : Bachelor of Engineering  
**BRANCH** : Computer Science and Engineering  
**E-MAIL ID** : [rramasamy81@gmail.com](mailto:rramasamy81@gmail.com)  
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### Objective

To seek a challenging career in an organization, where I can apply My knowledge and hard work, while being a resourceful and efficient team member and To contribute for development of an organization.

### Career Summary

- ❖ Overall 4 years and 2 months of experience in NVMe firmware testing and test development.
- ❖ Extensive hands-on knowledge in NVMe protocol testing and test development.
- ❖ Working knowledge on NVMe SSD.
- ❖ Experience in tracking and resolving defects using test management tool [Jira] by working Closely with Developers throughout Software Defect Life Cycle.
- ❖ Expert in developing corner and complex test scenarios [**Negative test cases**, out-of-testing].
- ❖ Experience in preparing test strategy, test plan and test case based on the **Product Requirement Document** (PRD).
- ❖ Hands-on experience in developing test cases for **Firmware**, NAND And all its features.
- ❖ Have debugged and fixed many bugs which were raised through Jira Ticket.
- ❖ Experience in troubleshooting problems encountered while running scripts.
- ❖ Experience in working with Python advanced concepts, Storage core concepts
- ❖ Skilled in developing python test scripts for NVMe product using python Framework And Storage Validation Framework by studying test information needs, studying drive details, conferring with onsite users, referring NVMe Specification documents, following systems Flow, and work processes
- ❖ Implemented new functionalities in the existing test scripts as per requirements and Involved in design, development of customized templates.
- ❖ Good knowledge and experience on using Oakgate, Lecroy, Aardvark, beagle, UNH scripting.
- ❖ Good working knowledge and experience on UNH compliance test suite.
- ❖ Have identified and reported the issues in UNH compliance test suite to the UNH group.
- ❖ Have developed python script to run Oakgate and UNH test suite from windows.
- ❖ Good Experience in understanding complex code and design buckets.
- ❖ Experience in code review, Issue tracking through JIRA and process improvement.
- ❖ Good team player with strong interpersonal skills.
- ❖ Flexible to adopt new technologies.
- ❖ Hands-on experience MCTP protocol (Basic and advanced).
- ❖ Hand-on experience MCTP PCIe VDM.

### Work Experience

**Duration:** August 2014 – Till Date  
**Company:** HCL Technologies, Chennai.  
**Designation:** Lead Engineer

## Technical Skills Set

**Languages:** Python, C, C++, Java

**Operating System:** Windows server 2012, Windows server 2012 R2, Windows 7, Ubuntu

**Tools:** Eclipse, Oakgate, Quarch, SMBUS, JIRA, Debug viewer, mercurial, aardvark, Lecroy.

**Specification:** NVMe 1.0, NVMe 1.2, NVMe 1.2B, NVMe 3.0 and NVMe management interface specification.

## Organizational Experience

**Employee Id:** 51516445

**Designation:** Software Engineer

**Client Project:** Intel CORP

**Work Summary:** NVMe SSD firmware validation.

### Project: NVME SSD Lecroy Validation

**Roles and Responsibility:** To develop trainer and verification script to validate the features Of NVMe SSD based on NVMe specification.

**Team size:** 2

**Environment:** Lecroy Platform

**Description:**

- ❖ Develop lecroy trainer script for all NVME features approved by client.
- ❖ Validate the lecroy traces for above trainer script using verification scripts.
- ❖ Hand-on experience in debugging the lecroy test failure.
- ❖ Have developed trainer and verification scripts for MCTP PCIe VDM Transport Binding specification (SPEC DSP0238)
- ❖ Developed python automation framework to run all trainer and verification scripts Using win32com module.

### Project: NVME SSD oakgate Validation

**Roles and Responsibility:** To develop AT 3 script and automation test script to validate the Features of NVMe SSD based on NVMe specification.

**Team size:** 2

**Environment:** Oakgate Platform

**Description:**

- ❖ Develop AT 3 script based on the given scenario.
- ❖ Validate direct tests for NVMe SSD devices.
- ❖ Hand-on experience in debugging the oakgate test failure.
- ❖ Developed python automation framework to run both AT 3 tests and direct tests.

**Project: NVME SSD Validationfor UNH**

**Roles and Responsibility:** To validate the UNH scripts based on features supported by the SSD.

**Team size:** 2

**Environment:** Linux Platform

**Description:**

- ❖ Develop automation test script to run all UNH tests using paramiko module in python.
- ❖ Hand-on experience in debugging the UNH test failure.
- ❖ Have developed trainer and verification scripts for MCTP PCIe VDM Transport Binding specification (SPEC DSP0238)
- ❖ Developed python automation framework to run both AT 3 tests and direct tests.

**Project:** NVMe SSD firmware validation.

**Roles and Responsibility:** Lead Engineer.

**Team size:** 8

**Environment:** Windows server 2012, Windows server 2012 R2, Windows 7, Ubuntu

**Testing Tools:** Eclipse, Oakgate, Lecroy, Quarch, SMBUS, JIRA

**Description:**

- ❖ Validation Test development for SSD firmware validation.
- ❖ Developed cross product test suite using python which is used by all NVMe products.
- ❖ Responsible for HOST test development implementation and process.
- ❖ Identify test requirements from the product requirement documents
- ❖ Develop test scenarios from the test requirements and implement it.
- ❖ Working closely with designers and developers in planning, scheduling, developing and Executing system level as well as integration tests for PCIe SSD firmware validation.
- ❖ Participate in Test plan work group meeting.

**Achievements**

- ❖ Got SPOT award from HCL for best performance of the year 2015.
- ❖ Awarded by client for the cross product test suite development in 2015.
- ❖ Got Best performance award from client for the year 2016.
- ❖ Consistent performance in past 4 years. Got first rating in all the appraisals.

**Academic**

Course	Institution	Board / University	Year of Completion	Aggregate Marks (%)
B.E.,(CSE)	PSNA College Of Engineering & Technology, Dindigul.	Anna University	2014	82.21
HSC	CSMA Hr.Sec school Dindigul	State Board	2010	84.91
SSLC	CSMA Hr.Sec school Dindigul	State Board	2008	91.2

## Area OF Interest

- ❖ NVME Specification 1.3, 1.2
- ❖ Python
- ❖ Oakgate
- ❖ Lecroy
- ❖ UNH
- ❖ MCTP
- ❖ MCTP PCIe VDM

## PERSONAL DETAILS

**NAME** : RAMASAMY.A  
**GENDER** : MALE  
**DATE OF BIRTH** : 21-01-1993  
**FATHER'S NAME** : Mr. R. ALAGAR  
**ADDRESS** : 2/418, SAKKINAYAKKAN PATTY,  
PERIYAKOTTAI (PO), DINDIGUL-624306.  
**MOTHER TONGUE** : Tamil  
**LANGUAGES** : **Read** : Tamil and English  
**Write** : Tamil and English  
**Speak** : Tamil, English and Kannada  
**HOBBIES** : CHESS

## DECLARATION

I, **RAMASAMY.A** do hereby confirm that the information given above is true to the Best of My knowledge.

**Place:** CHENNAI

**Date:**

**SIGNATURE,**

**[RAMASAMY.A]**