LCD Display Pin Connections

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **LCD**  **PIN #** | **1** | **2** | **3** | **4`** | **5** | **6** | **7-14** | **15-16** |
| Connection | GND | 5 Volts | Potentiometer  (10KΩ)  thru. to  GND | AVR PORT  D6 | GND | AVR PORT  D7 | AVR PORT  C0-C7 | Vcc-GND |

MICROCHIP

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|  |  |

USART

RXD0 and TXD0 are the receive and transmit pins for USART0 (this is the same for the ATmega32)

RXD0 is mapped to PD0

TXD0 is mapped to PD1

RXD1 and TXD1 are the receive and transmit pins for USART1(the ATmega32 does not have a USART on these pins)

RXD1 is mapped to PD2

TXD1 is mapped to PD3

LED MATRIX

picture