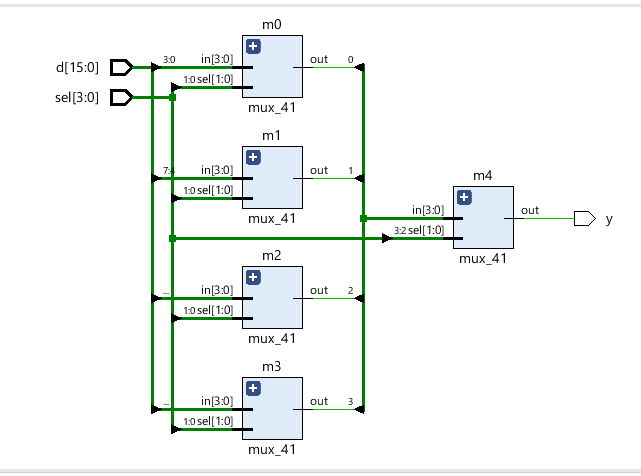
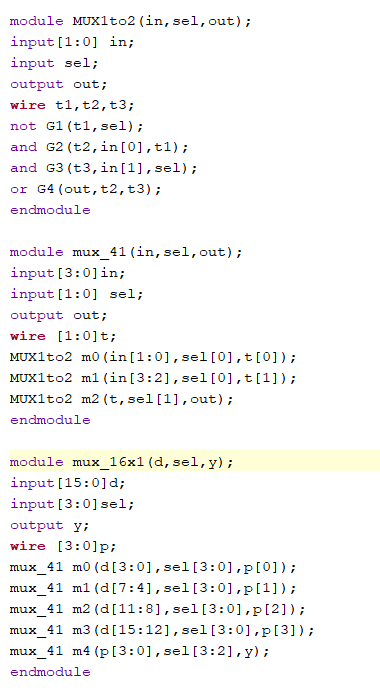
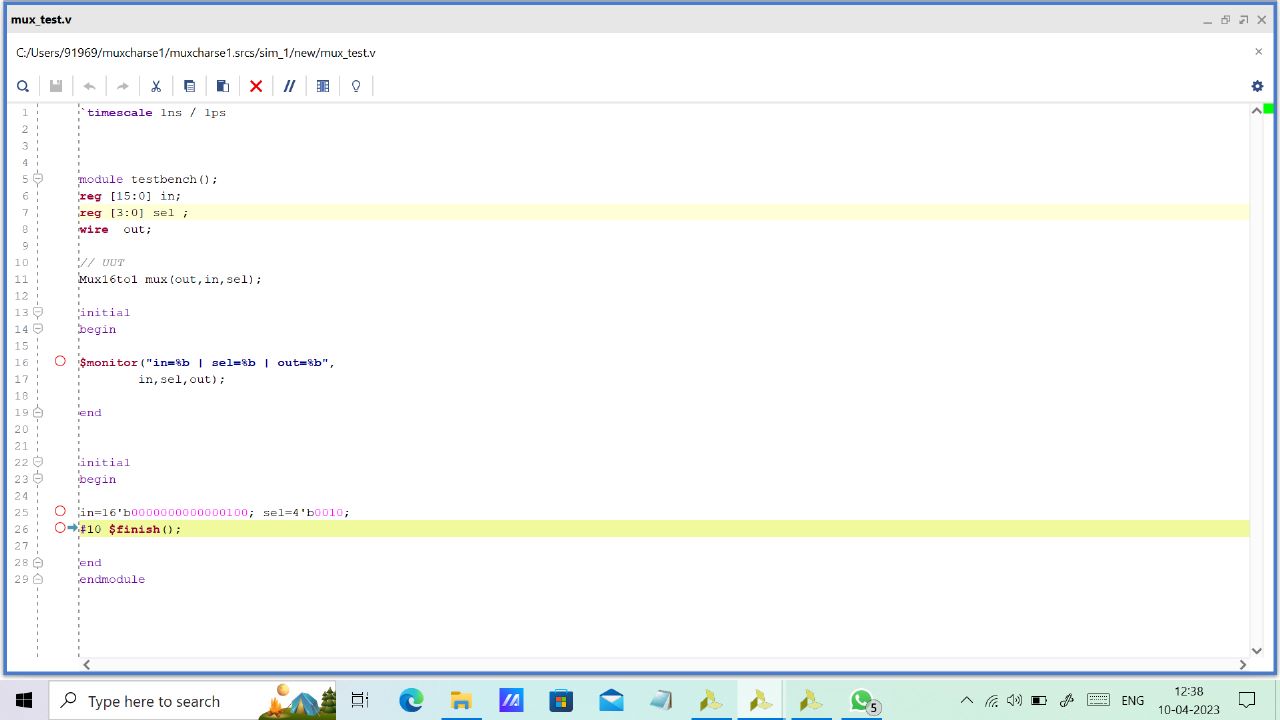
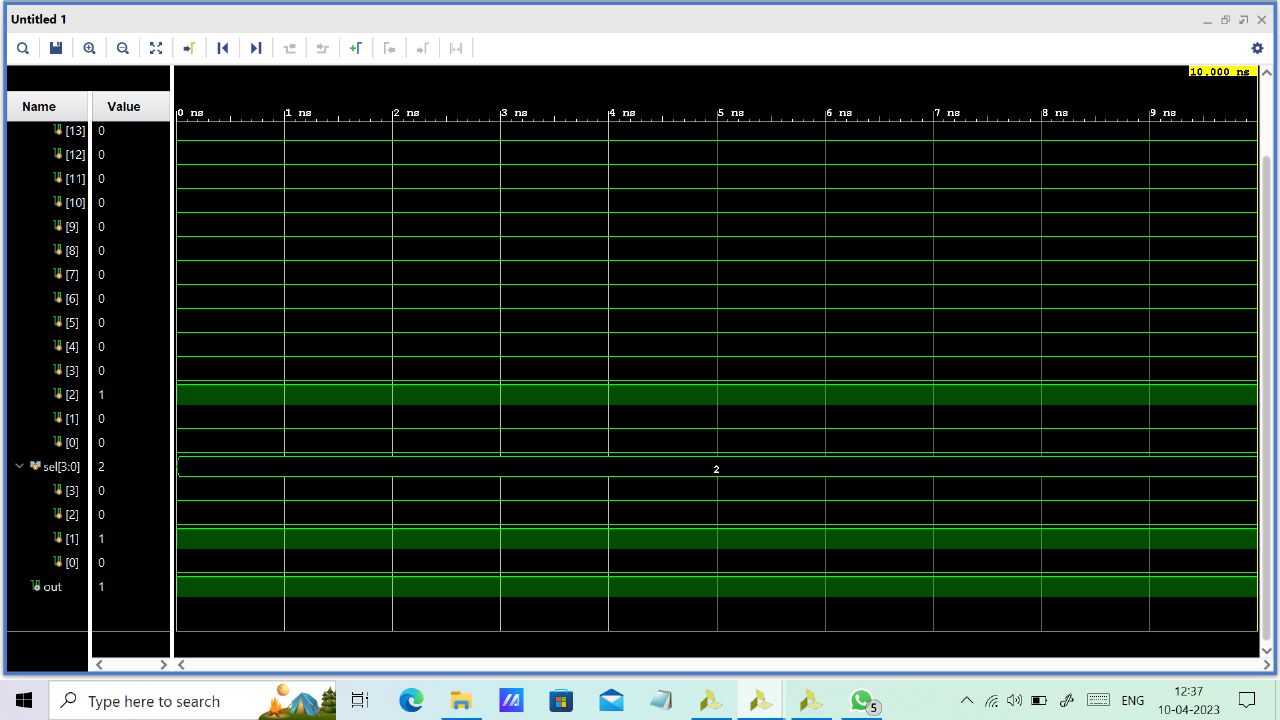
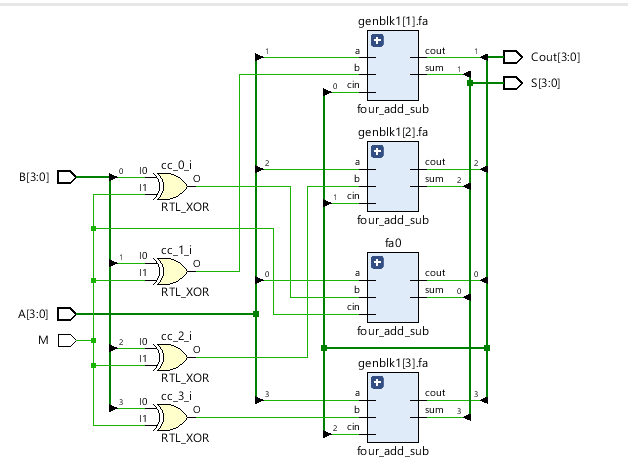
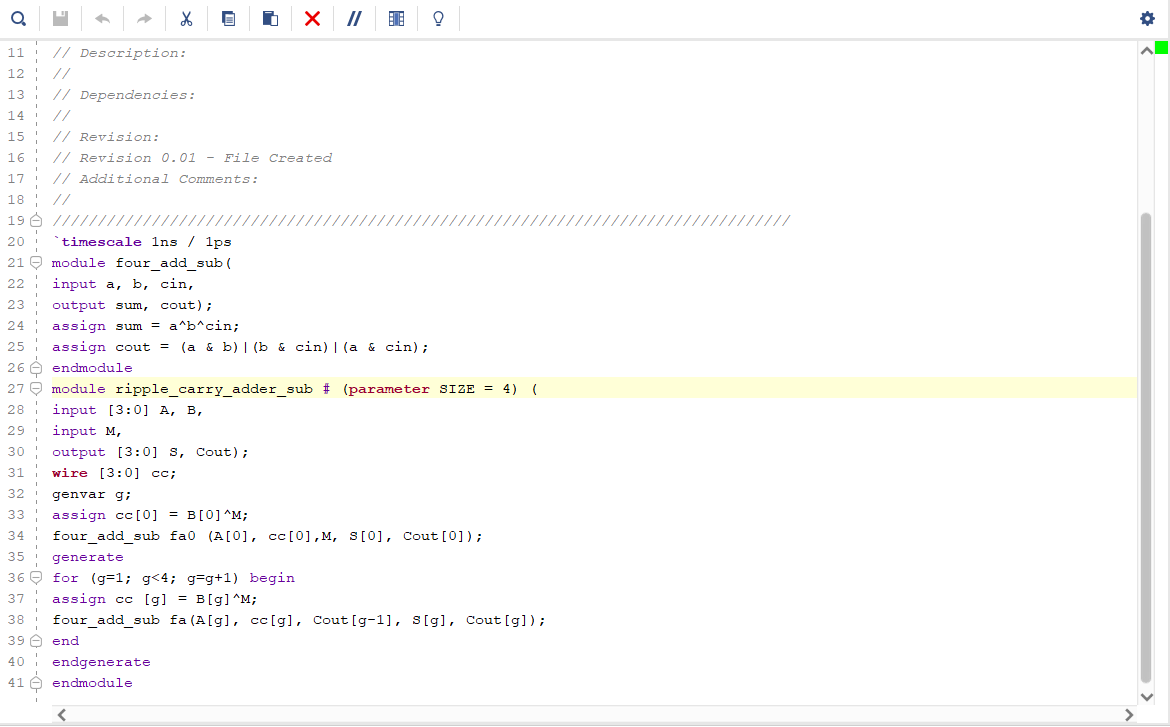
**Q1.16x1 MUX**



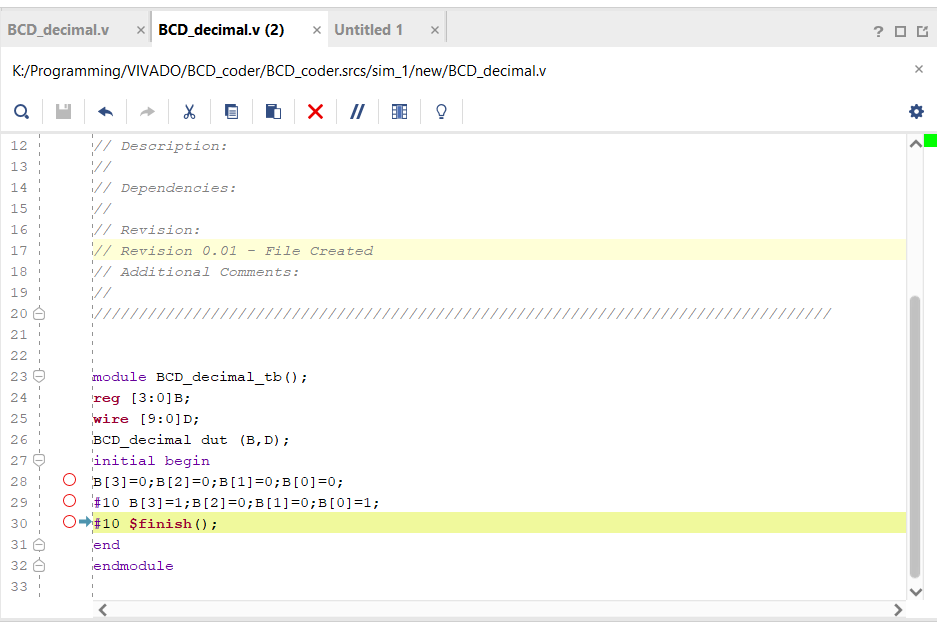
**Mux 16x1 (test bench)**

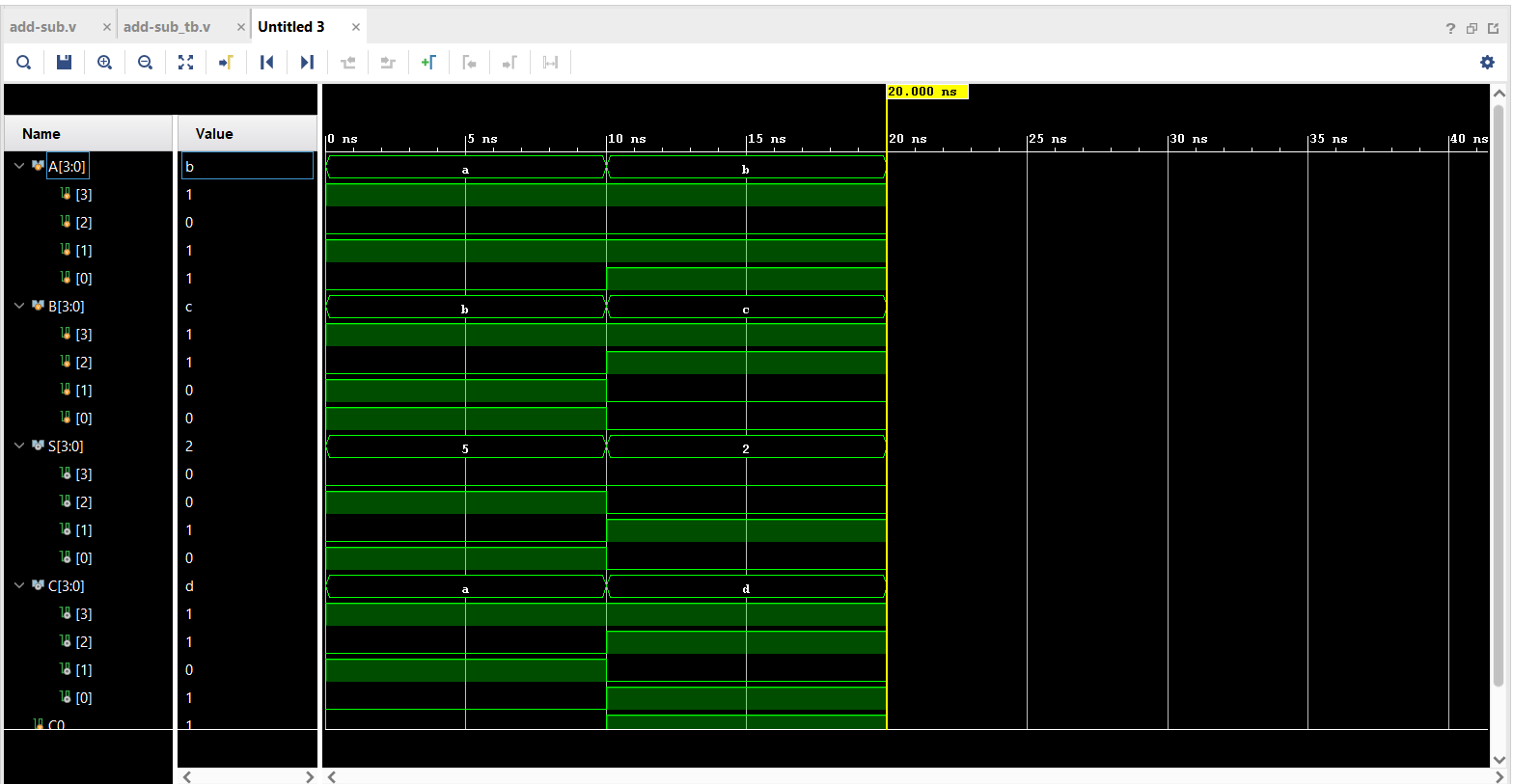




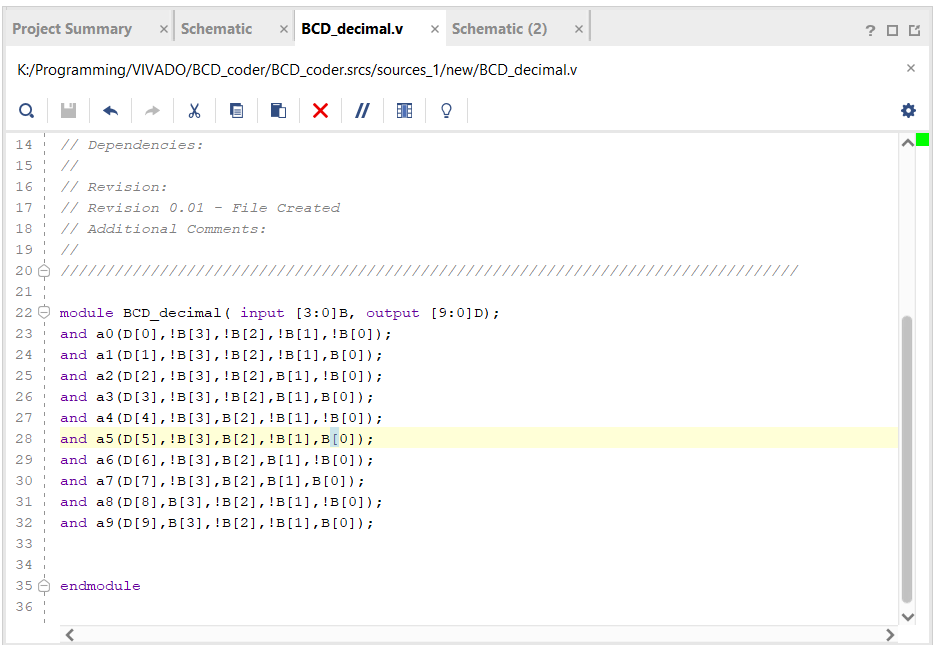
**Q2.4-bit adder/subtractor**

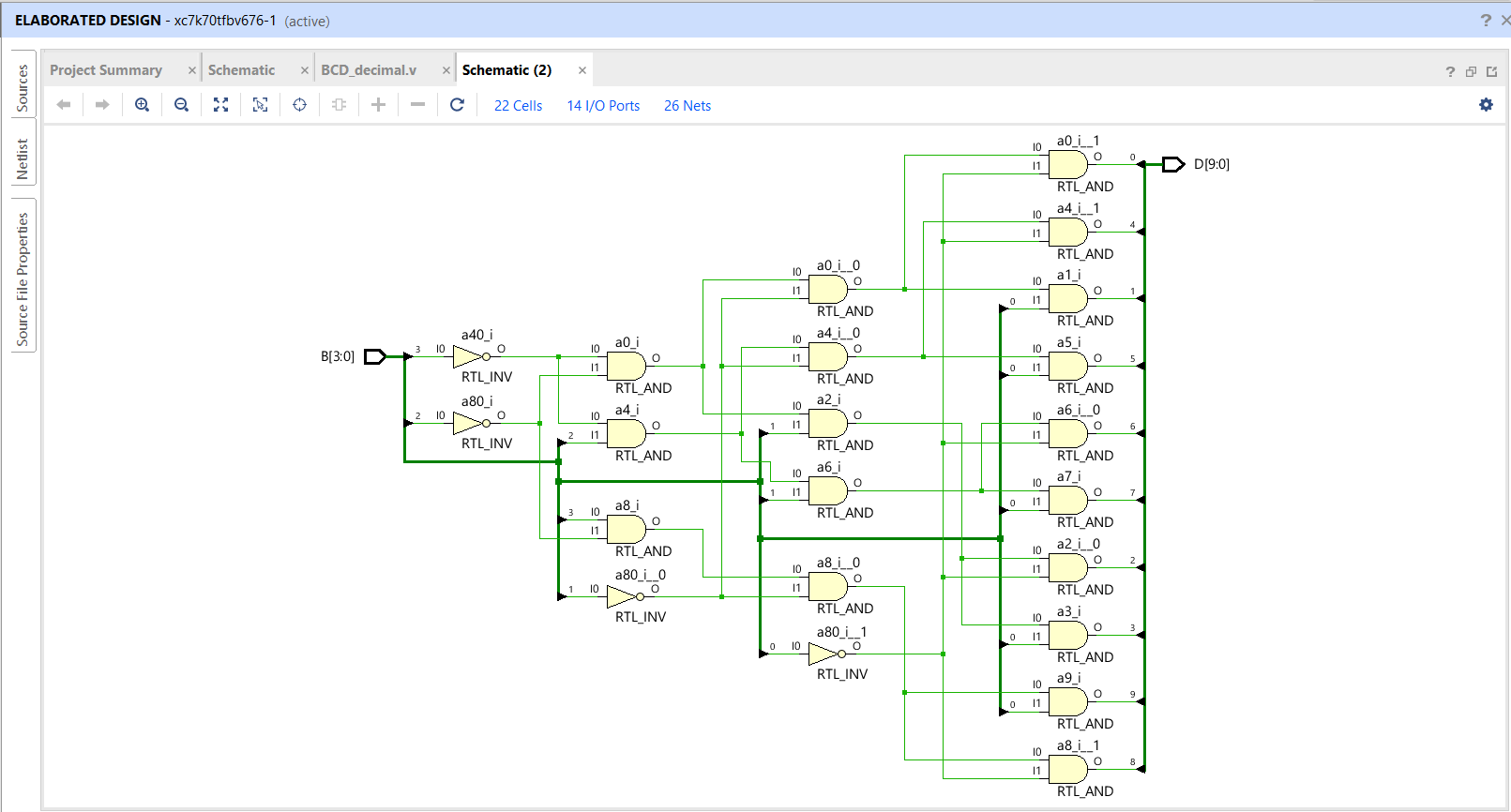
**4-bit add/sub(test bench)**

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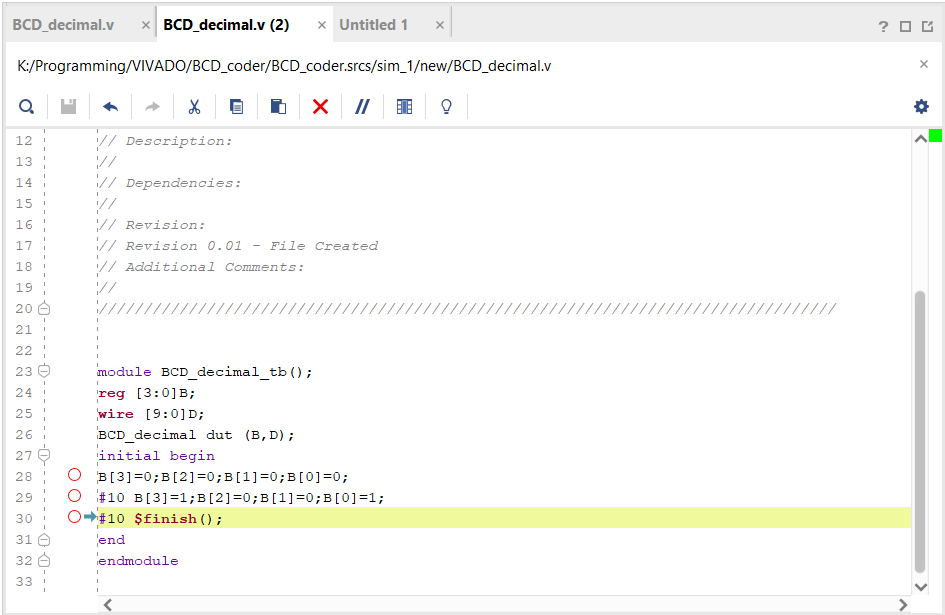
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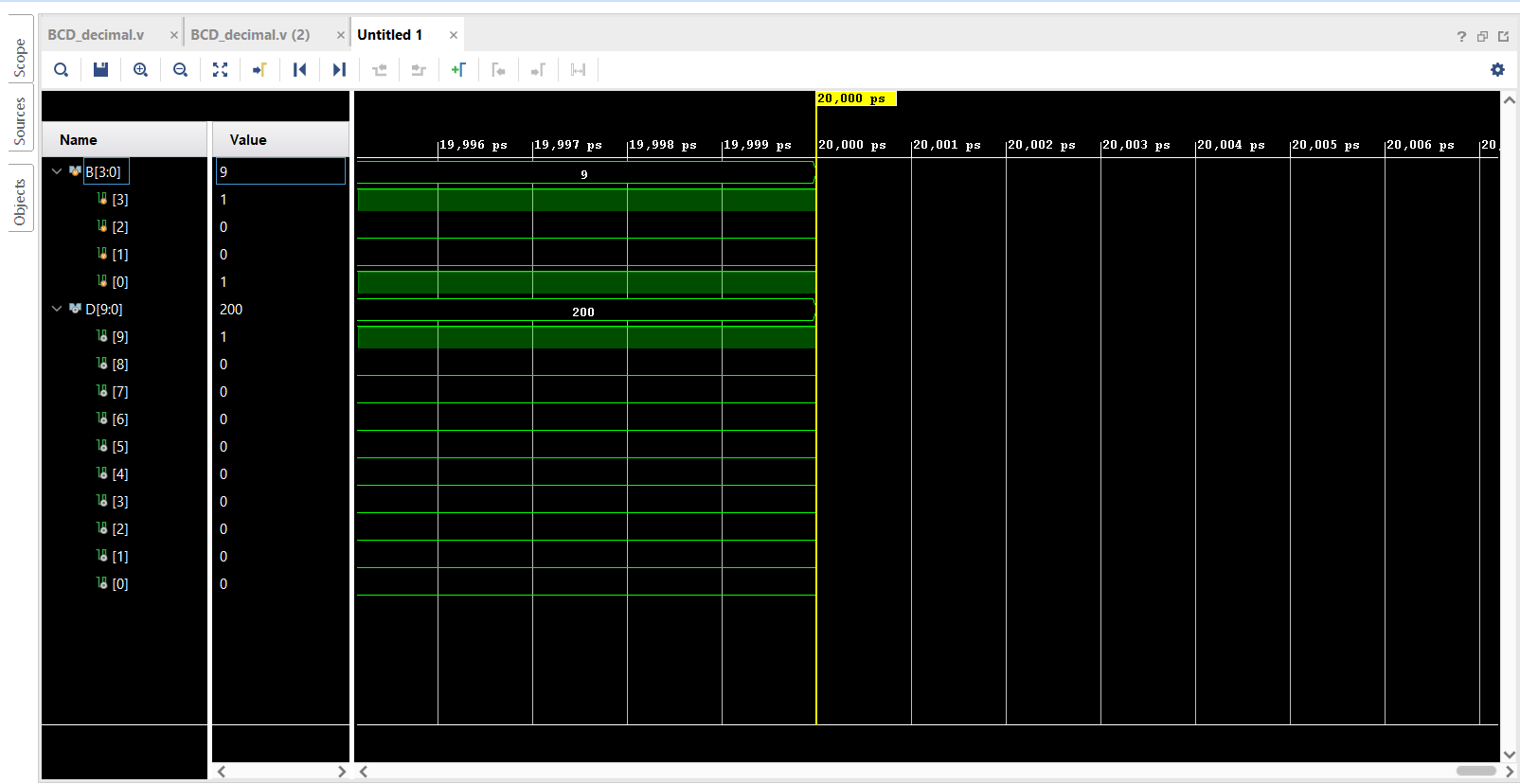
**Q3.BCD to Decimal decoder**





**BCD to Decimal decoder (test bench)**





**Submitted by**

**ARYAN KUMAR**

**210002016**