

# **Simulink Design Verifier Report**

**progetto\_verificaformale**

**Mattia**

## **Simulink Design Verifier Report: progetto\_verificaformale**

Mattia

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# Capitolo 1. Summary

## Analysis Information.

Model: progetto\_verificaformale  
Checksum:  
Mode: Property proving  
Status: In progress  
Elapsed Time: 0s

## Objectives Status.

<b>Number of Objectives:</b>	<b>3</b>	
Objectives Valid:	3	( 100% )

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# Capitolo 2. Analysis Information

## Indice

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## Model Information

File:	progetto_verificaformale
Version:	1.11
Time Stamp:	Thu Dec 09 10:53:27 2021
Author:	Alessandro

## Analysis Options

Mode:	PropertyProving
Rebuild Model Representation:	IfChangeIsDetected
Proving Strategy:	Prove
Proof Assumptions:	UseLocalSettings
Test Objectives:	UseLocalSettings
Assertions:	UseLocalSettings
Maximum Analysis Time:	300s
Block Replacement:	off
Parameters Analysis:	off
Include expected output values:	off
Randomize data that do not affect the outcome:	off
Additional analysis to reduce instances of rational approximation:	on
Save Data:	on
Save Harness:	off
Save Report:	off

# Constraints

## Analysis Constraints

Name	Analysis Constraint
Liveness/Assumption	{ -1 1 }

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# Capitolo 3. Proof Objectives Status

## Indice

Objectives Valid .....	4
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## Objectives Valid

#	Type	Model Item	Description	Analysis Time (sec)
1	Assert	Agreement /Assertion	Assert	-1
2	Assert	Liveness/Assertion	Assert	-1
3	Assert	Validity/Assertion	Assert	-1

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# Capitolo 4. Properties

## Indice

Agreement /Assertion .....	5
Liveness/Assertion .....	5
Validity/Assertion .....	5

## Agreement /Assertion

### Summary.

Model Item: Agreement /Assertion  
Property: Assert  
Status: Valid

## Liveness/Assertion

### Summary.

Model Item: Liveness/Assertion  
Property: Assert  
Status: Valid

## Validity/Assertion

### Summary.

Model Item: Validity/Assertion  
Property: Assert  
Status: Valid