# Simulink Design Verifier Report progetto\_verificaformale Mattia

Simulink	Design	Verifier	Report:	progetto	_verificaform	าale
Mattia	_		_	_		

Data di pubblicazione 09-Dec-2021 13:49:19

## Indice

1. Summary	1
2. Analysis Information	2
Model Information	2
Analysis Options	2
Constraints	3
Analysis Constraints	3
Unsupported Blocks	
3. Proof Objectives Status	
Objectives Valid	4
4. Properties	5
Agreement /Assertion	5
Liveness/Assertion	
Validity/Assertion	
1 0.22 0.25 1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	

# **Capitolo 1. Summary**

#### **Analysis Information.**

Model: progetto\_verificaformale

Release: R2021b Update 1

Checksum: 1031467178 400273699 1269143048 1281080086

Mode: Property proving

Model Representation: Built on 09-Dec-2021 13:49:13

Status: Completed normally

PreProcessing Time: 3s Analysis Time: 4s

**Objectives Status.** 

Number of Objectives: 3

Objectives Valid: 3 (100%)

# **Capitolo 2. Analysis Information**

#### **Indice**

Model Information	. 2
Analysis Options	
Constraints	
Analysis Constraints	
Unsupported Blocks	

#### **Model Information**

File: progetto\_verificaformale

Version: 1.13

Time Stamp: Thu Dec 09 11:19:45 2021

Author: Alessandro

## **Analysis Options**

Mode: PropertyProving
Rebuild Model Representation: IfChangeIsDetected

Proving Strategy: Prove

Proof Assumptions: UseLocalSettings
Test Objectives: UseLocalSettings
Assertions: UseLocalSettings

Maximum Analysis Time: 300s
Block Replacement: off
Parameters Analysis: off
Include expected output values: off
Randomize data that do not affect the off

outcome:

Additional analysis to reduce instan- on ces of rational approximation:

Save Data: on Save Harness: off Save Report: off

## **Constraints**

### **Analysis Constraints**

Name	Analysis Constraint
Liveness/Assumption	{-11}

## **Unsupported Blocks**

The following blocks are not supported by Simulink Design Verifier. They were abstracted during the analysis. This can lead Simulink Design Verifier to produce only partial results for parts of the model that depends on the output values of these blocks.

Block	Туре
Subsystem/Coordinator	chart
FeedForward	state
Subsystem/Tail	chart

# **Capitolo 3. Proof Objectives Status**

#### **Indice**

Objectives Valid .......4

## **Objectives Valid**

#	Туре	Model Item	Description	Analysis Time (sec)
1	Assert	Agreement /Assertion	Assert	2
2	Assert	Liveness/Assertion	Assert	2
3	Assert	Validity/Assertion	Assert	2

# **Capitolo 4. Properties**

#### **Indice**

Agreement /Assertion	. 5
Liveness/Assertion	. 5
Validity/Assertion	

## **Agreement / Assertion**

#### Summary.

Model Item: Agreement /Assertion

Property: Assert Status: Valid

## Liveness/Assertion

#### Summary.

Model Item: Liveness/Assertion

Property: Assert Status: Valid

## Validity/Assertion

#### Summary.

Model Item: Validity/Assertion

Property: Assert Status: Valid