NI R Series Multifunction RIO Specifications

This document lists the specifications of the NI 781xR/783xR/784xR/785xR. These specifications are typical at 25 °C unless otherwise noted.

	Français	Deutsch	日本語	한국어	简体中文	
ni.com/manuals						

Analog Input (NI 783xR/784xR/785xR Only)

Input Characteristics

Number of channels		Input impedance
NI 7830R	4	Powered on10 $G\Omega$ in parallel with
NI 7831R/7833R/7841R/784	2R/	100 pF
7851R/7852R/7853R/7854R	8	Powered off/overload4.0 k Ω min
Input modes	DIFF, RSE, NRSE	Input signal range±10 V
	(software-selectable; selection applies to all	Input bias current
	channels)	NI 783xR±2 nA
Type of ADC	Successive	NI $784xR/785xR$ ±5 nA
-71	approximation	Input offset current
Resolution	16 bits, 1 in 65,536	NI 783xR±1 nA
		NI $784xR/785xR$ ±5 nA
Conversion time NI 783xR/NI 784xR	4 μs	Input couplingDC
NI 785xR	1 μs	Maximum working voltage
Maximum sampling rate		(signal + common mode)Inputs should remain within ±12 V of ground
NI 783xR/784xR	200 kS/s (per channel)	· ·
NI 785xR	750 kS/s (per channel)	Overvoltage protection
	* '	Powered on±42 V



Powered off.....±35 V

Accuracy Information

NI 783xR

		Absolute Accuracy						Relativ	e Accuracy			
Nominal	Range (V)	% of R	eading		Noise + Quantization (μV)					Absolute Accuracy	Resolu	ıtion (μV)
Positive Full Scale	Negative Full Scale	24 Hours	1 Year	Offset (µV)	Single Point	Averaged	Temp Drift (%/°C)	at Full Scale (±mV)	Single Point	Averaged		
10.0	-10.0	0.0496	0.0507	2,542	1,779	165	0.0005	7.78	2,170	217		

Note: Accuracies are valid for measurements following an internal calibration. Averaged numbers assume dithering and averaging of 100 single-channel readings. Measurement accuracies are listed for operational temperatures within ± 1 °C of internal calibration temperature and ± 10 °C of external or factory-calibration temperature.

NI 784xR/785xR

			Absolute Accuracy						Relativ	e Accuracy
Nominal	Range (V)	% of R	teading	Noise + Quantization (μV)				Absolute Accuracy	Resolu	ıtion (μV)
Positive Full Scale	Negative Full Scale	24 Hours	1 Year	Offset (µV)	Single Point	Averaged	Temp Drift (%/°C)	at Full Scale (±mV)	Single Point	Averaged
10.0	-10.0	0.0186	0.0228	1,591	1,029	91.6	0.0005	3.97	1,205	121

Note: Accuracies are valid for measurements following an internal calibration. Averaged numbers assume dithering and averaging of 100 single-channel readings. Measurement accuracies are listed for operational temperatures within ± 1 °C of internal calibration temperature and ± 10 °C of external or factory-calibration temperature.

DC Transfer Characteristics

INL

NI 783xR.....±3 LSB typ, ±6 LSB max NI 784xR/785xR.....±1 LSB typ, ±3 LSB max

DNL

NI 783xR1.0 to +2.0 LSB max NI 784xR/785xR±0.4 LSB typ, ±0.9 LSB max

No missing codes

CMRR, DC to 60 Hz-86 dB

Dynamic Characteristics

Bandwidth

NI 783xR

Small signal (-3 dB)......650 kHz Large signal (1% THD)......55 kHz NI 784xR/785xR

Small signal (-3 dB) 1 MHz Large signal (1% THD) 500 kHz

Settling Time

	Step	Accuracy				
Device	Size	±16 LSB	±4 LSB	±2 LSB		
NI 783xR	±20.0 V	7.5 µs	10.3 μs	40 μs		
	±2.0 V	2.7 μs	4.1 μs	5.1 μs		
	±0.2 V	1.7 μs	2.9 μs	3.6 µs		
NI 784xR/	±20.0 V	2.1 μs	4.2 μs	8 µs		
785 <i>x</i> R	±2.0 V	1.3 µs	1.6 µs	1.8 µs		
	±0.2 V	0.8 μs	1.1 μs	1.2 μs		

Crosstalk-80 dB, DC to 100 kHz

Analog Output (NI 783xR/784xR/785xR Only)

Output Characteristics

•			
Output type		Resolution	16 bits, 1 in 65,536
	voltage output	Update time	1.0 μs
Number of channels		Maximum update rate	1 MC/o
NI 7830R	4	Maximum update rate	1 1/13/5
NI 7831R/7833R/7841R/78	42R/	Type of DAC	Enhanced R-2R
7851R/7852R/7853R/7854I	R 8		

Accuracy Information

Nominal l	Range (V)	% of Reading				Absolute Accuracy at
Positive Full Scale	Negative Full Scale	24 Hours	1 Year	Offset (μV)	Temp Drift (%/°C)	Full Scale (mV)
10.0	-10.0	0.0335	0.0351	2366	0.0005	5.88

Note: Accuracies are valid for analog output following an internal calibration. Analog output accuracies are listed for operation temperatures within ± 1 °C of internal calibration temperature and ± 10 °C of external or factory calibration temperature. Temp Drift applies only if ambient is greater than ± 10 °C of previous external calibration.

DC Transfer Characteristics

INL	. ±0.5 LSB typ, ±4.0 LSB max
DNL	.±0.5 LSB typ, ±1 LSB max
Monotonicity	. 16 bits, guaranteed
Voltage Output	
Range	.±10 V
Output coupling	.DC
Output impedance	
NI 783xR	. 1.25 Ω
NI 784xR/785xR	. 0.5 Ω
Current drive	. ±2.5 mA
Protection	. Short-circuit to ground

Dynamic Characteristics

Settling time

	Accuracy				
Step Size	±16 LSB	±4 LSB	±2 LSB		
±20.0 V	6.0 µs	6.2 µs	7.2 µs		
±2.0 V	2.2 μs	2.9 μs	3.8 µs		
±0.2 V	1.5 μs	2.6 µs	3.6 µs		

Slew rate	10 V/μs
Noise	150 μV_{rms} , DC to 1 MHz
Glitch energy at midscale transition	+200 mV for 3 us

Digital I/O

_
Number of channels
NI 781xR160
NI 7830R56
NI 7831R/7833R/7841R/7842R/
7851R/7852R/7853R/7854R96
CompatibilityTTL

Digital logic levels

Level	Min	Max
Input low voltage (V_{IL})	0.0 V	0.8 V
Input high voltage (V_{IH})	2.0 V	5.5 V
Output low voltage (V_{OL}), where $I_{OUT} = -4$ mA	0 V	0.4 V
Output high voltage (V_{OH}), where $I_{OUT} = 4 \text{ mA}$	2.4 V	3.3 V

Output current

Output current		
Source4.0 mA		
Sink4.0 mA		
Input leakage current±10 µA		
Power-on stateProgrammable, by line		
Protection		
Input		
NI 781xR/783xR0.5 to 7.0 V, single line		
NI $784xR/785xR$ -20.0 to 20.0 V, single line		
OutputShort-circuit		
(up to eight lines may be		
shorted at a time)		
Minimum pulse width		
Input25 ns		
Output12.5 ns		

Reconfigurable FPGA

NI 7811R/7830R/7831R

FPGA type	Virtex-II V1000
Number of flip-flops	10,240
Number of 4-input LUTs	10,240
Number of 18×18 multipliers	. 40
Embedded block RAM	720 kbits
Embedded block RAM	720 kbits

NI 7813R/7833R

FPGA type	. Virtex-II V3000
Number of flip-flops	. 28,672
Number of 4-input LUTs	. 28,672
Number of 18×18 multipliers	. 96
Embedded block RAM	. 1,728 kbits

NI 7841R/7851R

FPGA type	Virtex-5 LX30
Number of flip-flops	19,200
Number of 6-input LUTs	19,200

slices $(25 \times 18 \text{ multipliers})$ 32	
Embedded block RAM1,152 kbits	

NI 7842R/7852R

FPGA type	Virtex-5 LX50
Number of flip-flops	28,800
Number of 6-input LUTs	28,800
Number of DSP48	
slices (25 × 18 multipliers)	48

Embedded block RAM......1,728 kbits

NI 7853R

FPGA type	. Virtex-5 LX85
Number of flip-flops	. 51,840
Number of 6-input LUTs	. 51,840
Number of DSP48	
slices $(25 \times 18 \text{ multipliers})$. 48
Embedded block RAM	. 3,456 kbits

NI 7854R

- 1	
Number of flip-flops	69,120
Number of 6-input LUTs	69,120
Number of DSP48	
slices $(25 \times 18 \text{ multipliers})$	64
Embedded block RAM	4,608 kbits

FPGA type......Virtex-5 LX110

Minimum sampling period5 ns

Timebase reference sources	Bus Inter
NI PCI-781xR/783xR Onboard clock only	PCI/PCIe/PXI
NI PCIe-784xR/785xR Onboard clock only	D-4- 4
NI PXI-78xxROnboard clock,	Data transfers
phase-locked to PXI	
10 MHz clock	Number of DN
Timebase accuracy,	Power Re
onboard clock±100 ppm, 250 ps peak-to-peak jitter	+5 VDC (±5%
1 1 7	+5 VDC (±5% NI 781xR
Phase locked to PXI 10 MHz	NI 7813R NI 7830R/7
Clock (NI PXI-78xxR only)Adds 350 ps peak-to-peak jitter	NI 7833R
	NI PXI-784
Additional frequency-dependent peak-to-peak jitter	NI PXI-784
NI 781xR/783xR	NI 7853R
40 MHzNone	NI 7854R
80 MHz400 ps 120 MHz720 ps	
160 MHz710 ps	+3.3 VDC (±5
200 MHz700 ps	NI 7811R
NI 784xR/785xR	NI 7813R
40 MHzNone	NI 7830R/7
80 MHz460 ps	NI 7833R NI PCIe-78
120 MHz172 ps	NI PCIe-78 NI PCIe-78
160 MHz172 ps	NI PCIE-78
200 MHz 152 ps	NI PXI-784 NI PXI-784
200 MAZZ	NI 7853R
Calibration (NI 783xR/784xR/785xR Only)	NI 7854R
Recommended warm-up time 15 minutes	
Calibration interval 1 year	+12 V
•	NI 784 <i>x</i> R/7
Onboard calibration reference	−12 V
DC level	NI PXI-784
in Flash memory)	+5V terminal
Temperature coefficient±5 ppm/°C max	Connector (
Long-term stability $\pm 20 \text{ ppm}/\sqrt{1,000 \text{ h}}$	Connector



Note Refer to Calibration Certificates at ni.com/calibration to generate a calibration certificate for the NI 78xxR.

face

PCI/PCIe/PXI	Master, slave
Data transfers	DMA, interrupts,
	programmed I/O
Number of DMA channels	3

equirement

Power nequirement		
+5 VDC (±5%) ¹		
NI 781xR	9 mA typ	
NI 7830R/7831R	330 mA typ	
NI 7833R	364 mA typ	
NI PXI-7841R/7851R	125 mA typ	
NI PXI-7842R/7852R	136 mA typ	
NI 7853R	460 mA typ	
NI 7854R	484 mA typ	
+3.3 VDC (±5%) ²		
NI 7811R	650 mA typ	
NI 7813R	850 mA typ	
NI 7830R/7831R	462 mA typ	
NI 7833R	727 mA typ	
NI PCIe-7841R/7851R .	847 mA typ	
NI PCIe-7842R/7852R.	984 mA typ	
NI PXI-7841R/7851R	525 mA typ	
NI PXI-7842R/7852R	604 mA typ	
NI 7853R	640 mA typ	
NI 7854R	843 mA typ	
+12 V		
NI 784xR/785xR	0.5 A	
−12 V		
NI PXI-784xR/785xR	0.25 A	
+5V terminal		
Connector 0	0.5 A max ³	
Connector 1	0.5 A max ³	
Connector 2	0.5 A max ³	
All connectors	1.5 A max ^{3, 4}	

¹ Does not include current drawn from the +5 V line on the I/O connectors.

² Does not include current sourced by the digital outputs.

³ (NI PCle-78xxR only) Total maximum terminal current for all connectors is 100 mA unless disk drive connector is attached.

 $^{^4}$ (NI 784xR/785xR only) The NI 784xR/785xR has a user-replaceable socketed fuse that opens when current exceeds the current specification. Refer to the NIR Series Multifunction RIO User Manual, available at ni.com/manuals, for information about fuse replacement.

To calculate the total current sourced by the digital outputs, use the following equation:

 \sum current sourced on channel i

Power available at I/O connectors ... 4.50 to 5.25 VDC at 1 A total, 250 mA per I/O

connector pin

Physical

Dimensions (not including connection	ctors)
NI PCI-781xR/783xR	17 cm by 11 cm
	(6.7 in. by 4.3 in.)
NI PCIe-784xR/785xR	17 cm by 11 cm
	(6.7 in. by 4.3 in.)
NI PXI-78xxR	16 cm by 10 cm
	(6.3 in. by 3.9 in.)
Weight	
NI PCI-781xR/783xR	112 g
NI PCIe-784xR/785xR	127 g
NI PXI-78xxR	152 g
I/O connectors	
NI 781xR	
	high-density VHDCI type
NI 7830R	
	high-density VHDCI type
NI 783 <i>x</i> R/784 <i>x</i> R/785 <i>x</i> R	
	high-density VHDCI type
Disk drive power connector	
(PCIe devices)	Standard ATX peripheral
	connector (not serial
	ATA)

Maximum Working Voltage (NI 783xR/784xR/785xR Only)

Maximum working voltage refers to the signal voltage plus the common-mode voltage.

Channel-to-earth ±12 V, Measurement Category I Channel-to-channel±24 V, Measurement Category I



Caution Do *not* use the NI 783xR/784xR/785xRfor connection to signals in Measurement Categories II, III, or IV.

Environmental

The NI 78xxR is intended for indoor use only.

Operating Environment

NI 781xR	0 °C to 55 °C,
	tested in accordance with IEC-60068-2-1 and
	IEC-60068-2-2.
NI 7830R. NI 7831R	

40 MHz or 80 MHz timebase.	0 °C to 55 °C,
	tested in accordance with
	IEC-60068-2-1 and
	IEC-60068-2-2.

NI PCI/PXI-7833R

40 MHz timebase	.0 °C to 55 °C,
	tested in accordance with
	IEC-60068-2-1 and
	IEC-60068-2-2.
80 MHz timebase	.0 °C to 55 °C except the
	following: 0 °C to 45 °C

following: 0 °C to 45 °C
when installed in an
NI PXI-1000/B or
NI PXI-101X,
tested in accordance with
IEC-60068-2-1 and

IEC-60068-2-2.

NI PXI-7841R/7842R/7851R/7852R/7853R/7854R

40 MHz timebase	0 °C to 55 °C,
	tested in accordance with
	IEC-60068-2-1 and
	IEC-60068-2-2.

80 MHz timebase	0 °C to 55 °C except the
	following: 0 °C to 45 °C
	when installed in an
	NI PXI-1000/B or
	NI PXI-101X,
	tested in accordance with
	IEC-60068-2-1 and

IEC-60068-2-2.

40 MHz or 80 MHz timebase	. 0 °C to 40 °C,
	tested in accordance w

with IEC-60068-2-1 and IEC-60068-2-2.

Relative humidity range...... 10% to 90%, noncondensing,

NI PCIe-7841R/7842R/7851R/7852R

tested in accordance with IEC-60068-2-56.

temperature

Storage Environment

NI PCI/PXI-781xR/783xR –20 °C to 70 °C, tested in accordance will IEC-60068-2-1 and IEC-60068-2-2.	ith
NI PCIe-784xR/785xR20 °C to 70 °C, tested in accordance will IEC-60068-2-1 and IEC-60068-2-2.	i th
NI PXI-784xR/785xR40 °C to 70 °C, tested in accordance wide IEC-60068-2-1 and IEC-60068-2-2.	i th
Ambient temperature range–20 °C to 70 °C,	

tested in accordance with

IEC-60068-2-1 and IEC-60068-2-2.

Relative humidity range......5% to 95%,

noncondensing, tested in accordance with IEC-60068-2-56.



Note Clean the device with a soft, non-metallic brush. Make sure that the device is completely dry and free from contaminants before returning it to service.

Shock and Vibration (for NI PXI-78xxR Only)

Operational shock	30 g peak, half-sine,
	11 ms pulse;
	tested in accordance with
	IEC-60068-2-27. Test
	profile developed in
	accordance with
	MIL-PRF-28800F.

Random vibration

Operating	5 Hz to 500 Hz, 0.3 g _{rms}
Nonoperating	5 Hz to 500 Hz, 2.4 g _{rms} ,
	tested in accordance with
	IEC-60068-2-64.
	Nonoperating test profile
	exceeds the requirements
	of MIL-PRF-28800F,
	Class 3.

Safety

The NI 78xxR is designed to meet the requirements of the following standards of safety for electrical equipment for measurement, control, and laboratory use:

- IEC 61010-1, EN 61010-1
- UL 61010-1, CSA 61010-1



Note For UL and other safety certifications, refer to the product label or the *Online Product Certification* section.

Electromagnetic Compatibility

The NI 78xxR is designed to meet the requirements of the following standards of EMC for electrical equipment for measurement, control, and laboratory use:

- EN 61326 (IEC 61326): Class A emissions; Basic immunity
- EN 55011 (CISPR 11): Group 1, Class A emissions
- AS/NZS CISPR 11: Group 1, Class A emissions
- FCC 47 CFR Part 15B: Class A emissions

ICES-001: Class A emissions

Note For the standards applied to assess the EMC of this product, refer to the *Online Product Certification* section.



Note For EMC compliance, operate this device with shielded cabling.

CE Compliance $\subset \in$

This product meets the essential requirements of applicable European Directives as follows:

- 2006/95/EC; Low-Voltage Directive (safety)
- 2004/108/EC; Electromagnetic Compatibility Directive (EMC)

Online Product Certification

Refer to the product Declaration of Conformity (DoC) for additional regulatory compliance information. To obtain product certifications and the DoC for this product, visit ni.com/certification, search by model number or product line, and click the appropriate link in the Certification column.

Environmental Management

NI is committed to designing and manufacturing products in an environmentally responsible manner. NI recognizes that eliminating certain hazardous substances from our products is beneficial to the environment and to NI customers.

For additional environmental information, refer to the *NI and the Environment* Web page at ni.com/environment. This page contains the environmental regulations and directives with which NI complies, as well as other environmental information not included in this document.

Waste Electrical and Electronic Equipment (WEEE)



EU Customers At the end of their life cycle, all products *must* be sent to a WEEE recycling center. For more information about WEEE recycling centers and National Instruments WEEE initiatives, visit ni.com/environment/weee.htm.

电子信息产品污染控制管理办法 (中国 RoHS)



中国客户 National Instruments 符合中国电子信息产品中限制使用某些有害物质指令 (RoHS)。关于National Instruments 中国 RoHS 合规性信息,请登录 ni.com/environment/rohs_china。(For information about China RoHS compliance, go to ni.com/environment/rohs_china.)

Device Pinouts

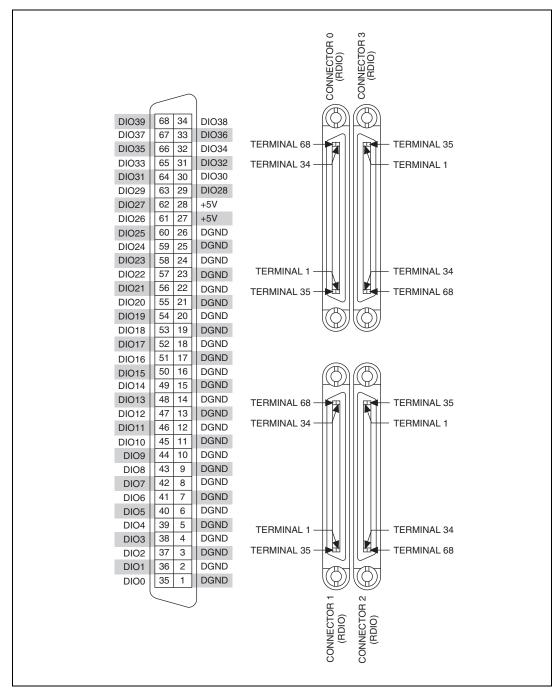


Figure 1. NI 781xR Connector Pin Assignments and Locations

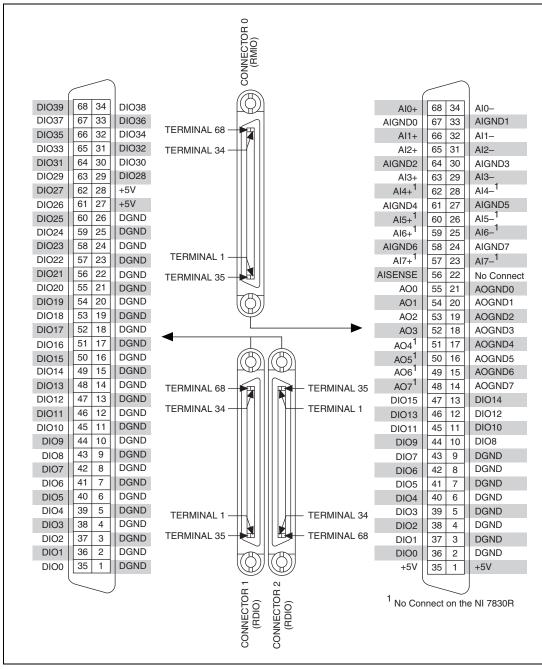


Figure 2. NI 783xR/784xR/785xR Connector Pin Assignments and Locations

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