

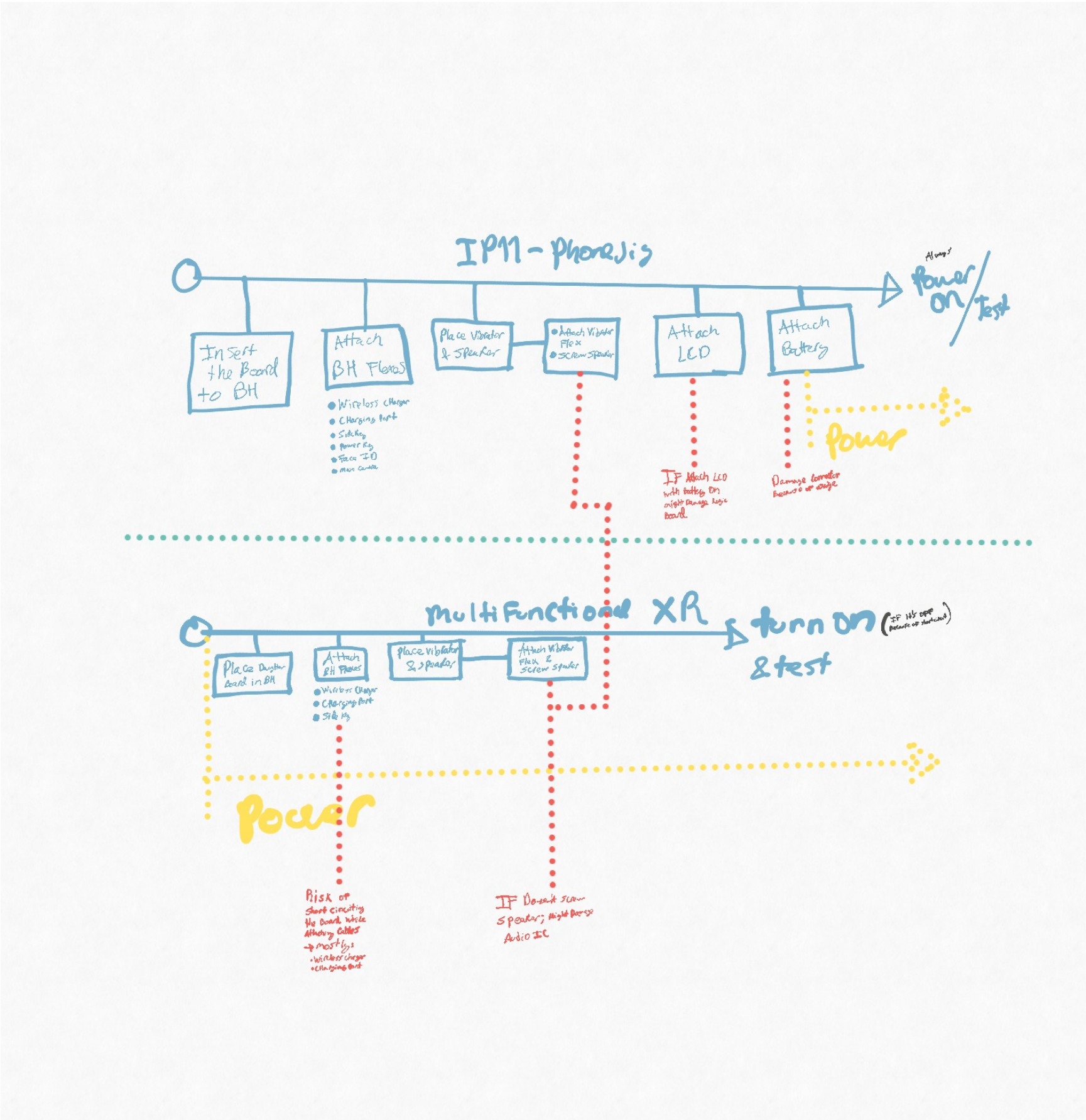
BH Update - Mar 17th

General updates

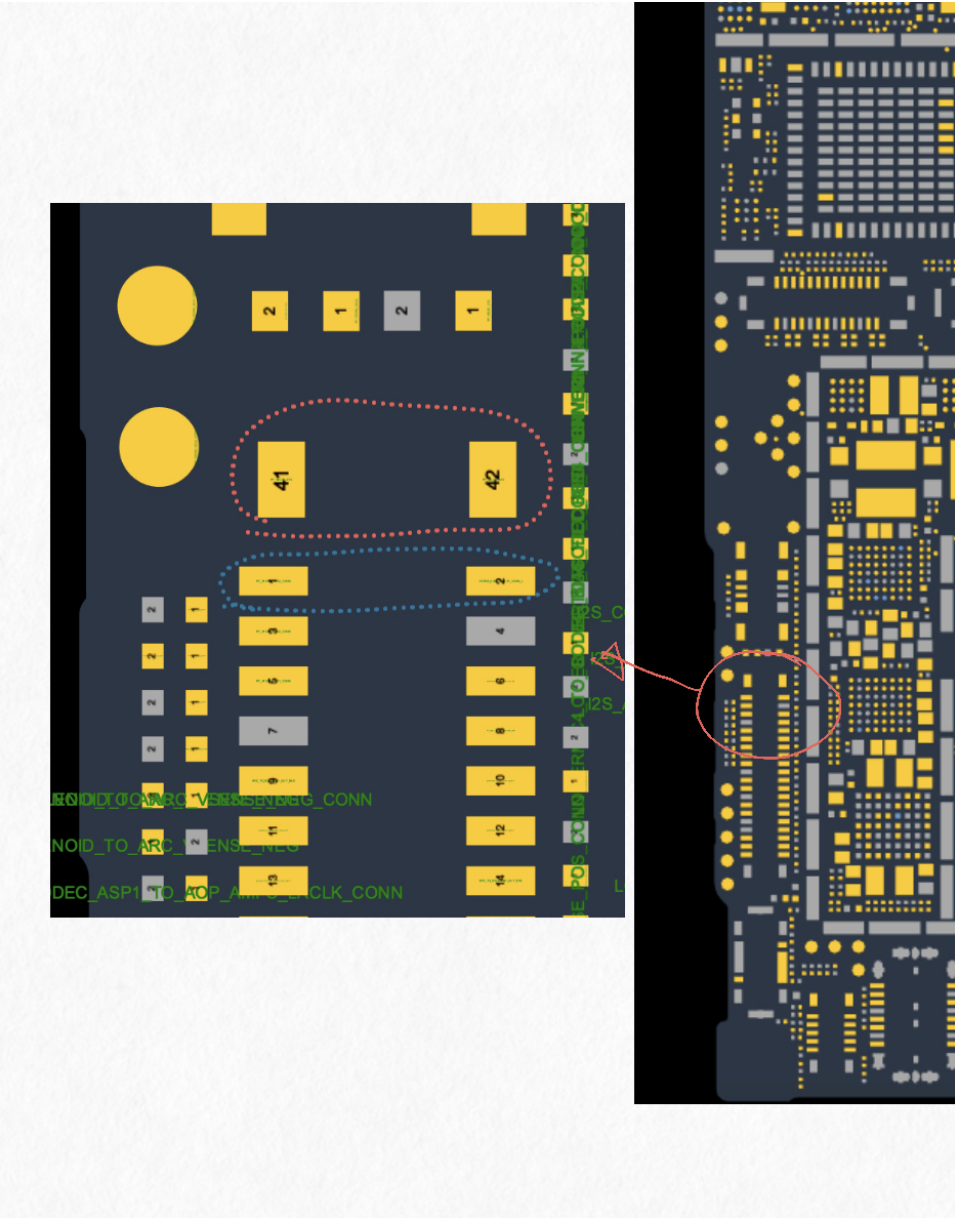
- PCBWAY
 - 3/6 PCBs are done. [XS XR 8P]
 - The other 3 are [IP8=86%] [IP11=73%] [IPX=73%]

BH Report

As requested I draw the flowchart of the process and the potential risks while assembling the back-housing. We gonna look over this tomorrow at 8:30am



We went to the schematics to see what are the pins that could be making the short-circuit, we conclude its a power pin (VBUS) and a data pin, any of them when it goes low for the XR



Draw some ideas for improvements for the jigs or part of the jigs. Even Add-ons.

