

# BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

The SN54/74LS247 thru SN54/74LS249 are BCD-to-Seven-Segment Decoder/Drivers.

The LS247 and LS248 are functionally and electrically identical to the LS47 and LS48 with the same pinout configuration. The LS249 is a 16-pin version of the 14-pin LS49 and includes full functional capability for lamp test and ripple blanking which was not available in the LS49.

The composition of all characters, except the 6 and 9 are identical between the LS247, 248, 249 and the LS47, 48 and 49. The LS47 thru 49 compose the  $\Box$  and  $\Box$  without tails, the LS247 thru 249 compose the  $\Box$  and  $\Box$  with the tails. The LS247 has active-low outputs for direct drive of indicators. The LS248 and 249 have active-high outputs for driving lamp buffers.

All types feature a lamp test input and have full ripple-blanking input/output controls. On all types an automatic leading and/or trailing-edge zero-blanking control (RBI and RBO) is incorporated and an overriding blanking input (BI) is contained which may be used to control the lamp intensity by pulsing or to inhibit the output's lamp test may be performed at any time when the BI/RBO node is at high level. Segment identification and resultant displays are shown below. Display pattern for BCD input counts above 9 are unique symbols to authenticate input conditions.

#### LS247

- Open-Collector Outputs Drive Indicators Directly
- Lamp-Test Provision
- Leading/Trailing Zero Suppression

### LS248

- Internal Pull-Ups Eliminate Need for External Resistors
- Lamp-Test Provision
- Leading/Trailing Zero Suppression

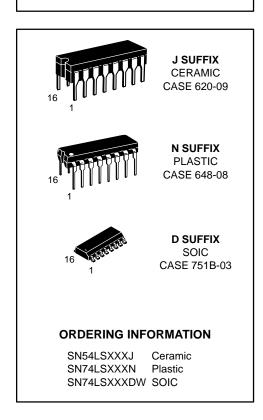
#### 1 5249

- Open-Collector Outputs
- Lamp-Test Provision
- Leading/Trailing Zero Suppression

## SN54/74LS247 SN54/74LS248 SN54/74LS249

# BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

LOW POWER SCHOTTKY



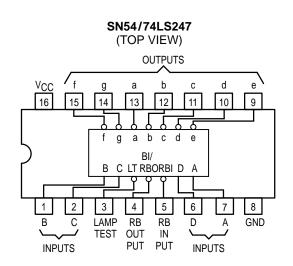


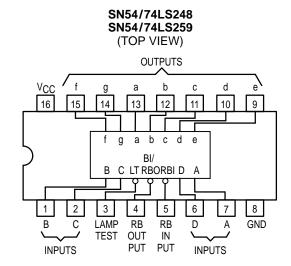
#### NUMERICAL DESIGNATIONS AND RESULTANT DISPLAYS



SEGMENT IDENTIFICATION

## SN54/74LS247 • SN54/74LS248 • SN54/74LS249

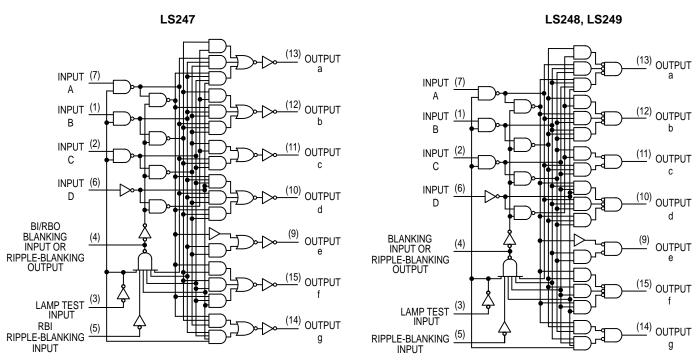




#### ALL CIRCUIT TYPES FEATURE LAMP INTENSITY MODULATION CAPABILITY

		DRIVER OUTF	PUTS		TYPICAL
TYPE	ACTIVE	OUTPUT	SINK	MAX	POWER
	LEVEL	CONFIGURATION	CURRENT	VOLTAGE	DISSIPATION
SN54LS247	low	open-collector	12 mA	15 V	35 mW
SN54LS248	high	2.0 kΩ pull-up	2.0 mA	5.5 V	125 mW
SN54LS249	high	open-collector	4.0 mA	5.5 V	40 mW
SN74LS247	low	open-collector	24 mA	15 V	35 mW
SN74LS248	high	2.0 kΩ pull-up	6.0 mA	5.5 V	125 mW
SN74LS249	high	open-collector	8.0 mA	5.5 V	40 mW

#### **LOGIC DIAGRAM**



## SN54/74LS247 • SN54/74LS248 • SN54/74LS249

### LS247 FUNCTION TABLE

DECIMAL OR			INP	UTS			BI/RBO <sup>†</sup>			C	UTPUT	s			NOTE
FUNCTION	Ľ	RBI	D	С	В	Α	DI/KBO	а	b	С	d	е	f	g	NOTE
0	Н	Н	L	L	L	L	Н	ON	ON	ON	ON	ON	ON	OFF	
1	Н	Х	L	L	L	Н	Н	OFF	ON	ON	OFF	OFF	OFF	OFF	
2	Н	Х	L	L	Н	L	Н	ON	ON	OFF	ON	ON	OFF	ON	
3	Н	Х	L	L	Н	Н	Н	ON	ON	ON	ON	OFF	OFF	ON	
4	Н	Х	L	Н	L	L	Н	OFF	ON	ON	OFF	OFF	ON	ON	
5	Н	Х	L	Н	L	Н	Н	ON	OFF	ON	ON	OFF	ON	ON	
6	Н	Х	L	Н	Н	L	Н	ON	OFF	ON	ON	ON	ON	ON	
7	Н	Х	L	Н	Н	Н	Н	ON	ON	ON	OFF	OFF	OFF	OFF	1
8	Н	Х	Н	L	L	L	Н	ON	ON	ON	ON	ON	ON	ON	
9	Н	Х	Н	L	L	Н	Н	ON	ON	ON	ON	OFF	ON	ON	
10	Н	Х	Н	L	Н	L	Н	OFF	OFF	OFF	ON	ON	OFF	ON	
11	Н	Х	Η	L	Н	Н	Н	OFF	OFF	ON	ON	OFF	OFF	ON	
12	Н	Х	Н	Н	L	L	Н	OFF	ON	OFF	OFF	OFF	ON	ON	
13	Н	Х	Н	Н	L	Н	Н	ON	OFF	OFF	ON	OFF	ON	ON	
14	Н	Х	Н	Н	Н	L	Н	OFF	OFF	OFF	ON	ON	ON	ON	
15	Н	Х	Η	Н	Н	Н	Н	OFF	OFF	OFF	OFF	OFF	OFF	OFF	
BI	Х	Х	Х	Х	Х	Х	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	2
RBI	Н	L	L	L	L	L	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	3
LT	L	Χ	Χ	Χ	Χ	Χ	Н	ON	ON	ON	ON	ON	ON	ON	4

### LS248, LS249 FUNCTION TABLE

DECIMAL OR			INP	UTS			BI/RBO <sup>†</sup>			O	UTPUT	S			NOTE
FUNCTION	Ľ	RBI	D	С	В	Α	BI/KBO	а	b	С	d	е	f	g	NOTE
0	Н	Н	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	1
1	Н	Х	L	L	L	Н	Н	L	Н	Н	L	L	L	L	1
2	Н	Х	L	L	Н	L	Н	Н	Н	L	Н	Н	L	Н	
3	Н	Х	L	L	Н	Н	Н	Н	Н	Н	Н	L	L	Н	
4	Н	Х	L	Н	L	L	Н	L	Н	Н	L	L	Н	Н	
5	Н	Х	L	Н	L	Н	Н	Н	L	Н	Н	L	Н	Н	
6	Н	Х	L	Н	Н	L	Н	Н	L	Н	Н	Н	Н	Н	
7	Н	Х	L	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	1
8	Н	Х	Н	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	
9	Н	Х	Н	L	L	Н	Н	Н	Н	Н	Н	L	Н	Н	
10	Н	Х	Н	L	Н	L	Н	L	L	L	Н	Н	L	Н	
11	Η	Х	Ι	L	Н	Н	Н	L	L	Н	Η	┙	L	Η	
12	Н	Х	Н	Н	L	L	Н	L	Н	L	L	L	Н	Н	
13	Н	Х	Н	Н	L	Н	Н	Н	L	L	Н	L	Н	Н	
14	Н	Х	Н	Н	Н	L	Н	L	L	L	Н	Н	Н	Н	
15	Н	Х	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	
BI	Х	Х	Х	Х	Х	Х	L	L	L	L	L	L	L	L	2
RBI	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	3
LT	L	Χ	Χ	Χ	Х	Χ	Н	Н	Н	Н	Н	Н	Н	Н	4

H = HIGH Level, L = LOW Level, X = Irrelevant

NOTES: 1. The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The ripple-blanking input (RBI) must be open or high if blanking of a decimal zero is not desired.

- 2. When a low logic level is applied directly to the blanking input (BI), all segment outputs are off regardless of the level of any other input.
- When ripple-blanking input (RBI) and inputs A, B, C, and D are at a low level with the lamp test input high, all segment outputs go off and the ripple-blanking output (RBO) goes to a low level (response condition).
- 4. When the blanking input/ripple blanking output (BI/RBO) is open or held high and a low is applied to the lamp-test input, all segment outputs are on. †BI/RBO is wire-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).

## SN54/74LS247

## **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
ЮН	Output Current — High BI/RBO	54, 74			-50	μΑ
lOL	Output Current — Low BI/RBO	54 74			1.6 3.2	mA
V <sub>O(off)</sub>	Off-State Output Voltage a-g	54, 74			15	V
IO(on)	On-State Output Current a-g a-g	54 74			12 24	mA

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

				Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Tes	st Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input All Inputs	HIGH Voltage for	
\/··	Input LOW Voltage	54			0.7	V	Guaranteed Input	LOW Voltage for	
V <sub>IL</sub>	input LOVV Voltage	74			0.8	]	All Inputs		
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> =	-18 mA	
V	Output HIGH Voltage	54	2.4	4.2		V	V <sub>CC</sub> = MIN, I <sub>OH</sub>	= MAX, V <sub>IN</sub> = V <sub>IH</sub>	
VOH	BI/RBO	74	2.4	4.2		V	or V <sub>IL</sub> per Truth T	able	
Mari	Output LOW Voltage	54, 74		0.25	0.4	V	I <sub>OL</sub> = 1.6 mA	V <sub>CC</sub> = V <sub>CC</sub> MIN, V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	
VOL	BI/RBO	74		0.35	0.5	V	I <sub>OL</sub> = 3.2 mA	per Truth Table	
IO(off)	Off-State Output Current a-g	54, 74			250	μА	$V_{CC} = MAX, V_{IH}$ $V_{O(off)} = 15 V, V_{I}$		
V-, ,	On-State Output Voltage	54, 74		0.25	0.4	V	$I_{O(on)} = 12 \text{ mA}$	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2.0 V,	
V <sub>O(on)</sub>	a-g	74		0.35	0.5	V	$I_{O(on)} = 24 \text{ mA}$	V <sub>IL</sub> per Truth Table	
I	Innut IIICI I Current				20	μΑ	$V_{CC} = MAX, V_{IN}$	= 2.7 V	
lН	Input HIGH Current				0.1	mA	$V_{CC} = MAX, V_{IN}$	= 7.0 V	
I <sub>IL</sub>	Input LOW Current Any Input, except BI/RB0	)			-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.4 V		
	BI/RBO				-1.2				
los	Short Circuit Current BI/RBO (Note 1)		-0.3		-2.0	mA	V <sub>CC</sub> = MAX		
Icc	Power Supply Current			7.0	13	mA	V <sub>CC</sub> = MAX		

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

## AC CHARACTERISTICS ( $V_{CC} = 5.0 \text{ V}, T_A = 25^{\circ}\text{C}$ )

			Limits	Limits		
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
t <sub>PLH</sub> t <sub>PHL</sub>	Turn-Off Time from A Input Turn-On Time from A Input			100 100	ns	C <sub>L</sub> = 15 pF,
t <sub>PHL</sub> t <sub>PLH</sub>	Turn-Off Time from RBI Input Turn-On Time from RBI Input			100 100	ns	$R_L = 665 \Omega$

## SN54/74LS248

## **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
loh	Output Current — High BI/RBO	54, 74			-50	μΑ
	a-g	54, 74			-100	1
I <sub>OL</sub>	Output Current — Low BI/RBO BI/RBO	54 74			1.6 3.2	mA
	a-g a-g	54 74			2.0 6.0	

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

				Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Tes	st Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input All Inputs	t HIGH Voltage for	
\/	Input LOW Voltage	54			0.7	V	Guaranteed Input LOW Voltage for		
VIL	Input LOW Voltage	74			0.8	\ \	All Inputs		
$V_{IK}$	Input Clamp Diode Voltage			-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> =	: –18 mA	
\/a	Output HIGH Voltage	54	2.4	4.2		V	V <sub>CC</sub> = MIN, I <sub>OH</sub>	= MAX, V <sub>IN</sub> = V <sub>IH</sub>	
VOH	a-g and BI/RBO	74	2.4	4.2		V	or V <sub>IL</sub> per Truth T	āble	
ЮН	Output Current a-g	54, 74	-1.3	-2.0		mA	$V_{CC}$ = MIN, $V_{O}$ = 0.85 V, Input Conditions as for $V_{OH}$		
	Output LOW Voltage a-g	54, 74		0.25	0.4	V	I <sub>OL</sub> = 2.0 mA		
VOL		74		0.35	0.5	1	I <sub>OL</sub> = 6.0 mA	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2.0 V, V <sub>II</sub> = per Truth Table	
	BI/RBO	54, 74		0.25	0.4	V	I <sub>OL</sub> = 1.6 mA	VIL = per Trutti Table	
		74		0.35	0.5	]	I <sub>OL</sub> = 3.2 mA	]	
1	Input HIGH Current				20	μΑ	V <sub>CC</sub> = MAX, V <sub>IN</sub>	= 2.7 V	
lіН	Any Input, except BI/RBO				0.1	mA	$V_{CC} = MAX, V_{IN}$	= 7.0 V	
lir.	Input LOW Current Any Input, except BI/RBO				-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub>	= 0.4 V	
	BI/RBO				-1.2				
los	Short Circuit Current BI/RBO (Note 1)	_	-0.3		-2.0	mA	V <sub>CC</sub> = MAX		
Icc	Power Supply Current			25	38	mA	V <sub>CC</sub> = MAX		

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

## AC CHARACTERISTICS ( $V_{CC} = 5.0 \text{ V}, T_A = 25^{\circ}\text{C}$ )

		Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay Time, High-to-Low-Level Output from A Input Propagation Delay Time, Low-to-High-Level Output from A Input			100 100	ns	$C_L = 15 \text{ pF}$ $R_L = 4.0 \text{ k}\Omega$
tPHL tPLH	Propagation Delay Time, High-to-Low-Level Output from RBI Input Propagation Delay Time, Low-to-High-Level Output from RBI Input			100 100	ns	$C_L = 15 \text{ pF}$ $R_L = 6.0 \text{ k}\Omega$

## SN54/74LS249

## **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
TA	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
ЮН	Output Current — High BI/RBO	54, 74			-50	μΑ
lOL	Output Current — Low BI/RBO BI/RBO	54 74			1.6 3.2	mA
VOH	Output Voltage — High a-g	54, 74			5.5	V
lOL	Output Current — Low a-g a-g	54 74			4.0 8.0	mA

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

				Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Tes	t Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input All Inputs	HIGH Voltage for	
V	Input LOW Voltage	54			0.7	V	Guaranteed Input	LOW Voltage for	
V <sub>IL</sub>	Input LOW Voltage	74			0.8	]	All Inputs		
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	$V_{CC} = MIN, I_{IN} =$	–18 mA	
Vari	Output HIGH Voltage	54	2.4	4.2		V	V <sub>CC</sub> = MIN, I <sub>OH</sub> :	= MAX, V <sub>IN</sub> = V <sub>IH</sub>	
VOH	BI/RBO	74	2.4	4.2		V	or V <sub>IL</sub> per Truth Ta	able	
<sup>I</sup> ОН	Output HIGH Current a-g	54, 74			250	μΑ	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2.0 V, V <sub>OH</sub> = 5.5 V, V <sub>IL</sub> = MAX		
	Output LOW Voltage BI/RBO	54, 74		0.25	0.4	V	I <sub>OL</sub> = 1.6 mA		
VOL		74		0.35	0.5	1	I <sub>OL</sub> = 3.2 mA	$V_{CC} = MIN, V_{IH} = 2.0 V,$	
	a-g	54, 74		0.25	0.4	V	I <sub>OL</sub> = 4.0 mA	$V_{IL}$ = per Truth Table	
		74		0.35	0.5	] '	I <sub>OL</sub> = 8.0 mA	1	
l	Input HIGH Current				20	μΑ	$V_{CC} = MAX, V_{IN}$	= 2.7 V	
lΗ	Any Input, except BI/RB0	)			0.1	mA	$V_{CC} = MAX, V_{IN}$	= 7.0 V	
I <sub>IL</sub>	Input LOW Current Any Input, except BI/RB0	)			-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub>	= 0.4 V	
	BI/RBO				-1.2				
los	Short Circuit Current BI/RBO (Note 1)		-0.3		-2.0	mA	V <sub>CC</sub> = MAX		
ICC	Power Supply Current			8.0	15	mA	V <sub>CC</sub> = MAX		

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

## AC CHARACTERISTICS ( $V_{CC} = 5.0 \text{ V}, T_A = 25^{\circ}\text{C}$ )

		Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
<sup>t</sup> PHL <sup>t</sup> PLH	Propagation Delay Time, High-to-Low-Level Output from A Input Propagation Delay Time, Low-to-High-Level Output from A Input			100 100	ns	$C_L = 15 \text{ pF, } R_L = 2.0 \Omega$
tPHL tPLH	Propagation Delay Time, High-to-Low-Level Output from RBI Input Propagation Delay Time, Low-to-High-Level Output from RBI Input			100 100	ns	$C_L = 15 \text{ pF}, R_L = 6.0 \Omega$