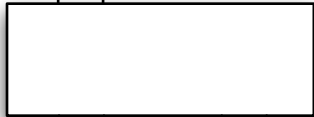


PredMemOp (Vector)



SuccMemOp
(Vector)

Out
(Vector)

Machine Logic:

PredMemOp.fire().andR == true)
: Do action

SuccMemOp.fire().andR && Out.fire().andR == true
: Reset

SuccMemOp[i].fire() == true
latch ready and activate sink

Out[i].fire() == true
latch ready and activate sink