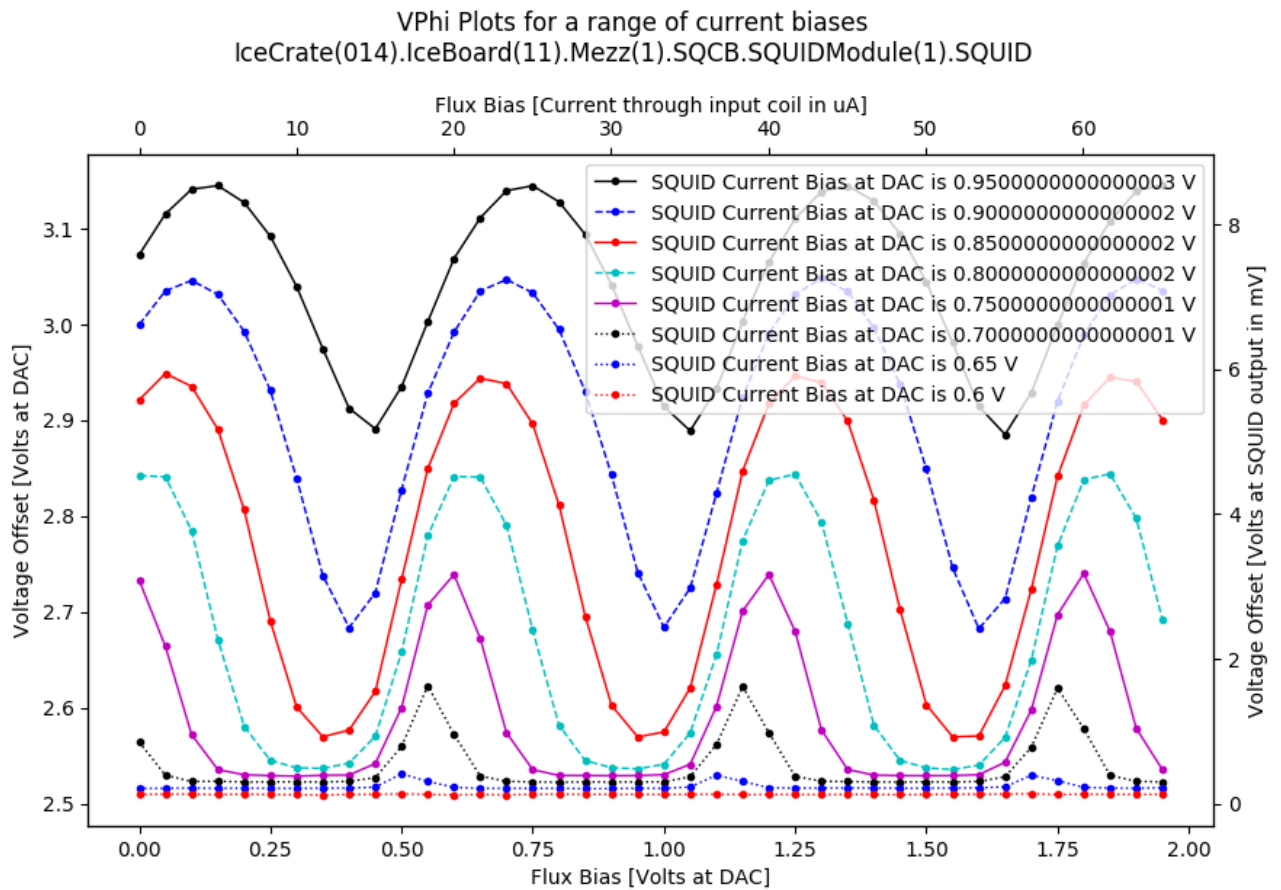# SQUID Tuning Algorithm Output
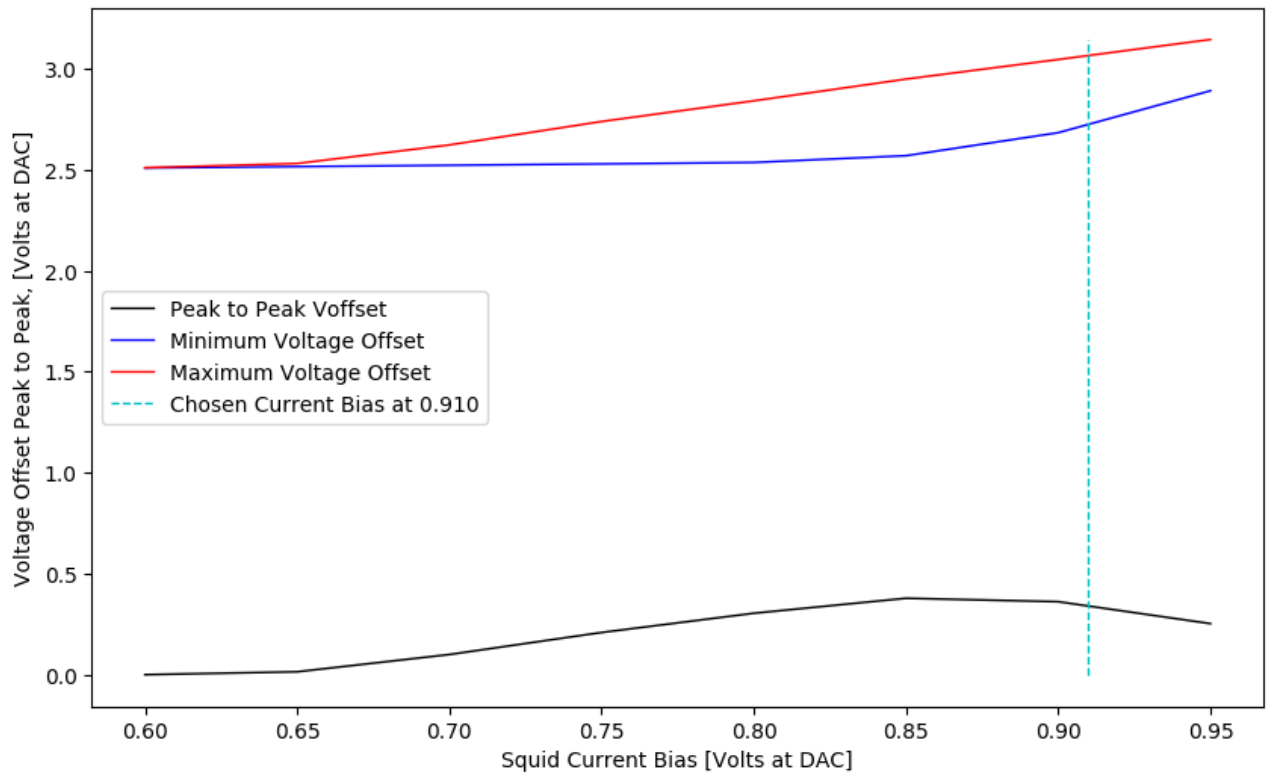
Target Data

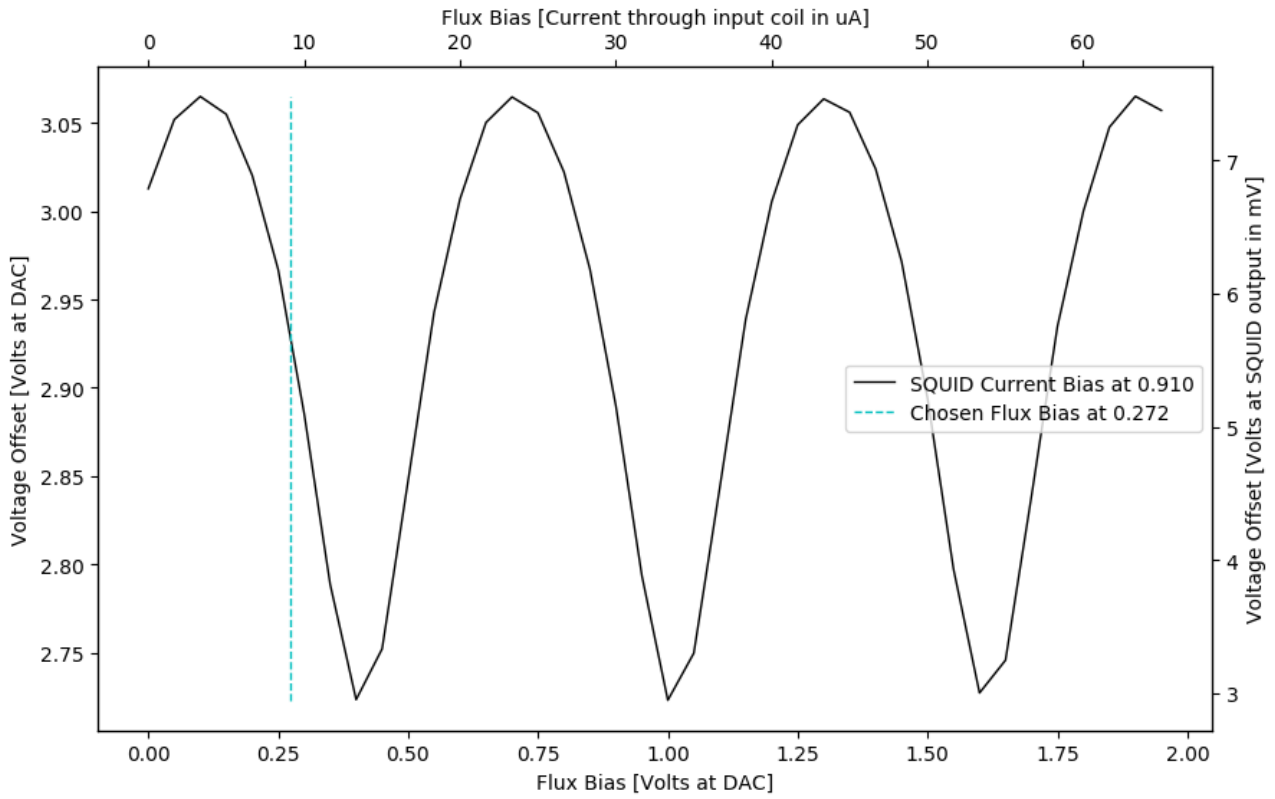|	
Full target name	IceCrate(014).IceBoard(11).Mezz(1).SQCB.SQUID Module(1).SQUID
HWM Name	PB2 HWM
HWM Path	/home/polarbear/hardware_maps/polarbear/PB2b/commissioning/202305_crate_test/crate_hwm_202310_shift_disable_bad_sq_v2.yaml

Plots

Current Bias Optimization (Voltage Offset Peak-to-Peak vs Current Bias Voltage)
IceCrate(014).IceBoard(11).Mezz(1).SQCB.SQUIDModule(1).SQUID

Flux Bias Optimization (For a single V-Phi curve at chosen current bias)
IceCrate(014).IceBoard(11).Mezz(1).SQCB.SQUIDModule(1).SQUID
Transimpedance: 64.224596 Ohms

Additional Data

Measured Transimpedance	64.22 Ohms
Chosen Flux Bias	0.27 Volts
Chosen Current Bias	0.91 Volts
Peak to Peak at Chosen Current Bias	0.34 Volts
Reference Current Bias Used	Unavailable

NOTE:

The Flux Bias and Current Bias are given as Voltages at the output of the Flux Bias and Current Bias DACs