

# SQUID Tuning Algorithm Output

## Target Data

|                  |  |
|------------------|--|
| Full target name | IceCrate(017).IceBoard(5).Mezz(2).SQCB.SQUID Module(3).SQUID   |
| HWM Name         | PB2 HWM  |
| HWM Path         | /home/polarbear/hardware_maps/polarbear/PB2b/commissioning/202305_crate_test/crate_hwm_202310_shift_disable_bad_sq_v2.yaml |

## Plots

## Additional Data

|                                     |             |
|-------------------------------------|-------------|
| Measured Transimpedance             | Unavailable |
| Chosen Flux Bias                    | Unavailable |
| Chosen Current Bias                 | Unavailable |
| Peak to Peak at Chosen Current Bias | Unavailable |
| Reference Current Bias Used         | Unavailable |

### NOTE:

The Flux Bias and Current Bias are given as Voltages at the output of the Flux Bias and Current Bias DACs