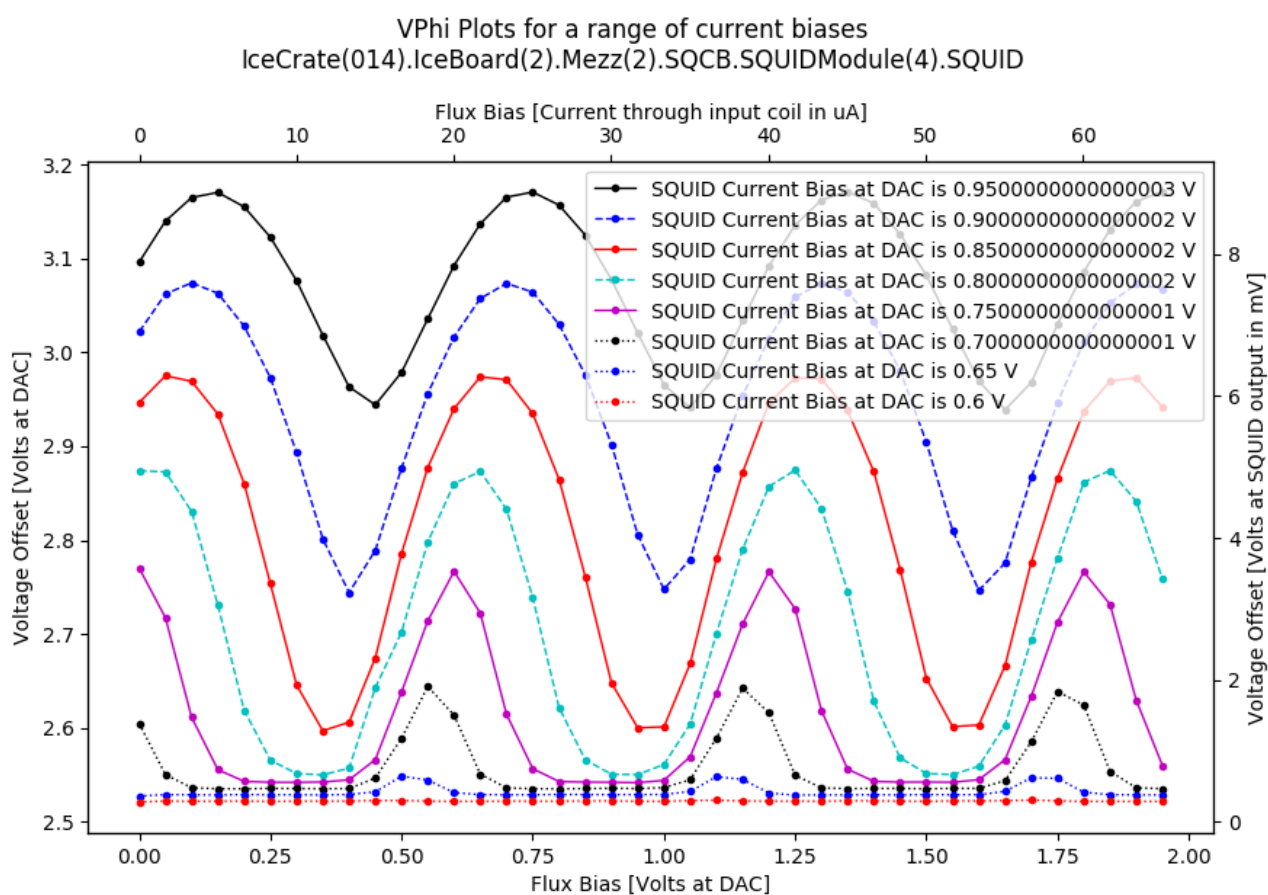


# SQUID Tuning Algorithm Output

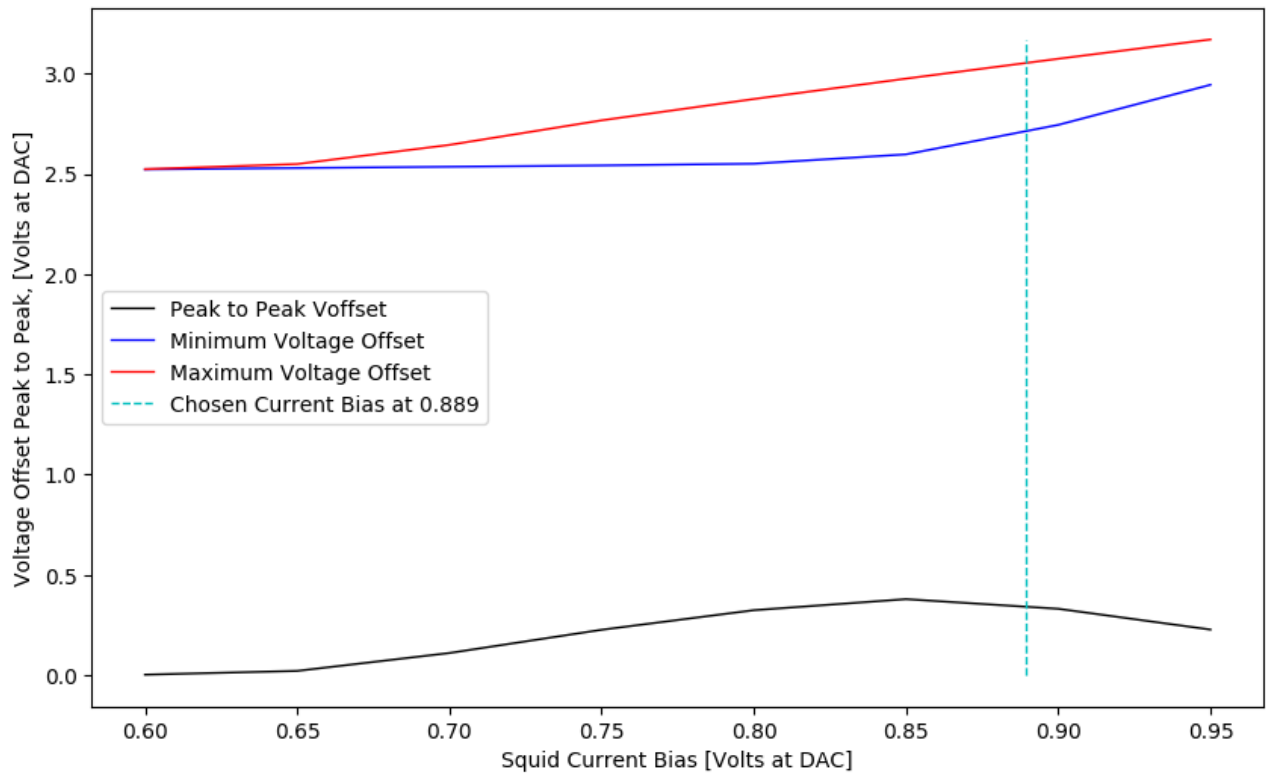
## Target Data

|                  |  |
|------------------|--|
| Full target name | IceCrate(014).IceBoard(2).Mezz(2).SQCB.SQUID Module(4).SQUID   |
| HWM Name         | PB2 HWM  |
| HWM Path         | /home/polarbear/hardware_maps/polarbear/PB2b/c ommissioning/202305_crate_test/crate_hwm_2023 10_shift_disable_bad_sq_v2.yaml |

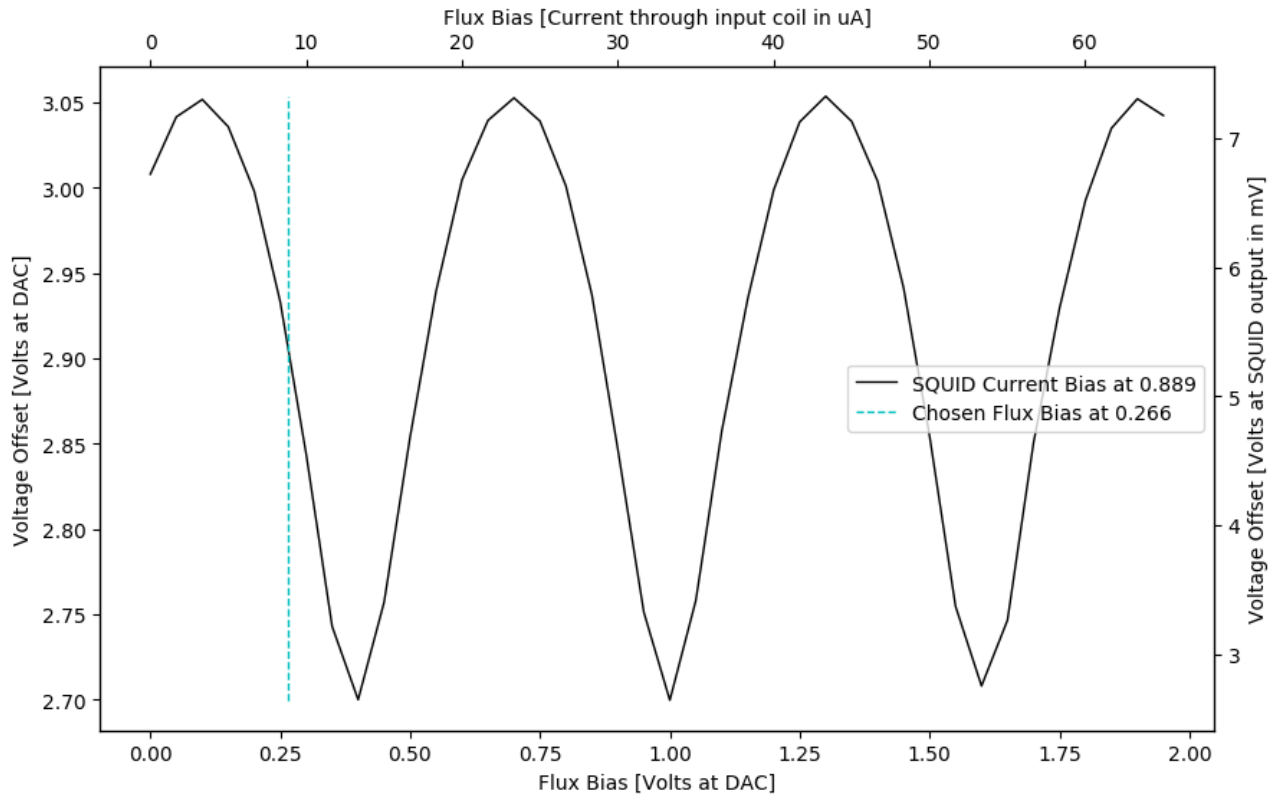
## Plots



Current Bias Optimization (Voltage Offset Peak-to-Peak vs Current Bias Voltage)  
IceCrate(014).IceBoard(2).Mezz(2).SQCB.SQUIDModule(4).SQUID



Flux Bias Optimization (For a single V-Phi curve at chosen current bias)  
IceCrate(014).IceBoard(2).Mezz(2).SQCB.SQUIDModule(4).SQUID  
Transimpedance: 710.125745 Ohms



## Additional Data

|                                     |             |
|-------------------------------------|-------------|
| Measured Transimpedance             | 710.13 Ohms |
| Chosen Flux Bias                    | 0.27 Volts  |
| Chosen Current Bias                 | 0.89 Volts  |
| Peak to Peak at Chosen Current Bias | 0.34 Volts  |
| Reference Current Bias Used         | Unavailable |

### NOTE:

The Flux Bias and Current Bias are given as Voltages at the output of the Flux Bias and Current Bias DACs