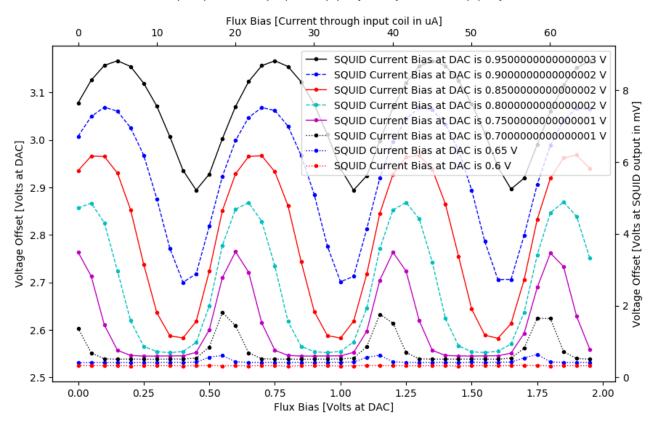
# **SQUID Tuning Algorithm Output**

### Target Data

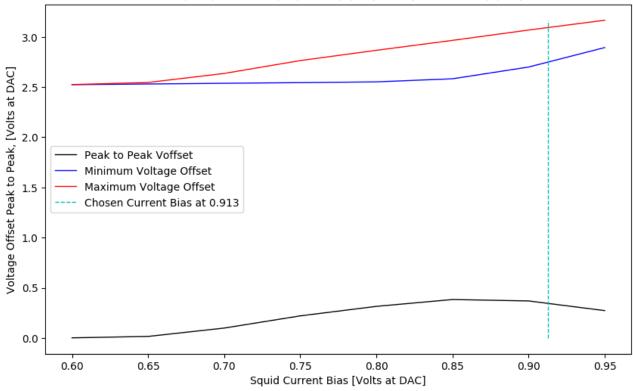
| Full target name | IceCrate(014).IceBoard(12).Mezz(1).SQCB.SQUID Module(4).SQUID   |
|------------------|---|
| HWM Name         | PB2 HWM   |
| HWM Path         | /home/polarbear/hardware_maps/polarbear/PB2b/c ommissioning/202305_crate_test/crate_hwm_2023 10_shift_disable_bad_sq.yaml |

## Plots

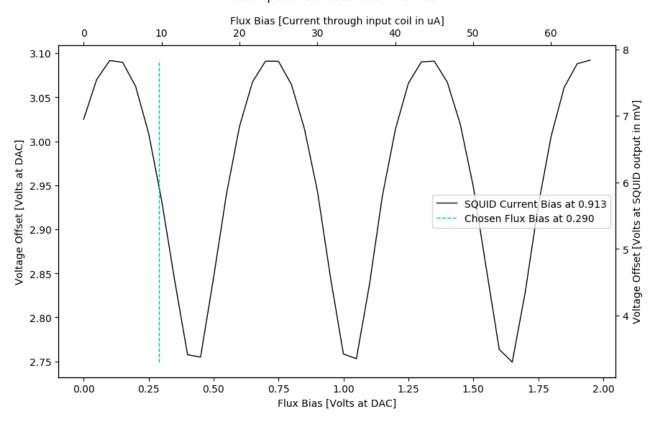
# VPhi Plots for a range of current biases lceCrate(014).lceBoard(12).Mezz(1).SQCB.SQUIDModule(4).SQUID



Current Bias Optimization (Voltage Offset Peak-to-Peak vs Current Bias Voltage) IceCrate(014).IceBoard(12).Mezz(1).SQCB.SQUIDModule(4).SQUID



# Flux Bias Optimization (For a single V-Phi curve at chosen current bias) IceCrate(014).IceBoard(12).Mezz(1).SQCB.SQUIDModule(4).SQUID Transimpedance: 652.426546 Ohms



### Additional Data

| Measured Transimpedance             | 652.43 Ohms |
|-------------------------------------|-------------|
| Chosen Flux Bias                    | 0.29 Volts  |
| Chosen Current Bias                 | 0.91 Volts  |
| Peak to Peak at Chosen Current Bias | 0.34 Volts  |
| Reference Current Bias Used         | Unavailable |

#### NOTE:

The Flux Bias and Current Bias are given as Voltages at the output of the Flux Bias and Current Bias DACs