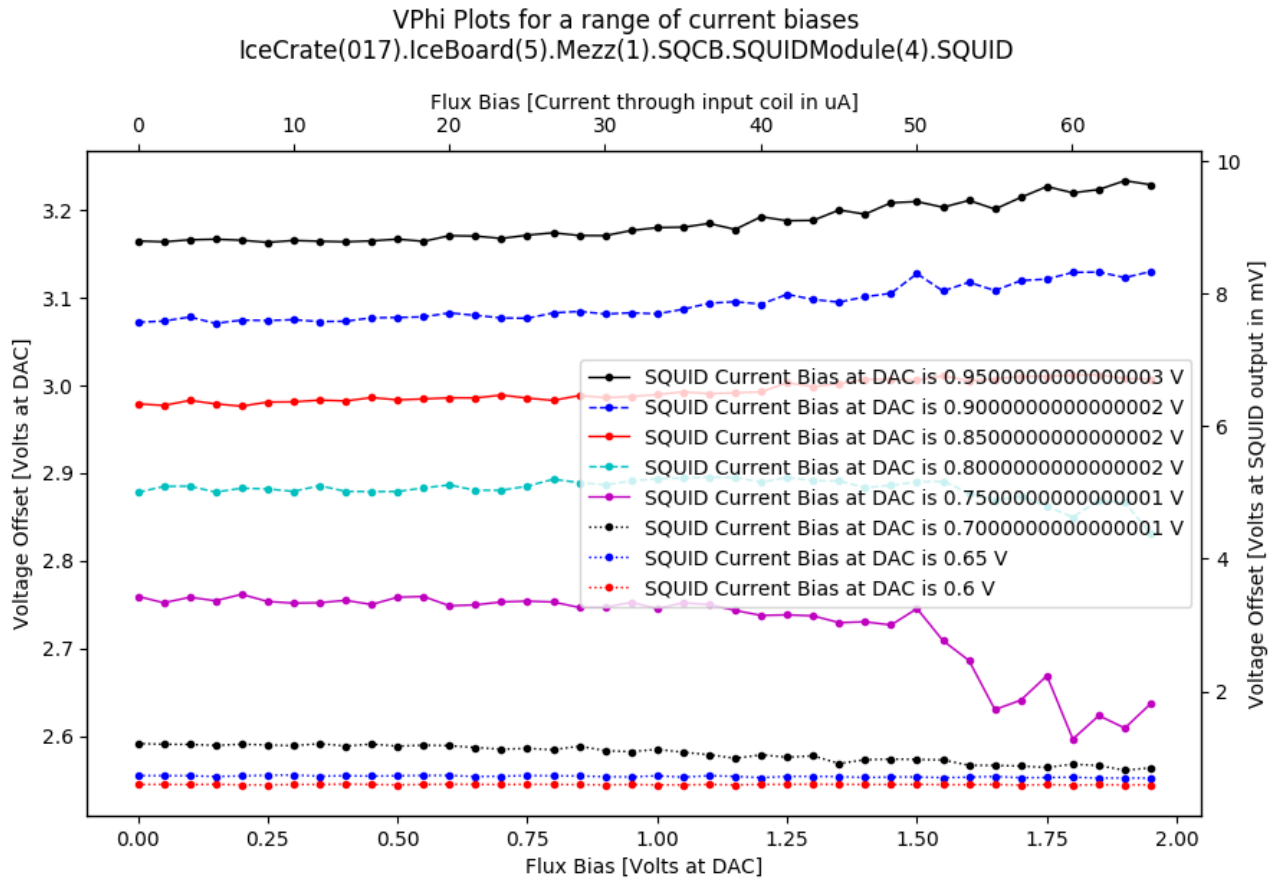


SQUID Tuning Algorithm Output

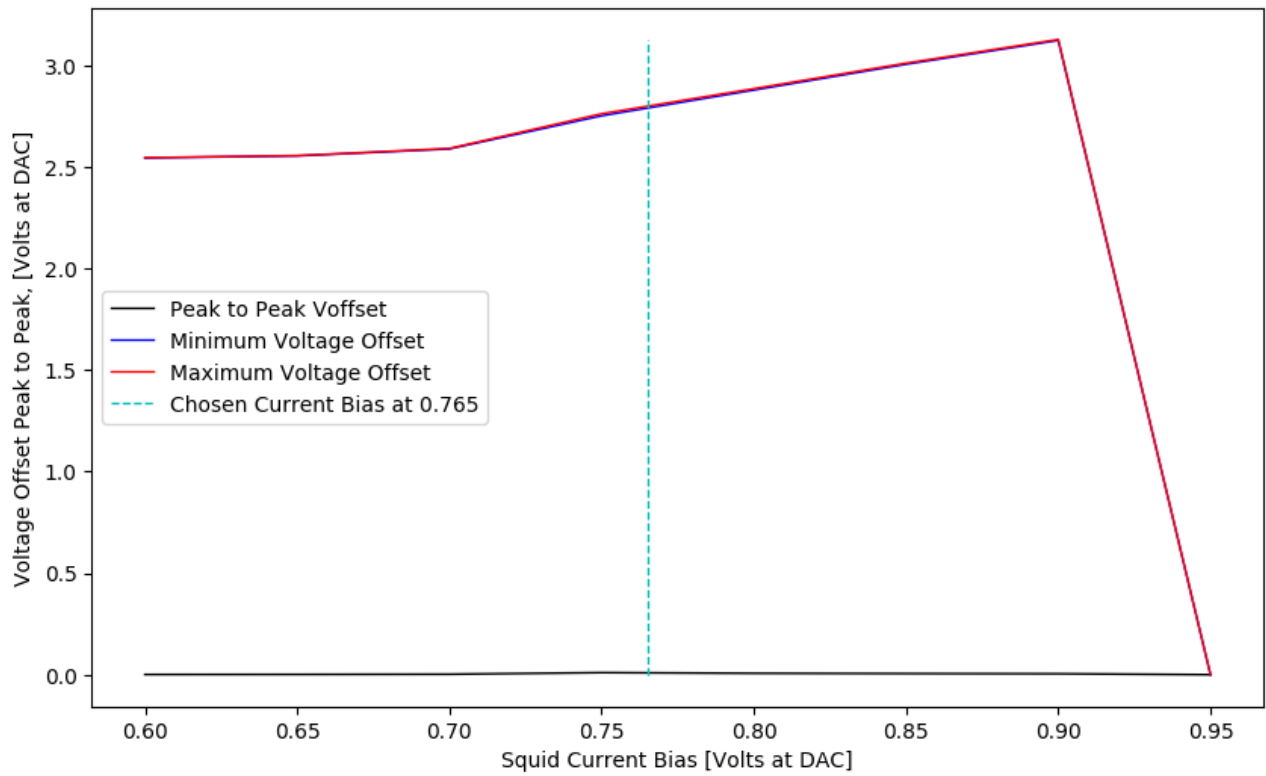
Target Data

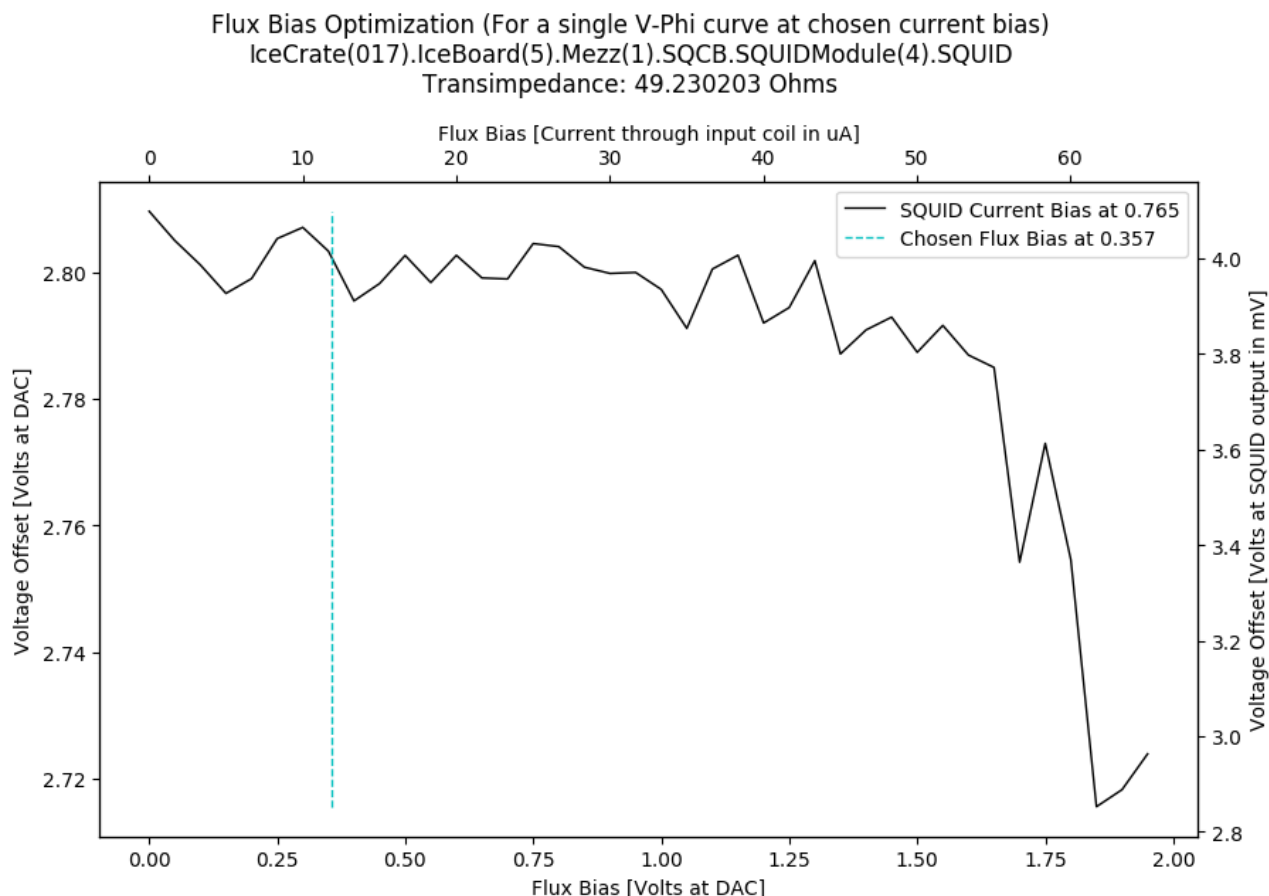
Full target name	IceCrate(017).IceBoard(5).Mezz(1).SQCB.SQUID Module(4).SQUID
HWM Name	PB2 HWM
HWM Path	/home/polarbear/hardware_maps/polarbear/PB2b/c ommissioning/202305_crate_test/crate_hwm_2023 10_shift_disable_bad_sq.yaml

Plots



Current Bias Optimization (Voltage Offset Peak-to-Peak vs Current Bias Voltage)
IceCrate(017).IceBoard(5).Mezz(1).SQCB.SQUIDModule(4).SQUID





Additional Data

Measured Transimpedance	49.23 Ohms
Chosen Flux Bias	0.36 Volts
Chosen Current Bias	0.77 Volts
Peak to Peak at Chosen Current Bias	0.01 Volts
Reference Current Bias Used	Unavailable

NOTE:

The Flux Bias and Current Bias are given as Voltages at the output of the Flux Bias and Current Bias DACs