

Lecture 7

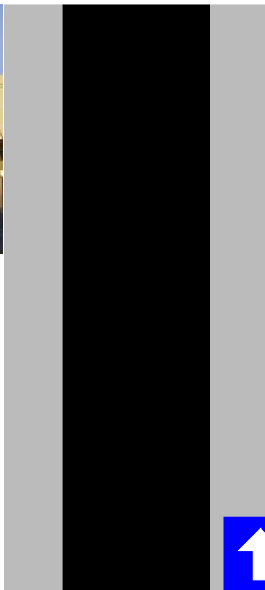
Digital system design

Finite State Machines

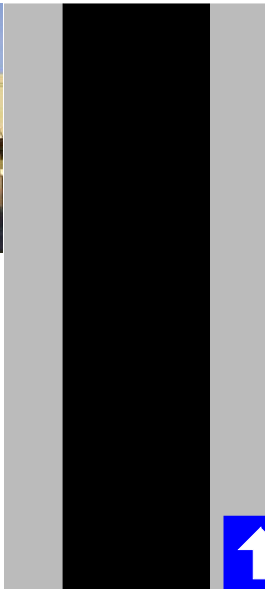
CS173 - Conception de systèmes numériques
November 2016

Prof. Dr. Theo Kluter
Bern University of Applied Sciences

Introduction



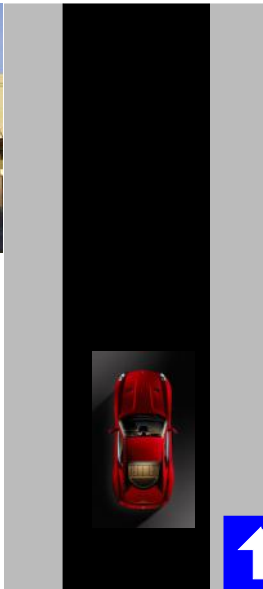
Introduction



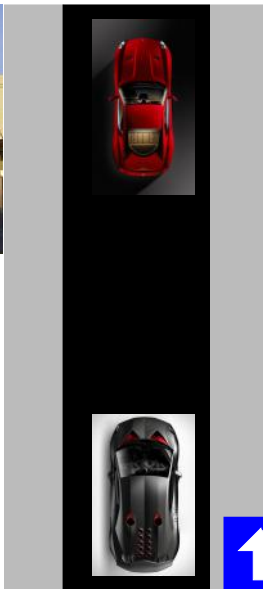
Introduction



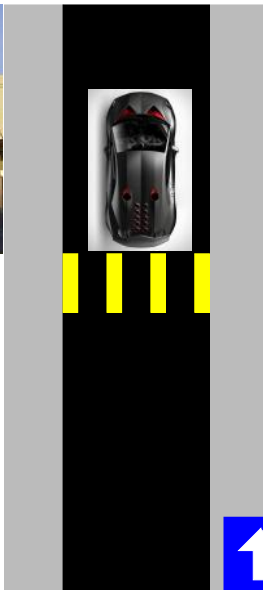
Introduction



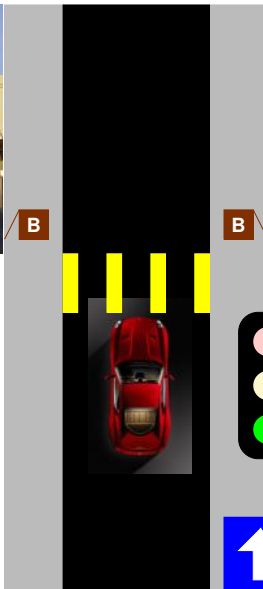
Introduction



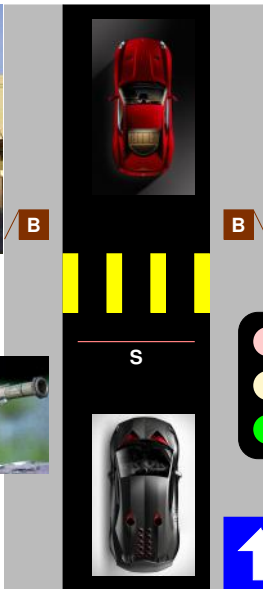
Introduction



Introduction



Introduction

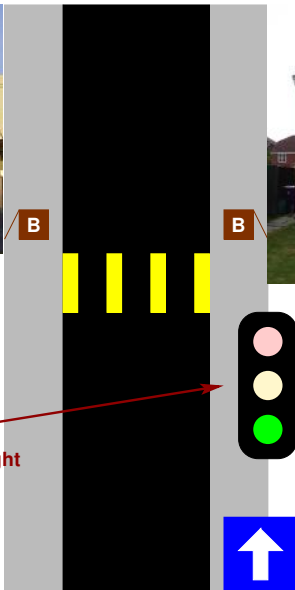


Sequence

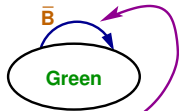


Green

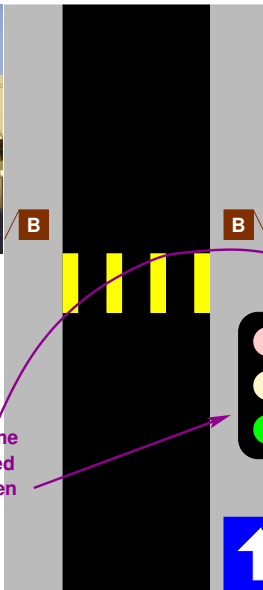
Current state of traffic light



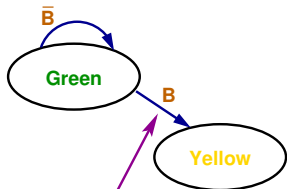
Sequence



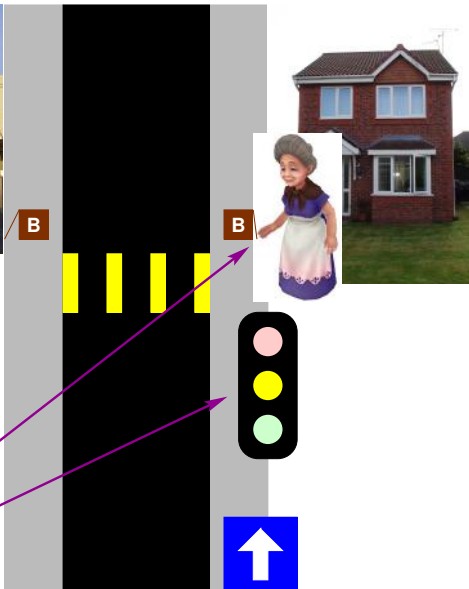
grandmother still at home
hence button not pushed
traffic light stays at green



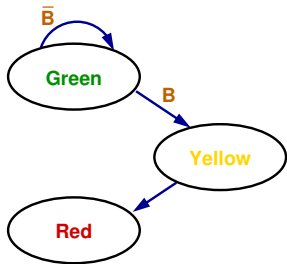
Sequence



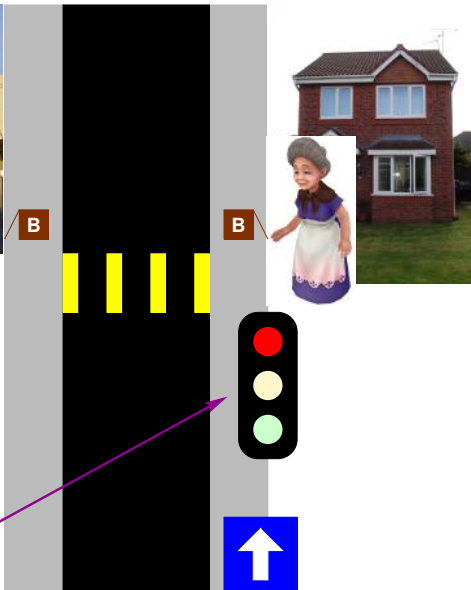
grandmother pushes button
traffic light goes to yellow



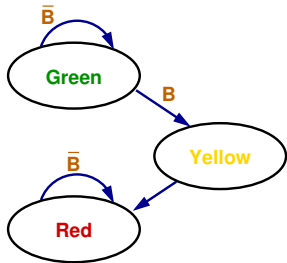
Sequence



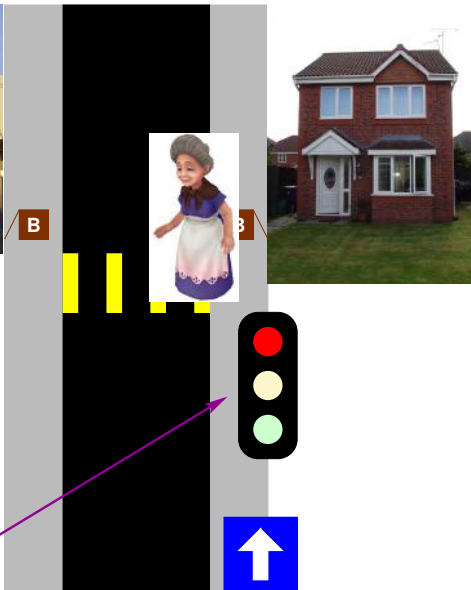
traffic light goes to red



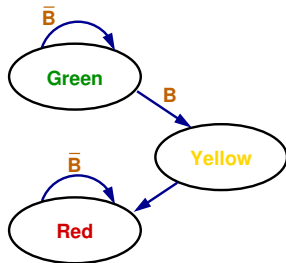
Sequence



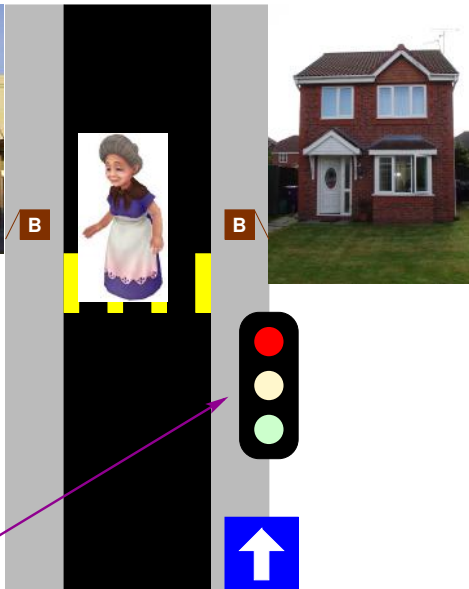
As long as the button is not pressed
the traffic light stays on red



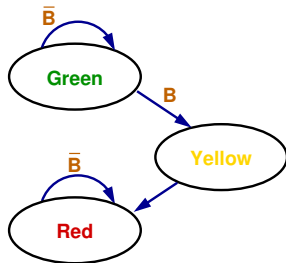
Sequence



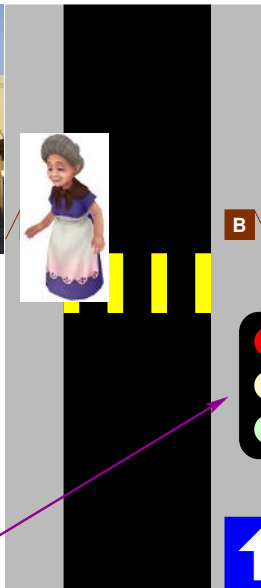
As long as the button is not pressed
the traffic light stays on red



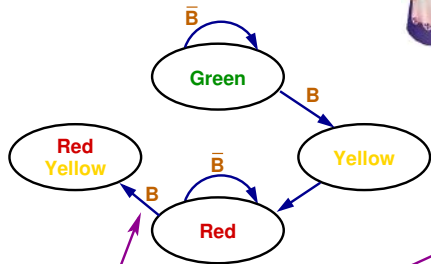
Sequence



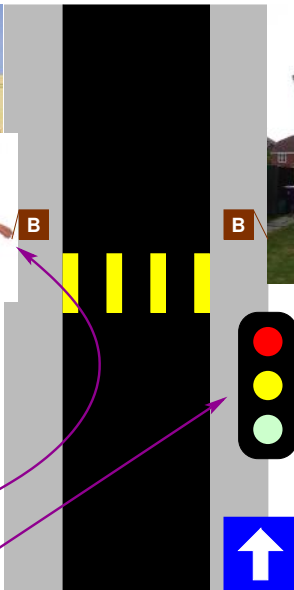
As long as the button is not pressed
the traffic light stays on red



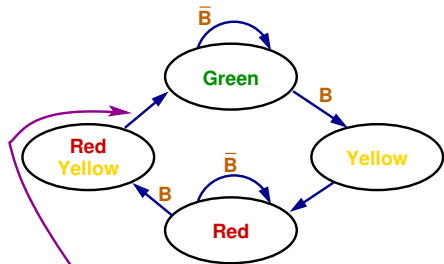
Sequence



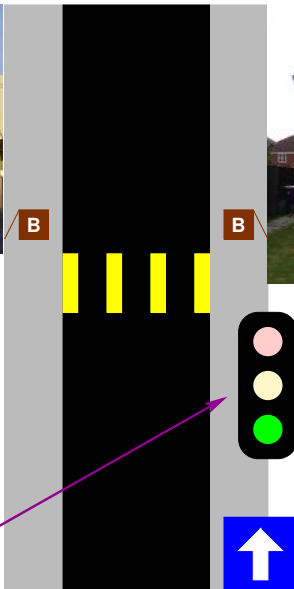
When the button is again pressed
the traffic light goes to red-yellow



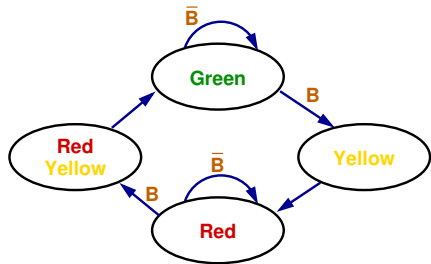
Sequence



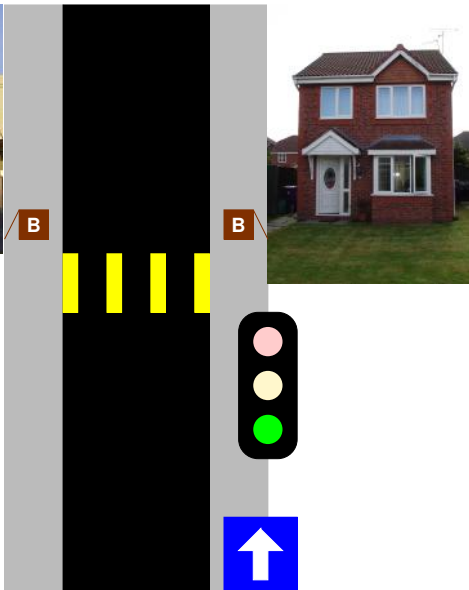
and finally back to green



Sequence

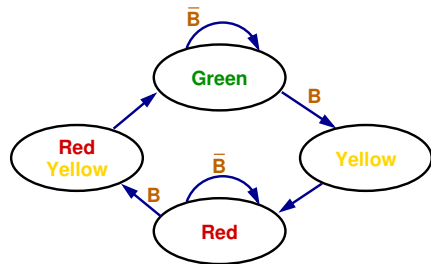


This sequence repeats itself



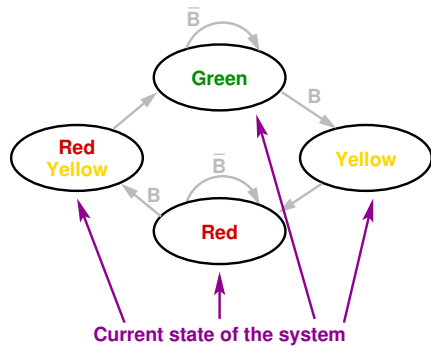
Basic Structure

State Diagram:



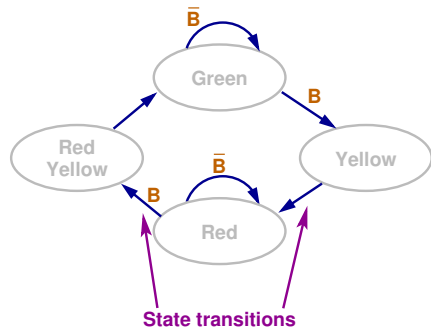
Basic Structure

State Diagram:

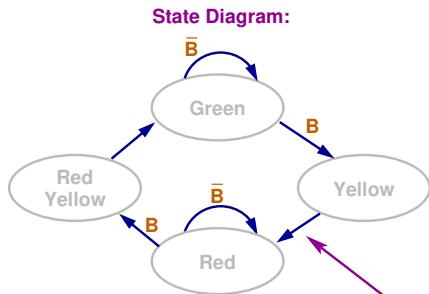


Basic Structure

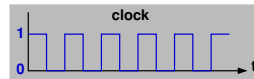
State Diagram:



Basic Structure

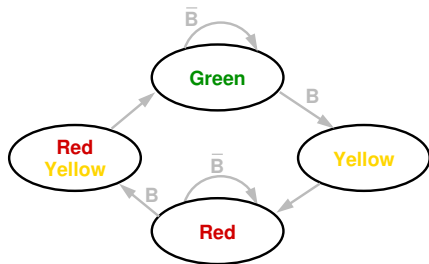


To make a sequence (perform transitions)
we require a repeating signal (clock)

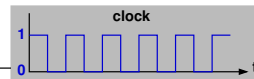
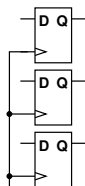


Basic Structure

State Diagram:

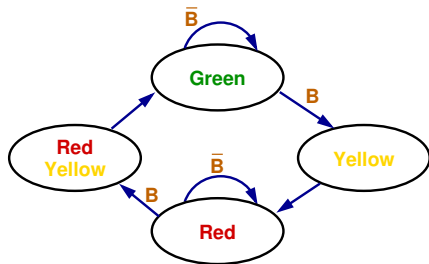


To store the current state we require memory elements

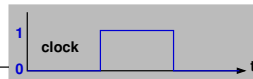
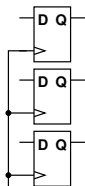


Basic Structure

State Diagram:

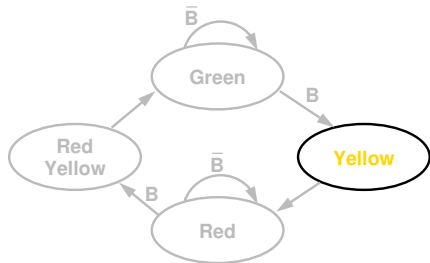


Why D-flipflops?

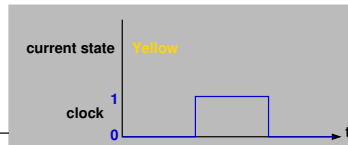
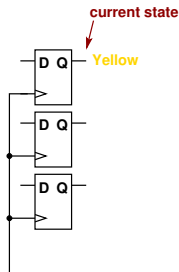


Basic Structure

State Diagram:

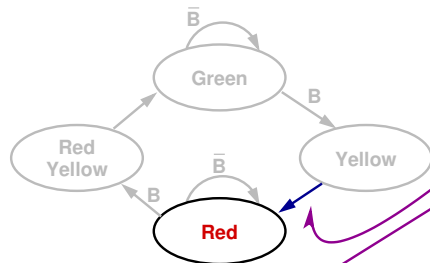


Let's assume that the current state is yellow



Basic Structure

State Diagram:



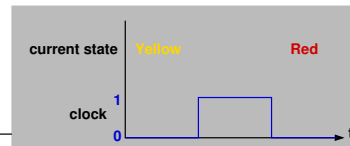
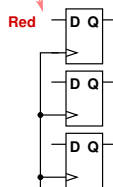
Then the next state should be red

next state

Red

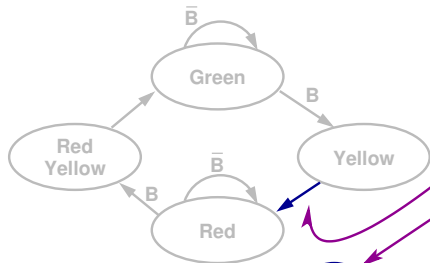
current state

Yellow



Basic Structure

State Diagram:

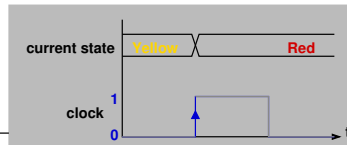
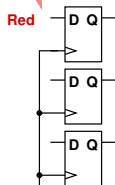


The transition must take place at exactly a single point in time (the rising edge)

next state

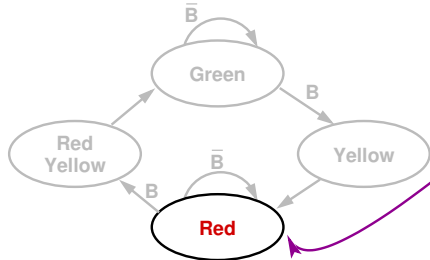
Red

Yellow



Basic Structure

State Diagram:



After the transition the current state is red. This repeats itself over and over.

next state

?

D

Q

current state

Red

D

Q

D

Q

current state

Yellow

Red

clock

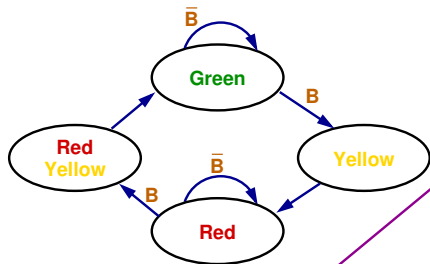
1

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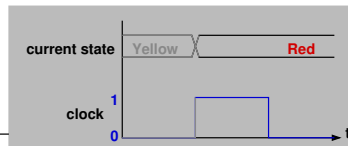
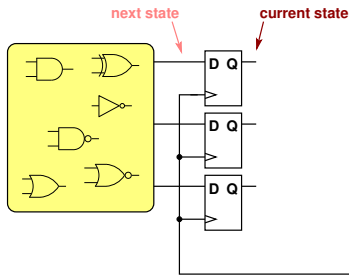
t

Basic Structure

State Diagram:

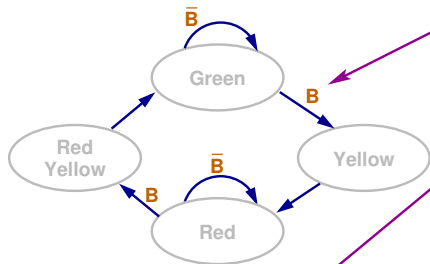


Besides the memory we require also logic to determine the next state

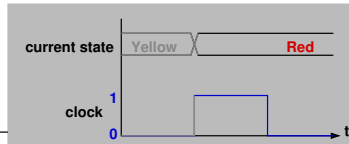
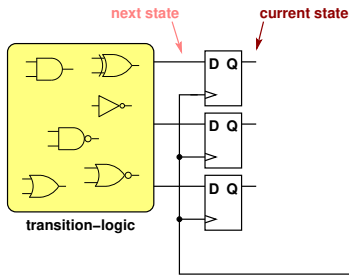


Basic Structure

State Diagram:

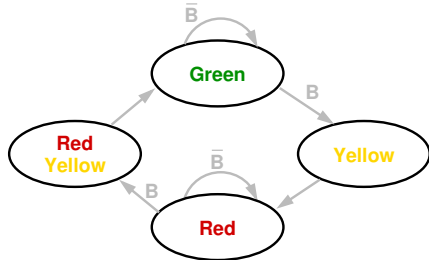


This combinational logic determines the transitions and is called the transition-logic

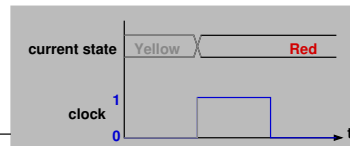
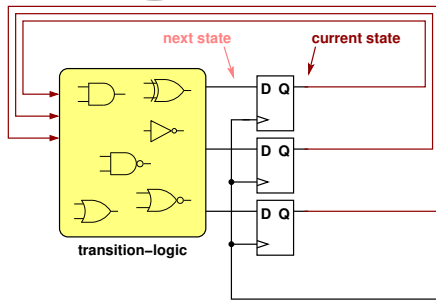


Basic Structure

State Diagram:

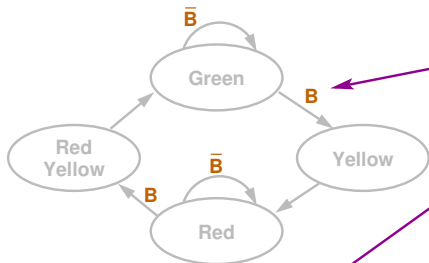


The transition-logic needs to know what the current state is

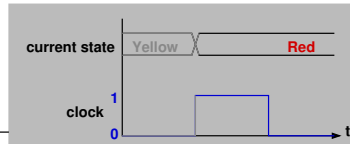
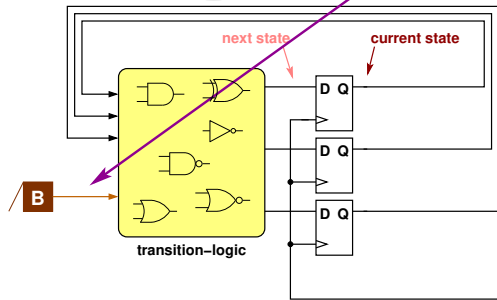


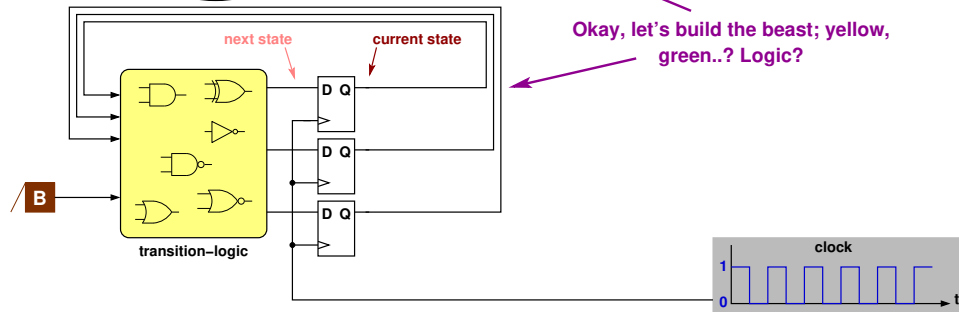
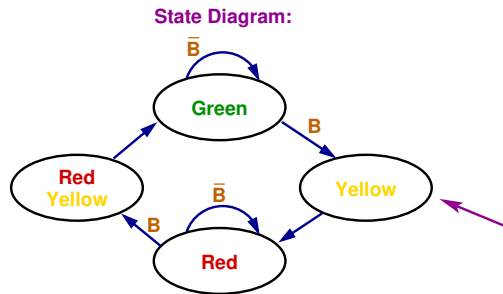
Basic Structure

State Diagram:

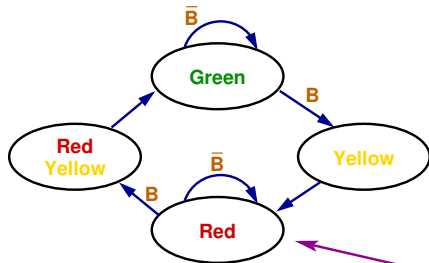


The transition-logic also needs to know the state of the system inputs





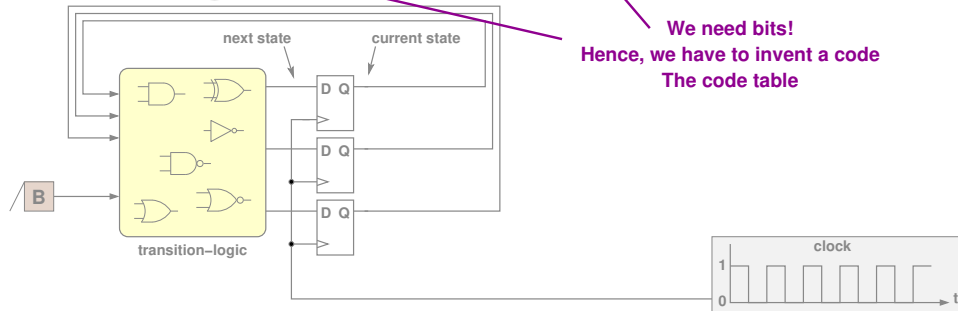
State Diagram:



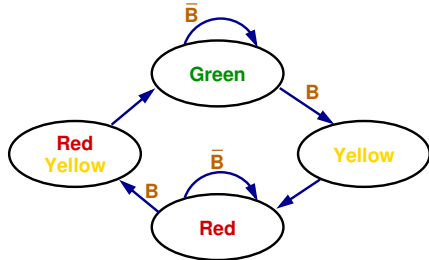
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

We need bits!
Hence, we have to invent a code
The code table

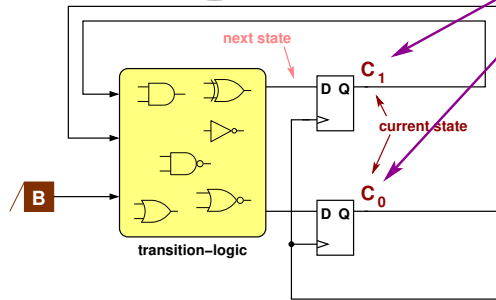


State Diagram:

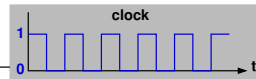


Code-table:

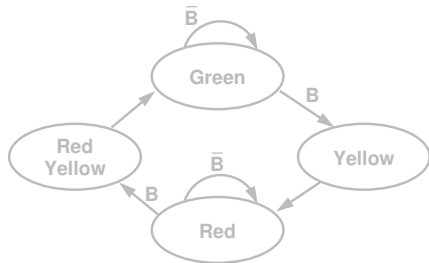
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



The bits are assigned to a flipflop

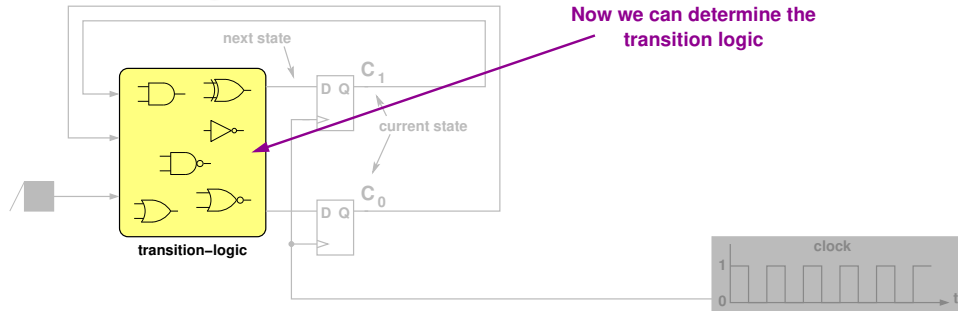


State Diagram:

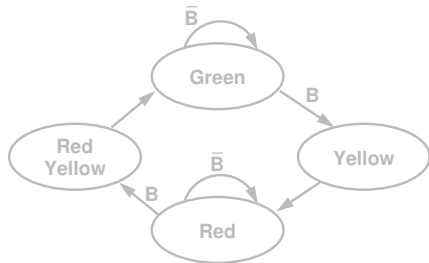


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

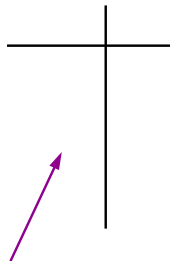


State Diagram:

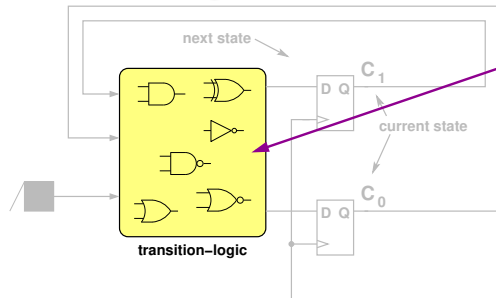


Code-table:

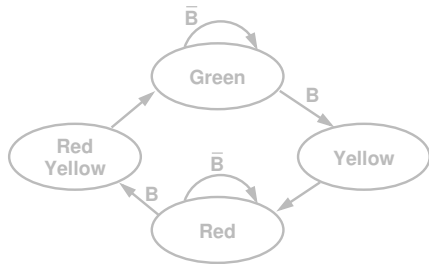
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



Now we can determine the transition logic
We make a truthtable!

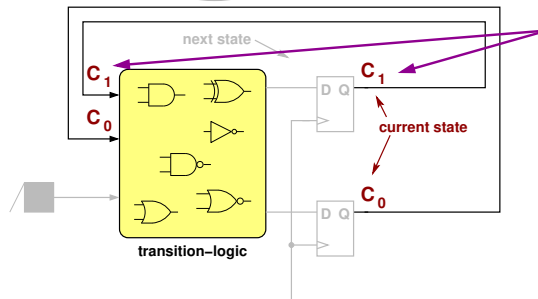
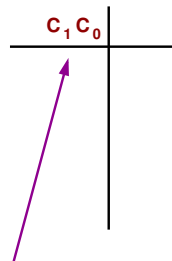


State Diagram:



Code-table:

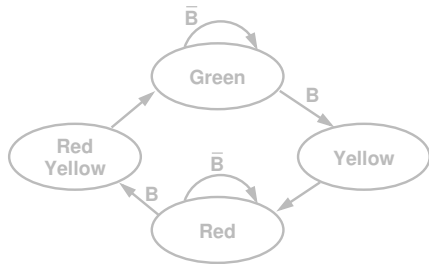
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



The inputs of the transition logic are the current state

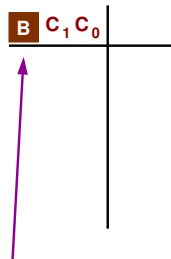


State Diagram:

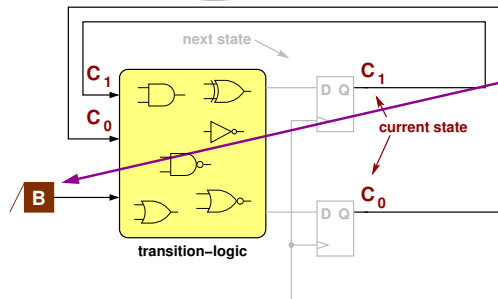


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

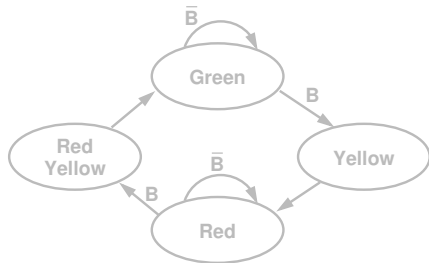


The inputs to the transition logic are the current state and the external input(s).



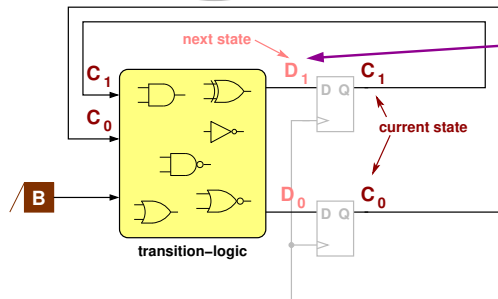
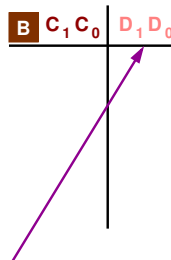
Medvedev FSM

State Diagram:



Code-table:

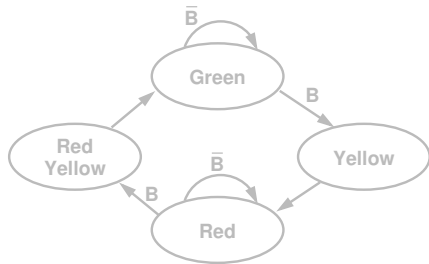
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



The output(s) of the transition logic is the next state.
We have to name them.



State Diagram:



Code-table:

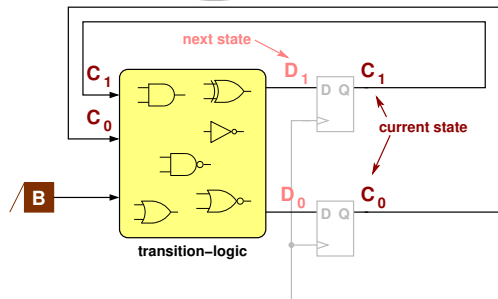
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



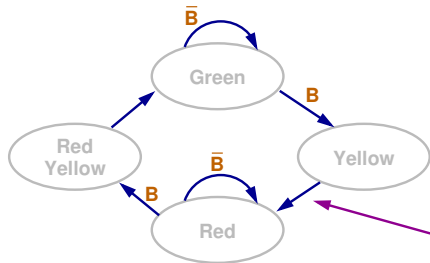
IMPORTANT:

Please note the order of the state bits!

Always: MSB...LSB!

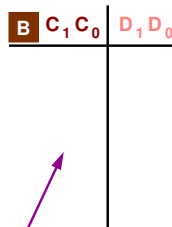


State Diagram:

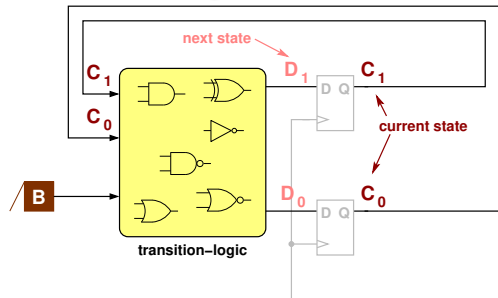


Code-table:

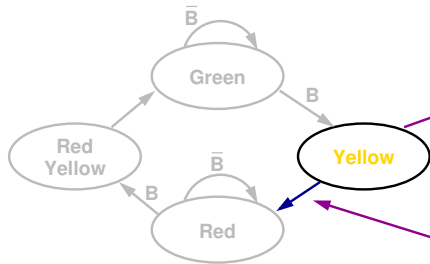
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b



The transition logic defines the transitions in the state diagram. Each transition represents one line.



State Diagram:

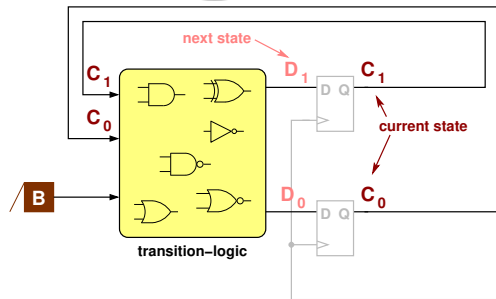


Code-table:

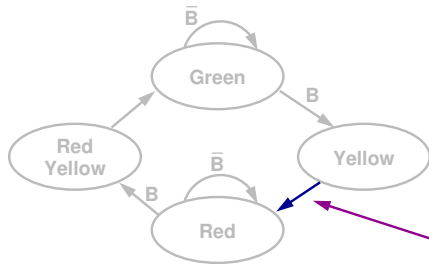
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
	0	1		

Let's start with this transition:
The current state is Yellow



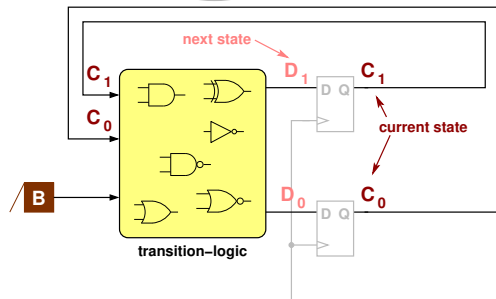
State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

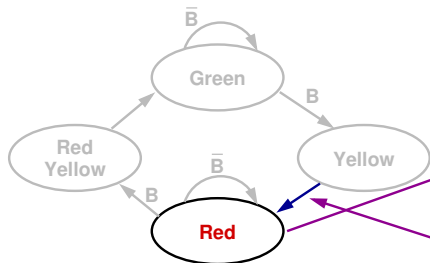
B	C ₁	C ₀	D ₁	D ₀
-	0	1		



Let's start with this transition:
The current state is Yellow
and the input B has no influence;
we have an unconditional transition!



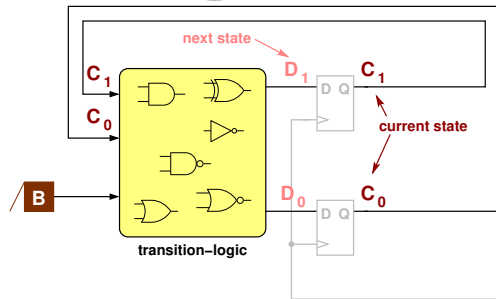
State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

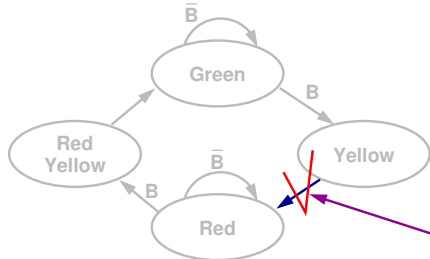
B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0



Let's start with this transition:
The current state is Yellow
and the input B has no influence;
we have an unconditional transition!
The next state is Red



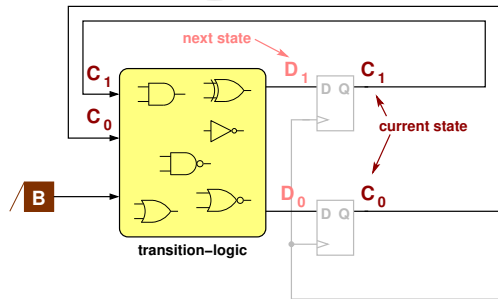
State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

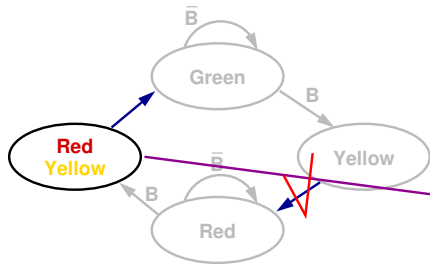
B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0



We are done for this transition;
we have to continue for all the others



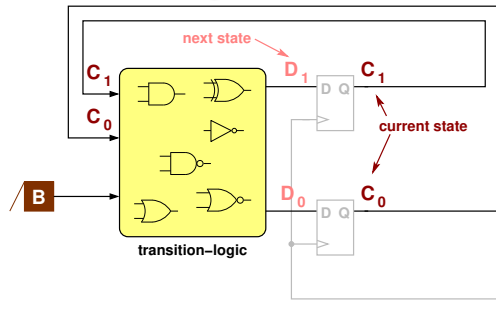
State Diagram:



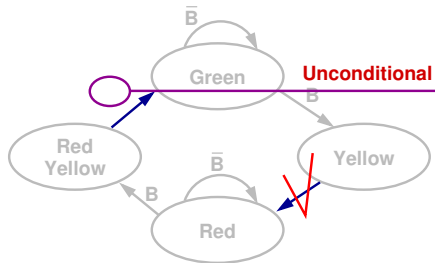
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
	1	1		



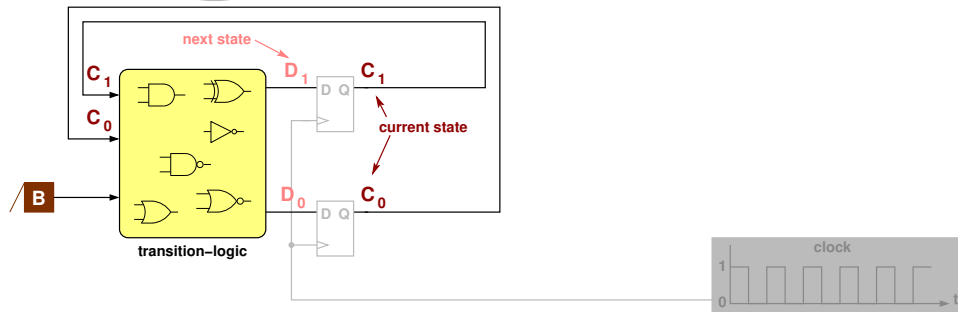
State Diagram:



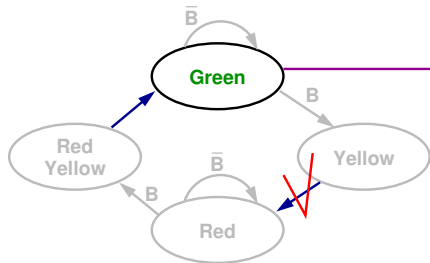
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1		



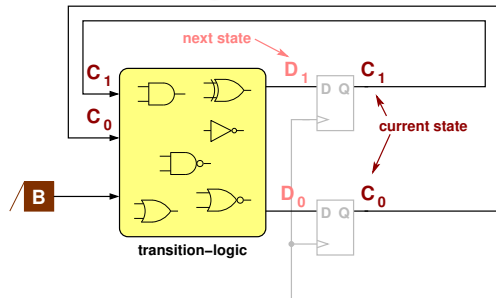
State Diagram:



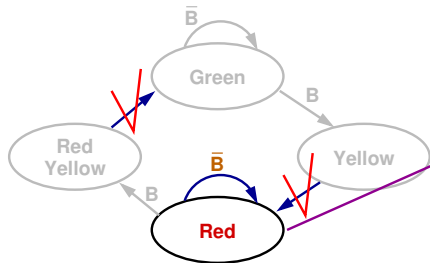
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0



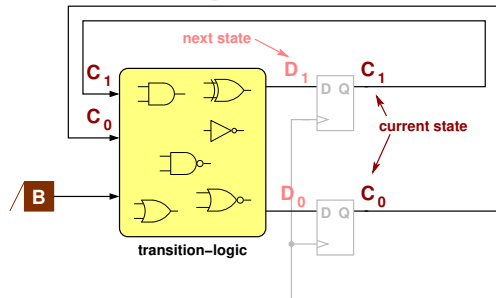
State Diagram:



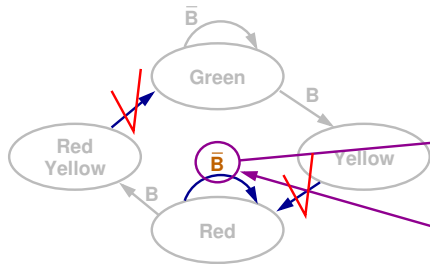
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
	1	0		



State Diagram:

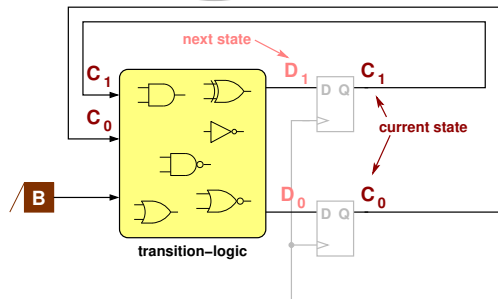


Code-table:

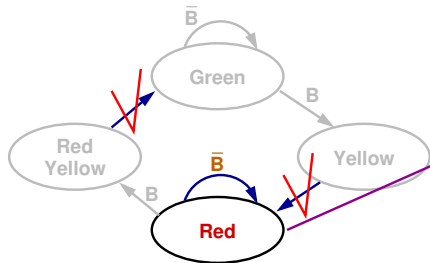
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0		

For this transition
the input B must be 0!



State Diagram:

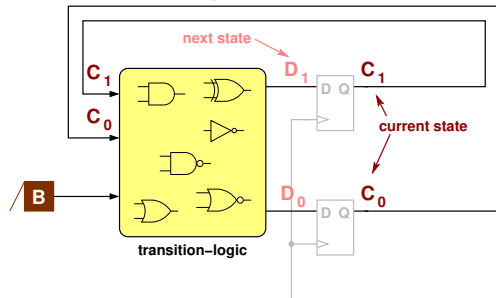


Code-table:

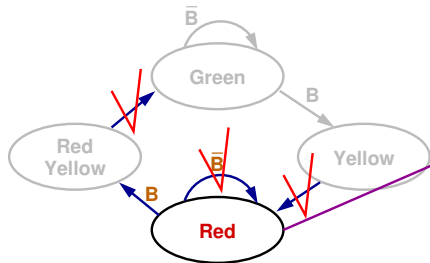
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0

The state transitions to itself



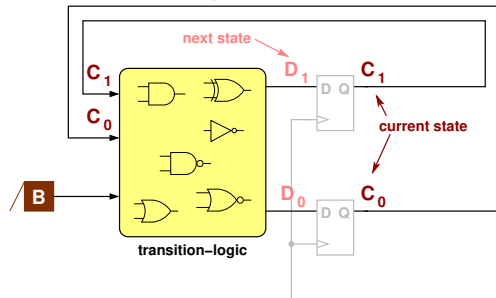
State Diagram:



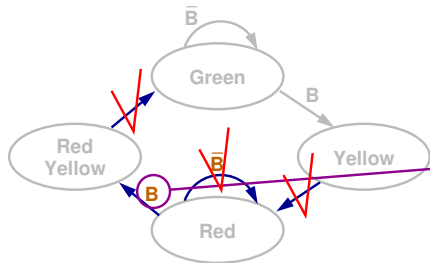
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	0	-	-	-



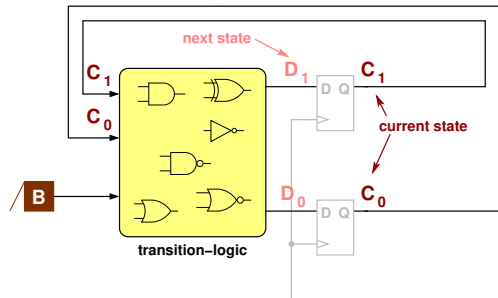
State Diagram:



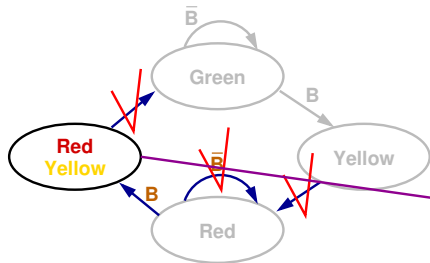
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0		



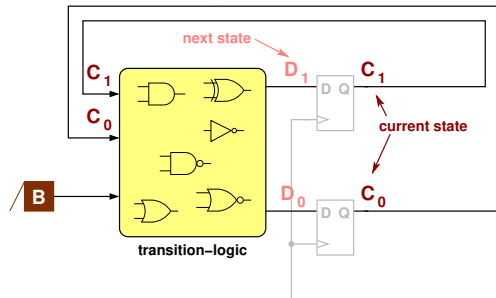
State Diagram:



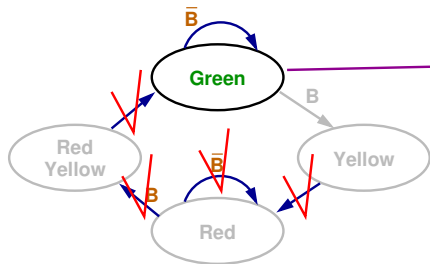
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1



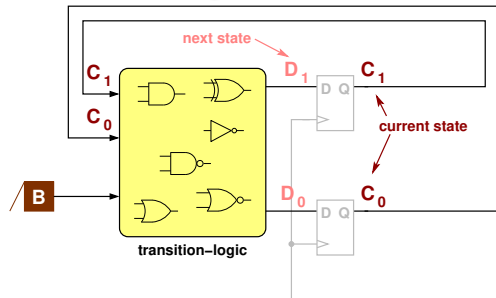
State Diagram:



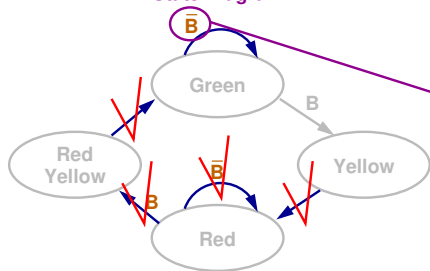
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
	0	0		



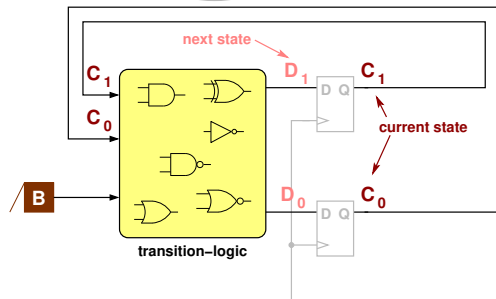
State Diagram:



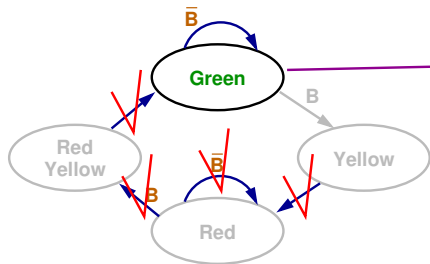
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0		



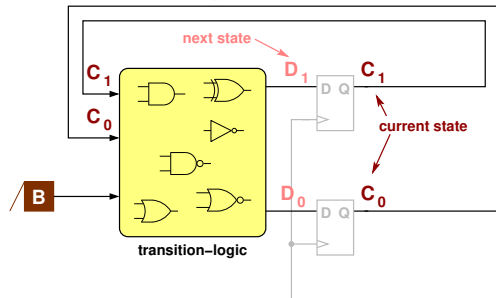
State Diagram:



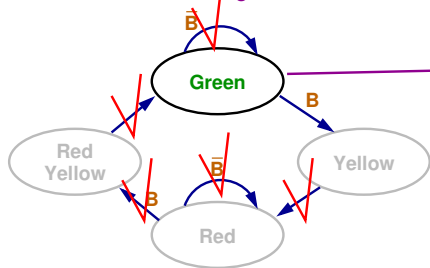
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0



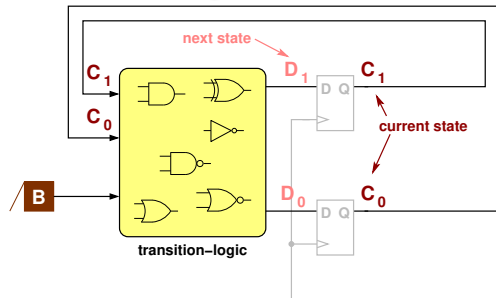
State Diagram:



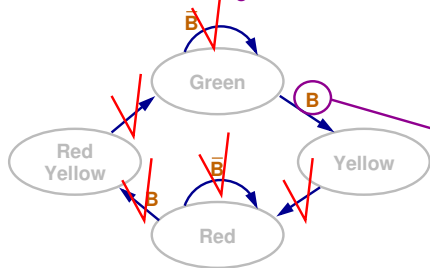
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
0	0	0	0	0



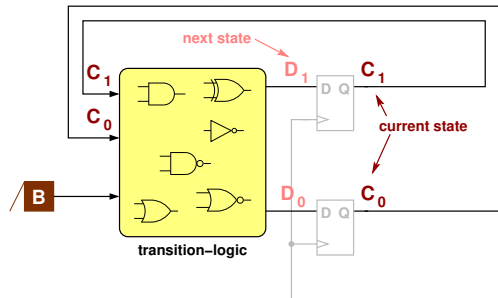
State Diagram:



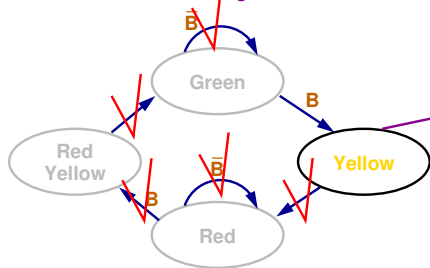
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	1	0



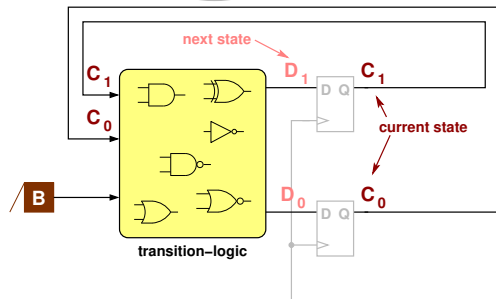
State Diagram:



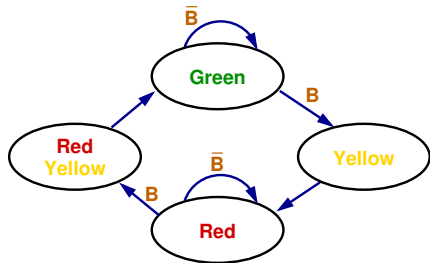
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



State Diagram:



Code-table:

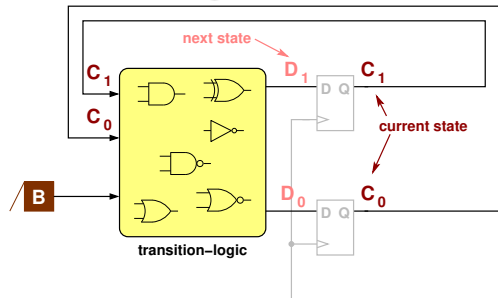
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

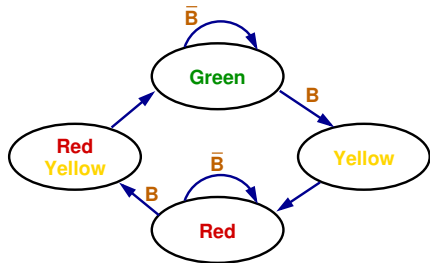
B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We are done!

This created truthtable is called the transition table of an FSM.



State Diagram:



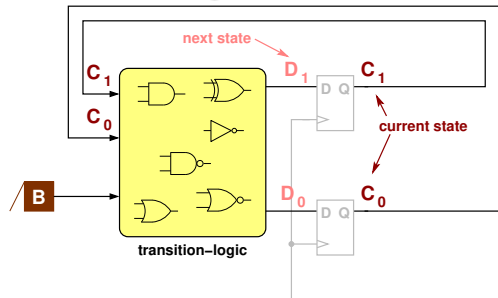
Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We can build the circuit

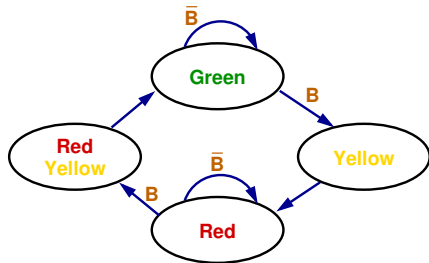


$$D_1 = C_0 \oplus C_1$$

$$D_0 = \overline{C_0} \cdot B$$



State Diagram:

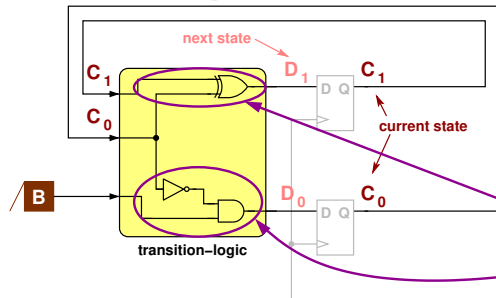


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



	B
C ₁	1 1
C ₀	0 0
	1 1
	0 0

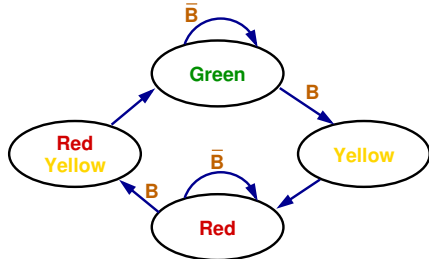
$$D_1 = C_0 \oplus C_1$$

	B
C ₀	0 0
	0 0
	0 1
	0 1

$$D_0 = \overline{C_0} \cdot B$$



State Diagram:

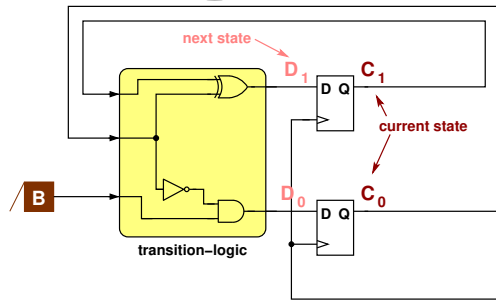


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

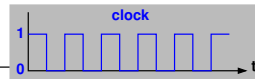
B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



This type of Finite State Machine (FSM)

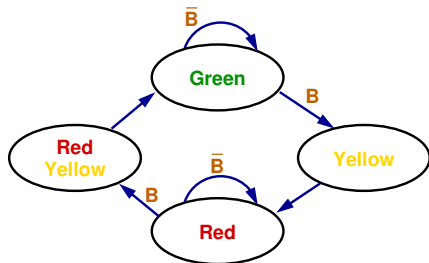
is called a:

Medvedev FSM



Moore FSM

State Diagram:

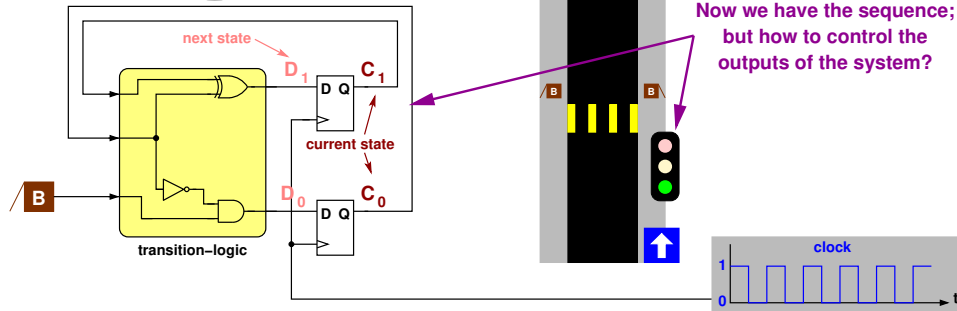


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

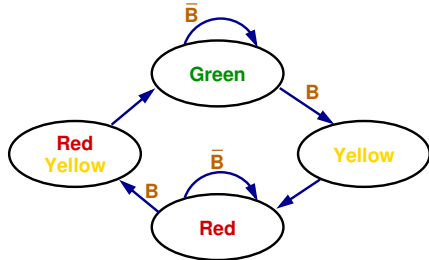
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



Moore FSM

State Diagram:

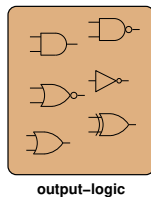
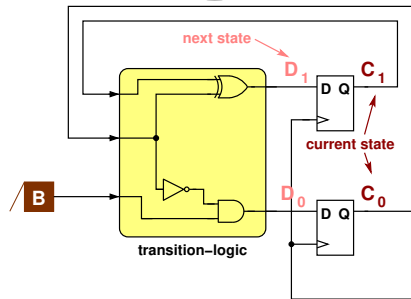


Code-table:

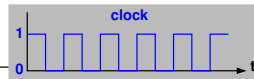
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

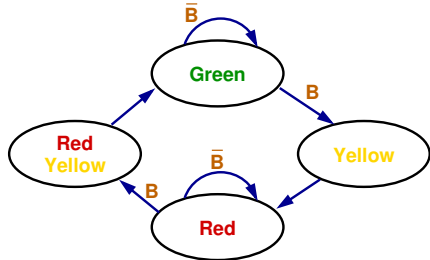


We need a new logic block
the output logic



Moore FSM

State Diagram:

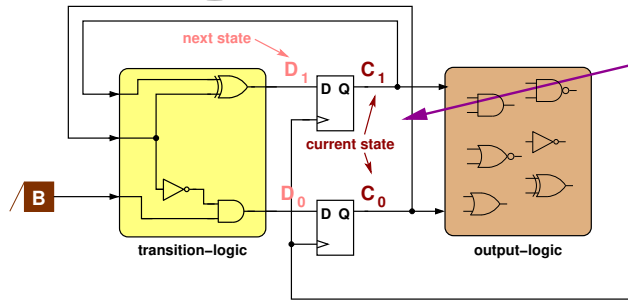


Code-table:

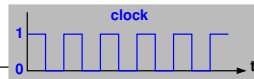
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

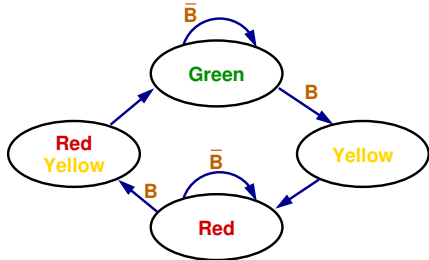


We need a new logic block
the output logic
it depends on the current state



Moore FSM

State Diagram:

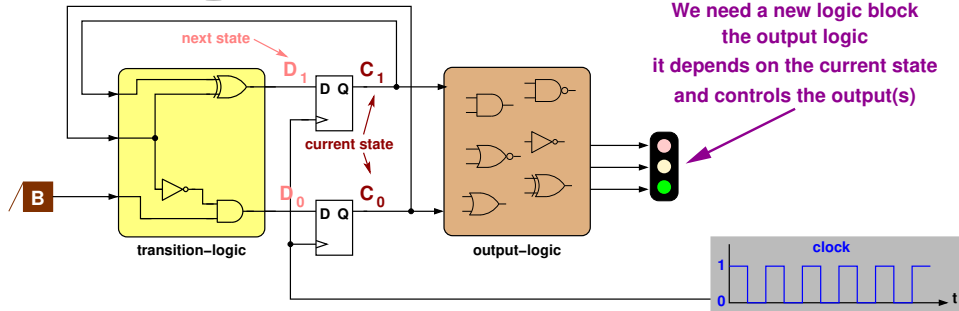


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

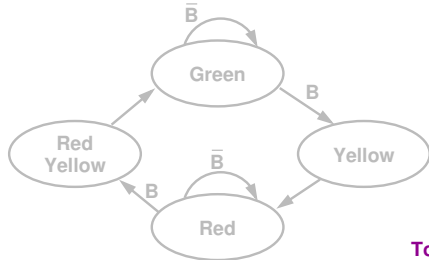
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



Moore FSM

State Diagram:



Code-table:

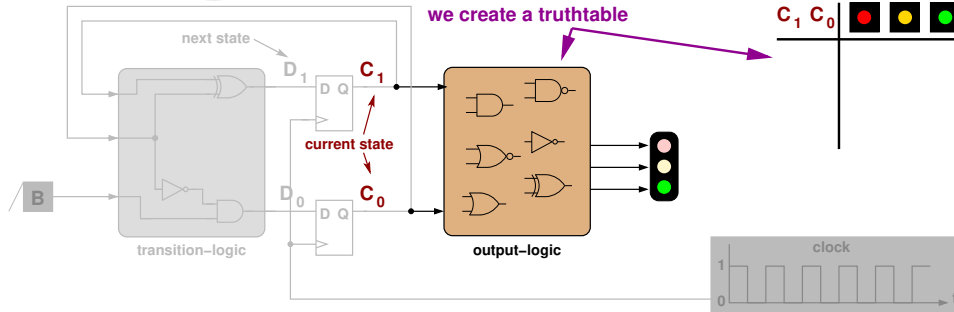
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

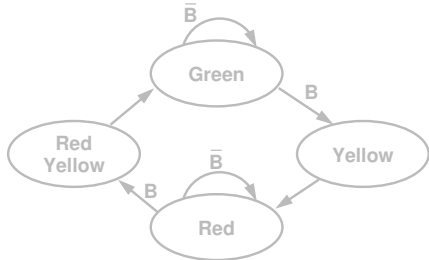
To realize the output logic we create a truthtable

C ₁ C ₀	Red	Yellow	Green
00	0	0	1
01	0	1	0
10	1	0	0
11	1	1	0



Moore FSM

State Diagram:



Code-table:

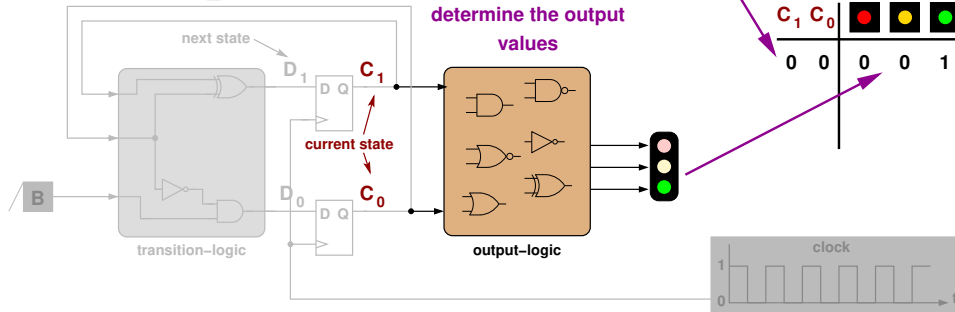
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

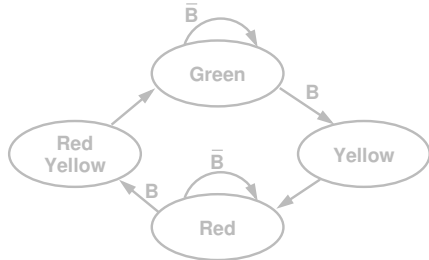
For each state we determine the output values

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1



Moore FSM

State Diagram:

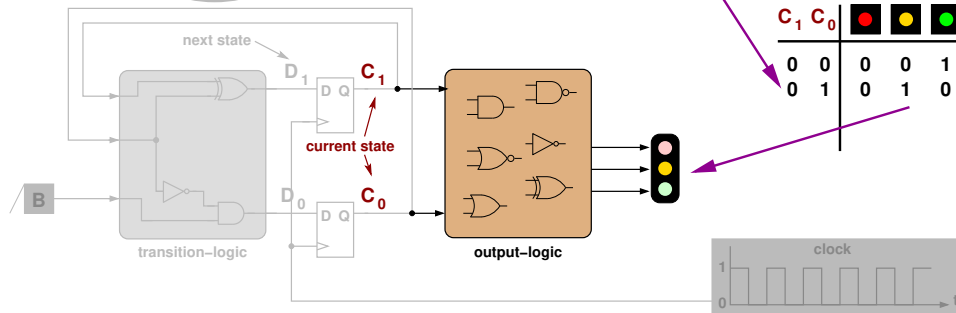


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

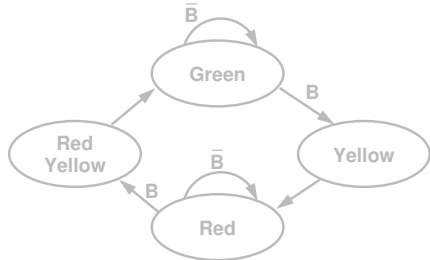
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



Moore FSM

State Diagram:

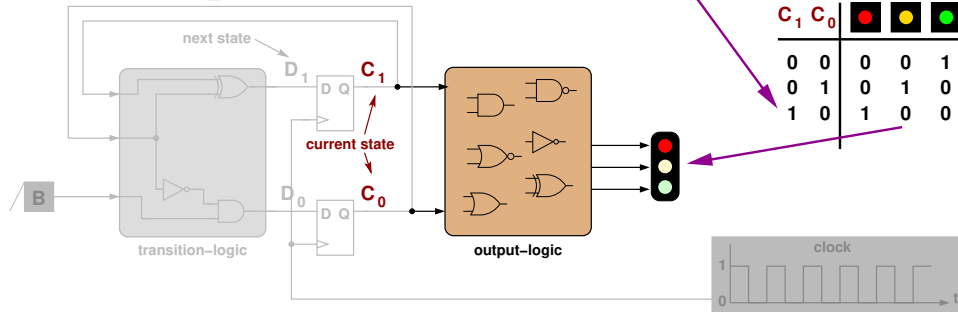


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

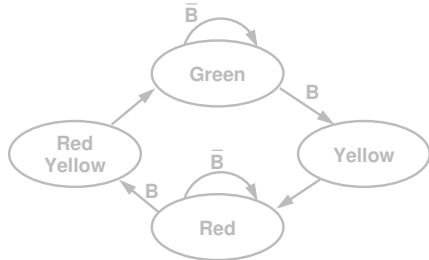
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



Moore FSM

State Diagram:

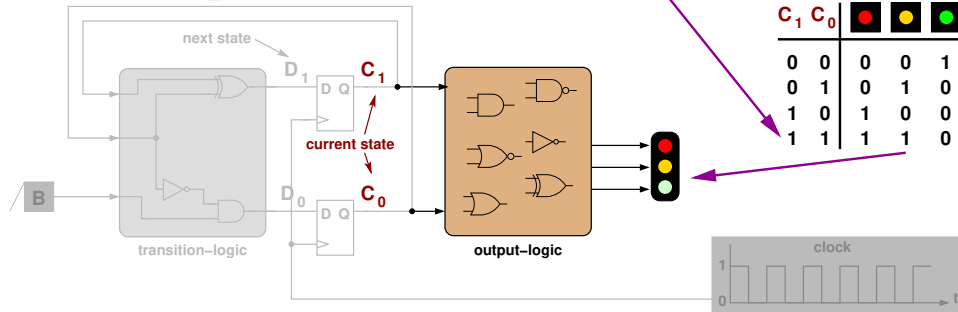


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

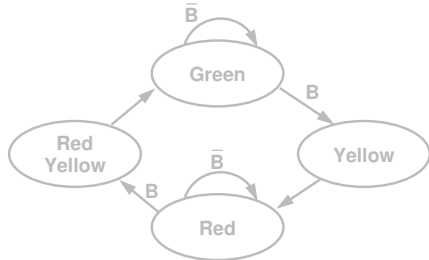
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1



Moore FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

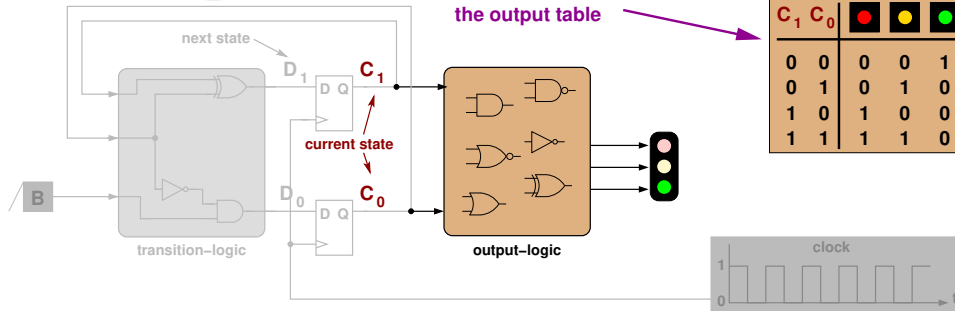
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We call this truth table the output table

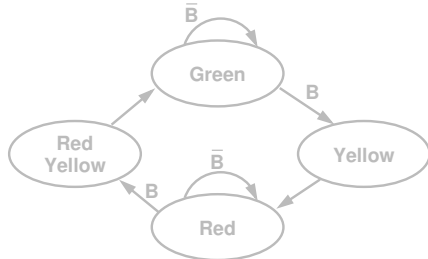
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Moore FSM

State Diagram:



Code-table:

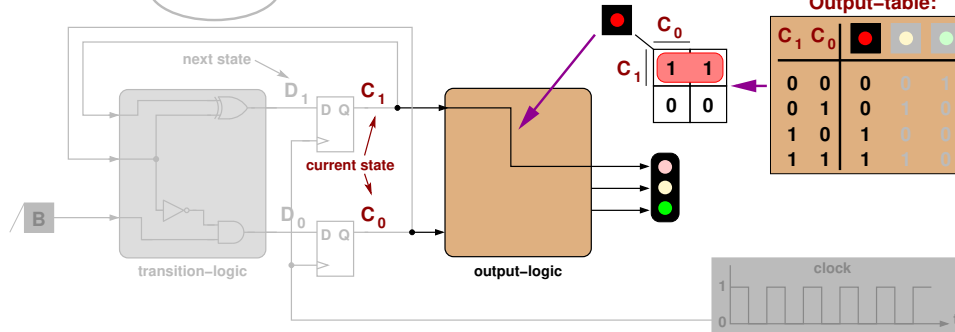
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
–	0	1	1	0
–	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

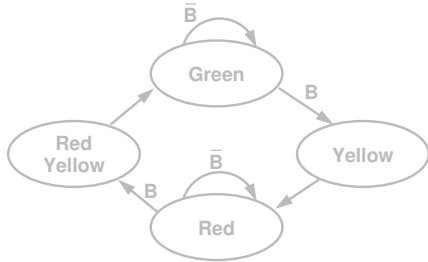
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Moore FSM

State Diagram:



Code-table:

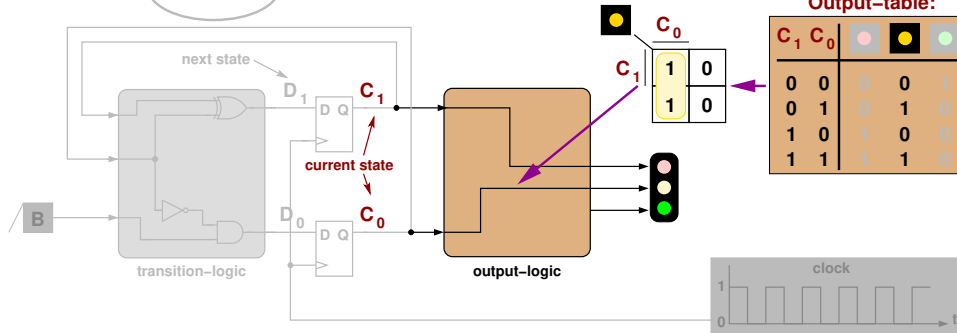
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

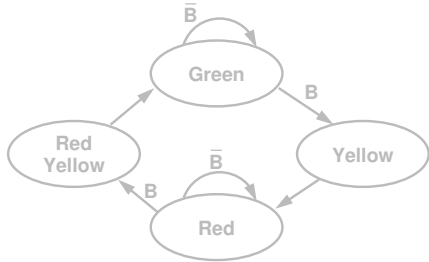
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Moore FSM

State Diagram:



Code-table:

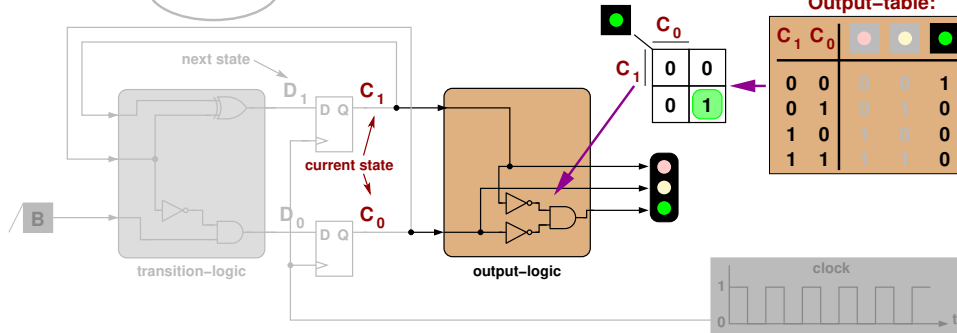
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

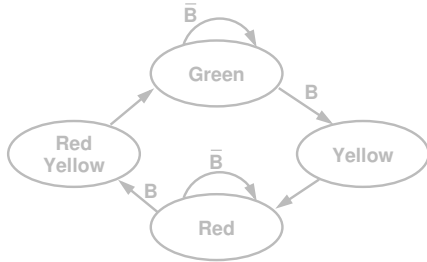
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Moore FSM

State Diagram:



Code-table:

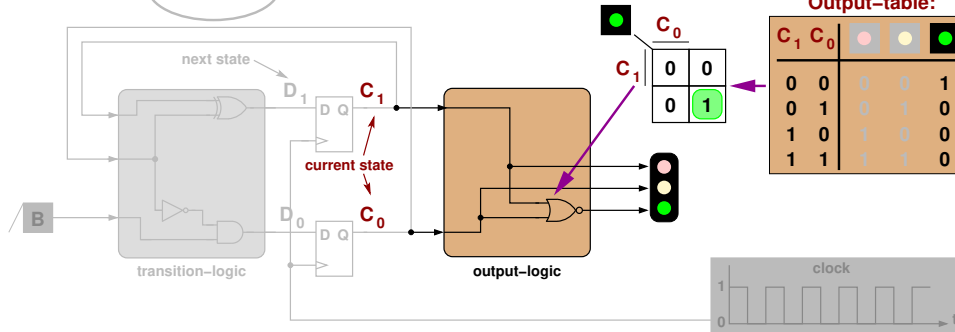
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

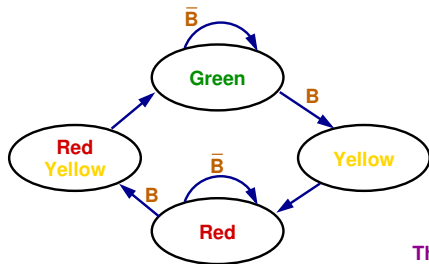
Output-table:

C ₁	C ₀			
0	0			
0	1			
1	0			
1	1			



Moore FSM

State Diagram:



Code-table:

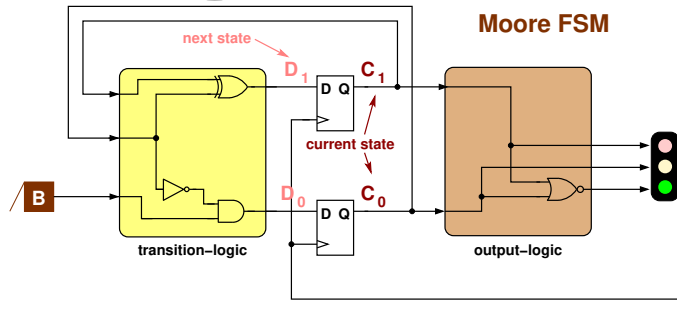
State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

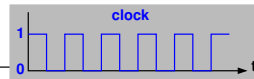
This type of FSM is called a:

Moore FSM



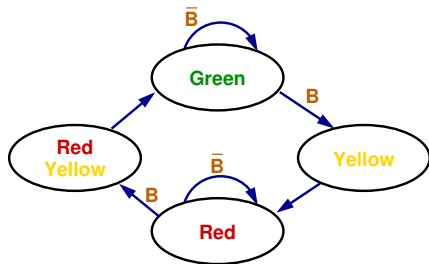
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Mealy FSM

State Diagram:

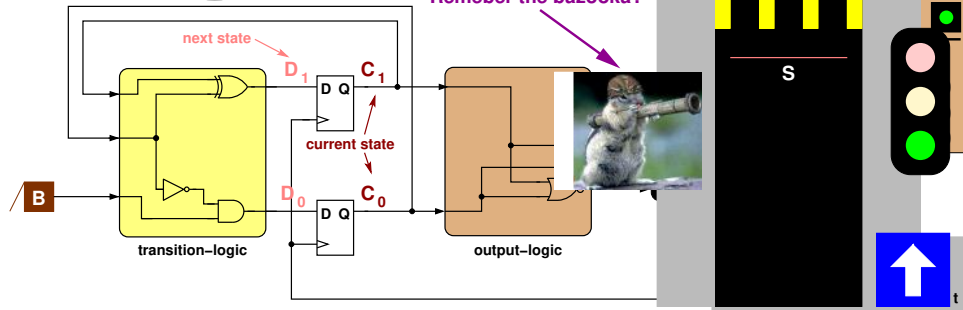


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

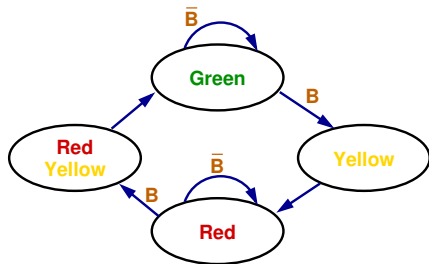
Transition table:

Remeber the bazooka?



Mealy FSM

State Diagram:

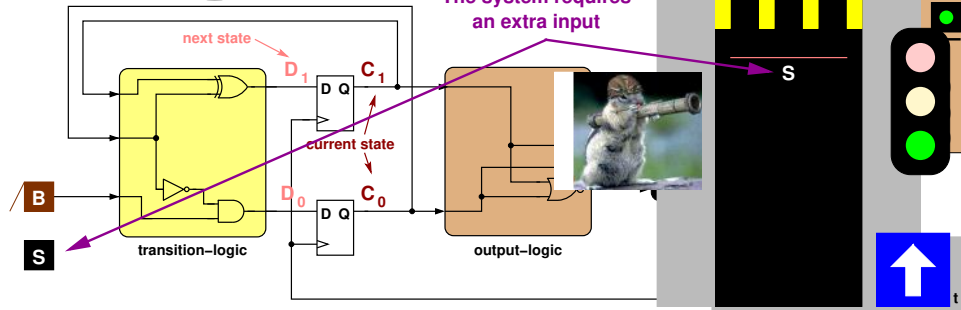


Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

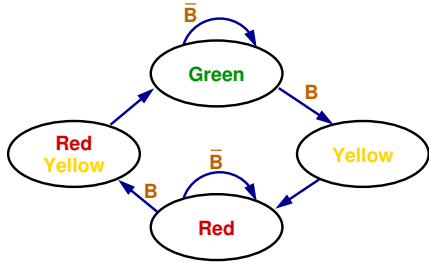
Transition table:

The system requires an extra input



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

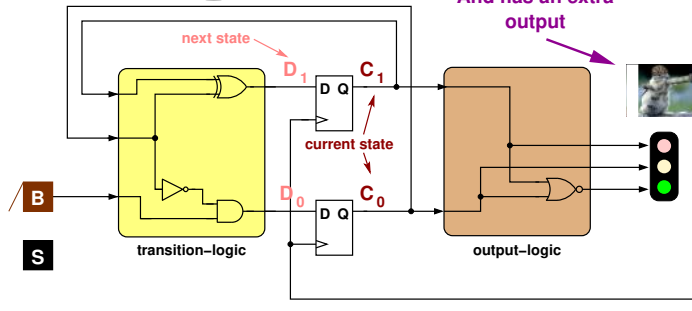
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

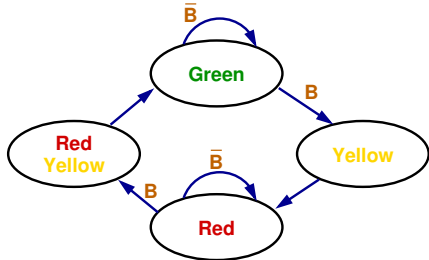
C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0

And has an extra output



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

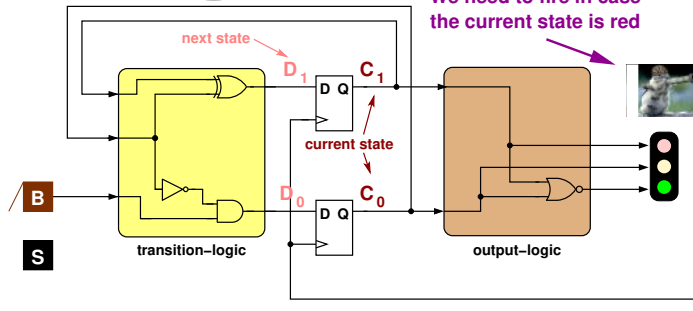
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

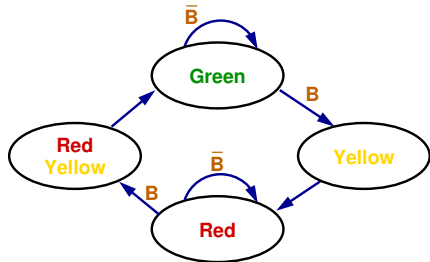
C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0

We need to fire in case the current state is red



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

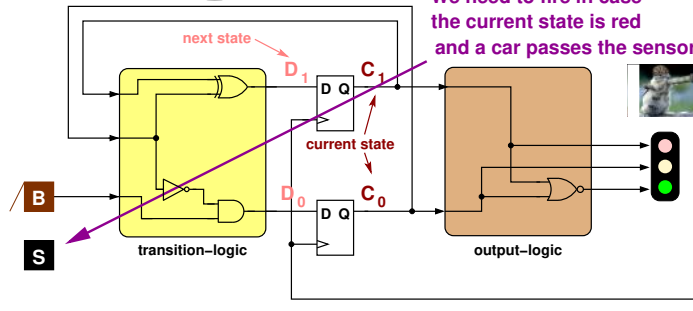
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

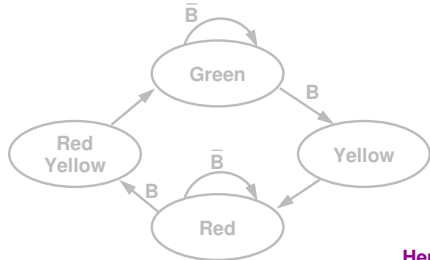
C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0

We need to fire in case the current state is red and a car passes the sensor



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

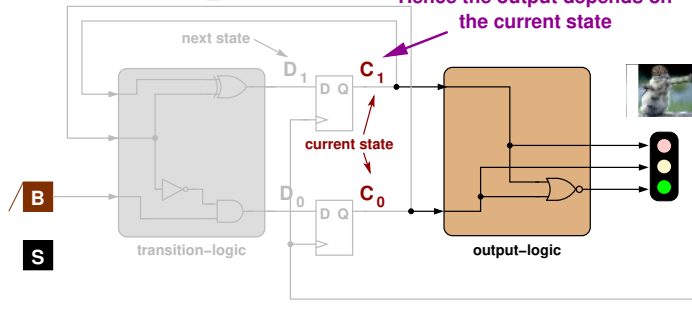
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

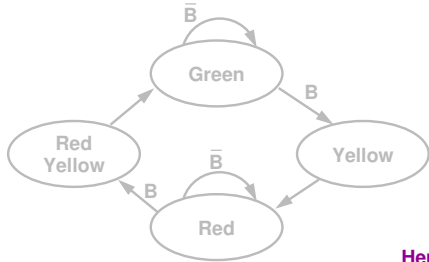
C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0

Hence the output depends on the current state



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

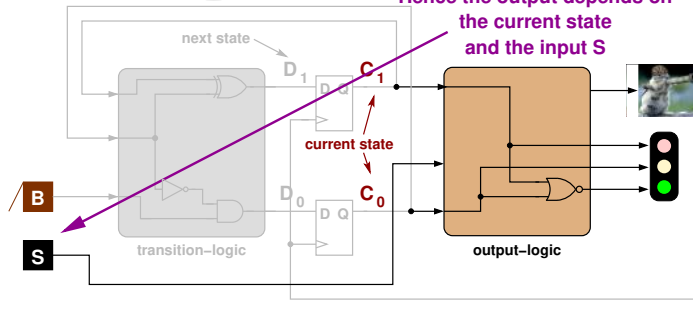
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

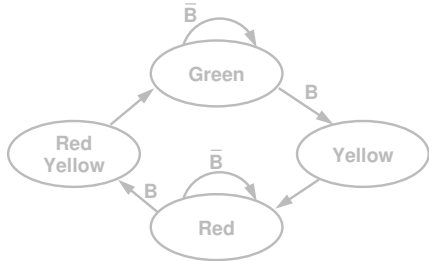
C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0

Hence the output depends on the current state and the input S



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

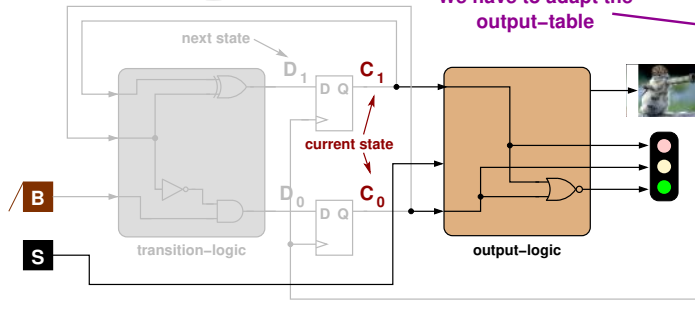
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

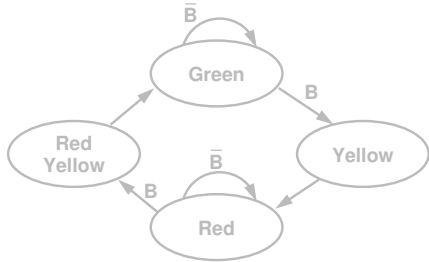
Output-table:

C ₁	C ₀	Red	Yellow	Green
0	0	0	0	1
0	1	0	1	0
1	0	1	0	0
1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

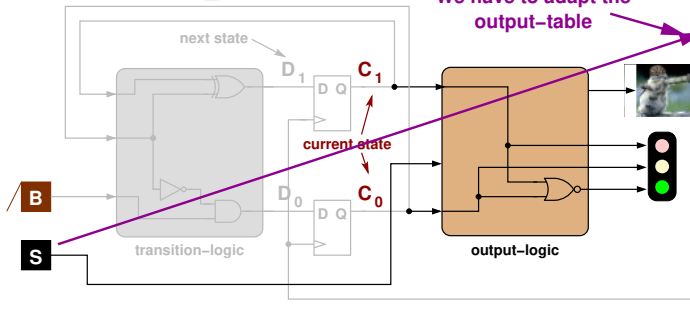
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

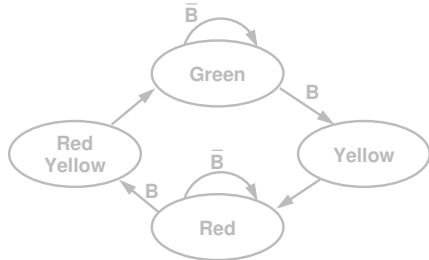
Output-table:

S	C ₁	C ₀	Red	Yellow	Green
0	0	0	0	0	1
0	1	0	0	1	0
1	0	1	1	0	0
1	1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

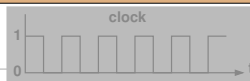
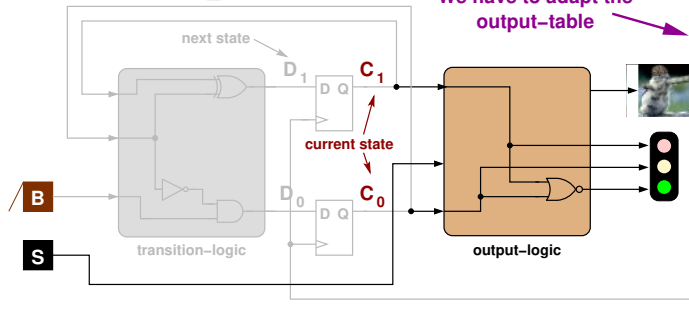
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

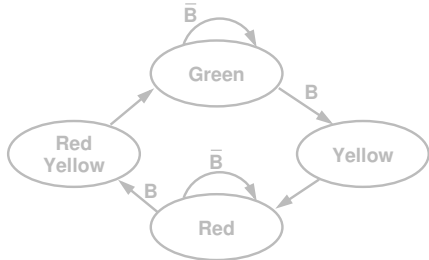
Output-table:

S	C ₁	C ₀	Red	Yellow	Green
0	0	0	0	0	1
0	0	1	0	1	0
0	1	0	1	0	0
0	1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

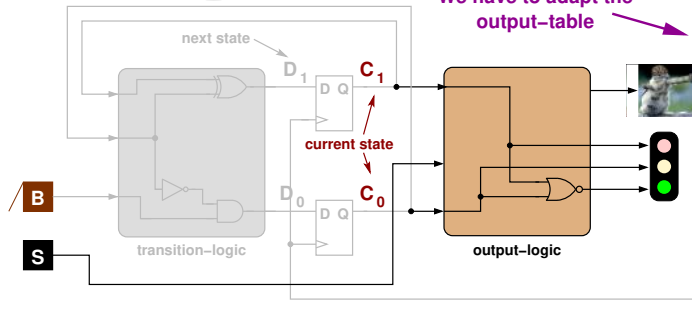
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

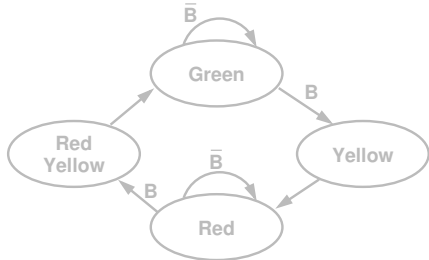
Output-table:

S	C ₁	C ₀	Red	Yellow	Green
0	0	0	0	0	1
0	0	1	0	1	0
0	1	0	1	0	0
0	1	1	1	1	0
1	0	0	0	0	1
1	0	1	0	1	0
1	1	0	1	0	0
1	1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

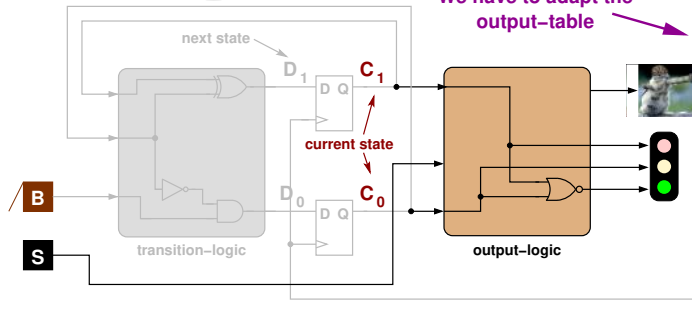
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

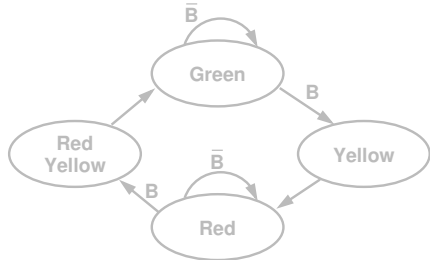
Output-table:

S	C ₁	C ₀	Red	Yellow	Green
0	0	0	0	0	1
0	0	1	0	1	0
0	1	0	1	0	0
0	1	1	1	1	0
1	0	0	0	0	1
1	0	1	0	1	0
1	1	0	1	0	0
1	1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

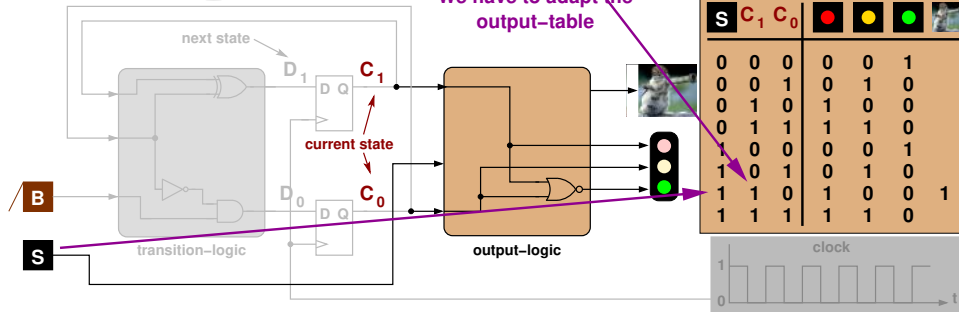
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

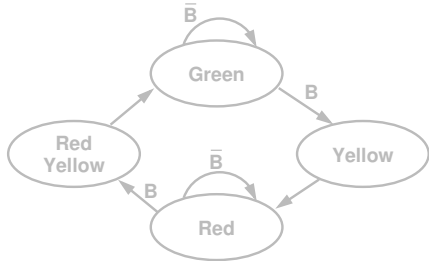
Output-table:

S	C ₁	C ₀	Red	Yellow	Green
0	0	0	0	0	1
0	0	1	0	1	0
0	1	0	1	0	0
0	1	1	1	1	0
1	0	0	0	0	1
1	0	1	0	1	0
1	1	0	1	0	0
1	1	1	1	1	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

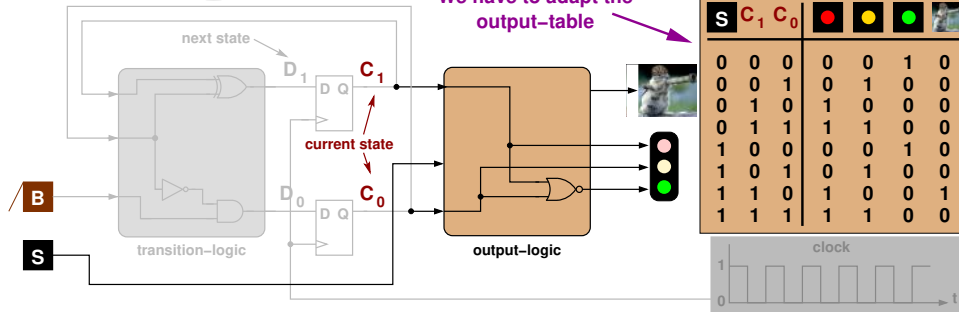
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

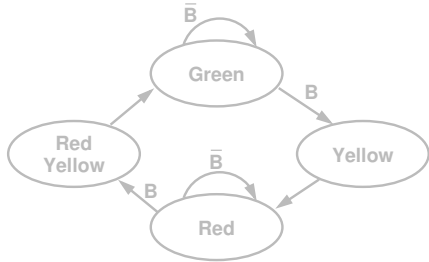
Output-table:

S	C ₁	C ₀	Red	Yellow	Green	Light
0	0	0	0	0	1	0
0	0	1	0	1	0	0
0	1	0	1	0	0	0
0	1	1	1	1	0	0
1	0	0	0	0	1	0
1	0	1	0	1	0	0
1	1	0	1	0	0	1
1	1	1	1	1	0	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

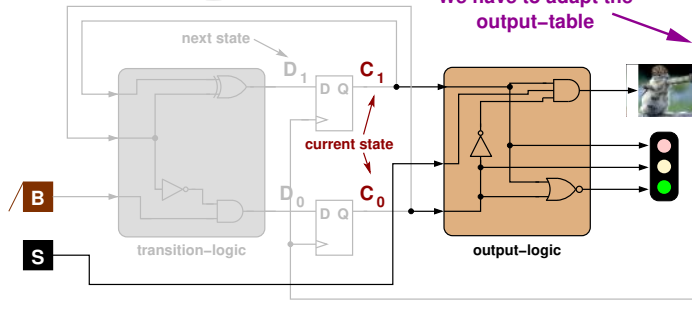
Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

We have to adapt the output-table

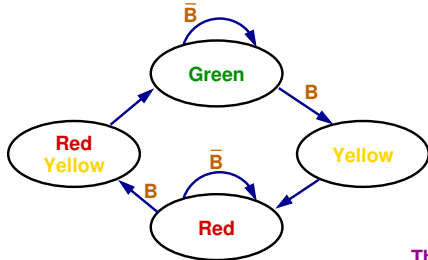
Output-table:

S	C ₁	C ₀	Red	Yellow	Green	Red Yellow
0	0	0	0	0	1	0
0	0	1	0	1	0	0
0	1	0	1	0	0	0
0	1	1	1	1	0	0
1	0	0	0	0	1	0
1	0	1	0	1	0	0
1	1	0	1	0	0	1
1	1	1	1	1	0	0



Mealy FSM

State Diagram:



Code-table:

State	Code
Green	00 _b
Yellow	01 _b
Red	10 _b
Red Yellow	11 _b

Transition-table:

B	C ₁	C ₀	D ₁	D ₀
-	0	1	1	0
-	1	1	0	0
0	1	0	1	0
1	1	0	1	1
0	0	0	0	0
1	0	0	0	1

Output-table:

S	C ₁	C ₀	Red	Yellow	Green	Mealy
0	0	0	0	0	1	0
0	0	1	0	1	0	0
0	1	0	1	0	0	0
0	1	1	1	1	0	0
1	0	0	0	0	1	0
1	0	1	0	1	0	0
1	1	0	1	0	0	1
1	1	1	1	1	0	0

This type of FSM is called a
Mealy FSM

