|  |  |  |  |
| --- | --- | --- | --- |
| **Pic24ep512gu810** | | | |
| **Pin#** | **Description** | | **Label** |
| **In Datasheet** | **In Schematic** |
| Reset | | | |
| 13 | ~MCLR | ~MCLR | ICSP\_~MCLR\_VPP\_TARGET |
| Clock | | | |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
| IO Pins | | | |
| 17 | TMS/RPI16/RA0 | TMS/RA0 | TMS/RA0 |
|  |  |  |  |
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**Document the IO pins in ports A.**

**If an IO pin is not connected, label the pin as NC.**