

# Arindam Bhattacharjee



Odalbakra main road House no  
172  
Ghy-781034  
Guwahati, Assam, India

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**Phone:** 8638986180

**PortfolioUrl :** RealArindam.github.io/MyPortfolio

## OBJECTIVE

Dedicated and focused individual aspiring to work in a reputed company to gain hands on experience in IT industry to enhance my skills and knowledge with learning enthusiasm and contribute towards the growth of the company.

## EDUCATION

### Bachelor of Engineering

Assam Engineering College

Year of passout : 2020

### HSSLC

Shrimanta Shankar Academy

Year of passout : 2016

### HSLC

St Stephen's School

Year of passout : 2014

## TECHNICAL SKILLS

**Programming Skills:** Html5 , Css3 , Bootstrap , Javascript , JQuery , NodeJs , ReactJs , C/C++, SASS, Using APIs.

**Tools/IDE:** Netlify, Firebase , Github , Visual Studio Code, Xilinx Vivado

**Database:** MongoDB

## PROJECT DETAILS

### Final Year Project:

**"Pedestrian Detection System for Advanced Driver Assistant Systems(ADAS)"** Using algorithms like HOG(Histogram of Oriented Gradients) and SVM(Support Vector Machines) using sliding window detection for miniscule support for better accuracy and fast detection .

### Mini Projects:

- Created an end to end basic responsive shadi website using pure Html, Css , bootstrap , Javascript , Nodejs , MongoDB database.

## LIST OF TRAININGS/INTERNSHIPS

### NARESH-I-TECHNOLOGIES:

**Oct 2021 - Feb 2022**

Hands-on training on UI full stack development working in technologies on the frontend such as html5, Css3, bootstrap Javascript, JQuery, Sass, including server side scripting with Nodejs, using mongodb database and express middleware.

**MANVI RD SUMMER INTERN:****Jun 2019 - July 2019**

Implementation of various algorithms using FPGA, and MATLAB , including cordic algorithms in DAC,generating slow clock for FPGA,along with real time signal processing algorithms with matlab.

**BSNL RTTC:****Jun 2018 - July 2018**

Completed Vocational training in the basics of Telecom from BSNL Regional Training Centre(RTTC) Guwahati.

**XESP:****Dec 2018 - Jan 2019**

Training on the basics of Verilog, VHDL and FPGA.

**PERSONAL  
ATTRIBUTES**

- Good Communication Skills
- Hardworking
- Quicklearner

**PERSONAL  
INFORMATION**

Father's Name - Late Ashok Bhattacharjee

DOB - 05/06/1997

Gender - Male

Nationality - Indian

Permanent Address - Odalbakra main road House no 172 Near Lachit

Path Religion - Hindu

Marital Status - Unmarried

**LANGUAGES  
KNOWN**

- English: Fluency - read, write, speak
- Hindi: Fluency - read, write, speak
- Bengali: Fluency - read, write, speak
- Assamese: Fluency - read, write, speak

**HOBBIES**

Photography, listening music , cooking

**Place:**Guwahati,Assam

**Date:**28/03/2022

**Signature**

Arindam Bhattacharjee