

IN5391 thru IN5399

PLASTIC SILICON RECTIFIERS

REVERSE VOLTAGE - 50 to 1000 Volts FORWARD CURRENT - 1.5 Amperes

FEATURES

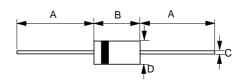
- Low cost
- Diffused junction
- Low forward voltage drop
- Low reverse leakage current
- High current capability
- The plastic material carries UL recognition 94V-0

MECHANICAL DATA

Case: JEDEC DO-15 molded plastic
Polarity: Color band denotes cathode
Weight: 0.015 ounces, 0.4 grams

• Mounting position : Any

DO-15



	DO-15						
Dim.	Min.	Max.					
Α	25.4	-					
В	5.80	7.60					
С	0.71 Ø	0.86 Ø					
D	2.60 Ø	3.60 Ø					
All Dimensions in millimeter							

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%

CHARACTERISTICS	SYMBOL	IN 5391	IN 5392	IN 5393	IN 5394	IN 5395	IN 5396	IN 5397	IN 5398	IN 5399	UNIT
Maximum Recurrent Peak Reverse Voltage	VRRM	50	100	200	300	400	500	600	800	1000	V
Maximum RMS Voltage	VRMS	35	70	140	210	280	350	420	560	700	V
Maximum DC Blocking Voltage	VDC	50	100	200	300	400	500	600	800	1000	V
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Lengths @TL=70°C	IAV)	1.5								Α	
Peak Forward Surge Current 8.3ms single half sine-wave super imposed on rated load (JEDEC Method)	I FSM	50						Α			
Maximum forward Voltage at 1.5A DC	VF	1.1							V		
Maximum DC Reverse Current @T _J =25°C at Rated DC Blocking Voltage @T _J =100°C	lR	5.0 50							uA		
Typical Junction Capacitance (Note 1)	ည	20						pF			
Typical Thermal Resistance (Note 2)	Røjl	26						°C/W			
Operating Temperature Range	TJ	-55 to +125						°C			
Storage Temperature Range	Тѕтс	-55 to +150						°C			

NOTES: 1.Measured at 1.0MHz and applied reverse voltage of 4.0V DC. 2.Thermal Resistance Junction to Lead.

REV. 1, 24-May-2000



