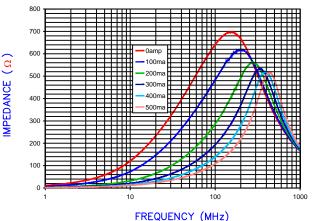
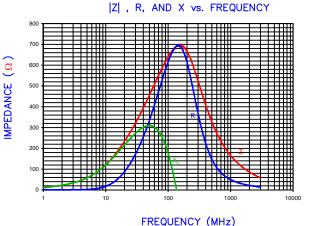
# HZ0805E601R-10

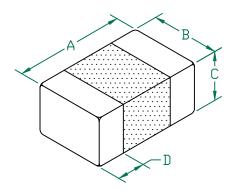
## PHYSICAL DIMENSIONS:

## Z vs FREQUENCY IMPEDANCE UNDER DC BIAS





AGILENT E4991A RF Impedance/Material Analyzer HP 16194A Test Fixture. TEST REF. 3228



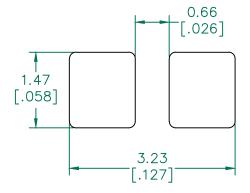
ELEC1	ELECTRICAL CHARACTERISTICS:										
Z @ 100N ( Ω )	1Hz	DCR $\left(\begin{array}{c}\Omega\end{array}\right)$	Rated Current								
Nominal	600										
Minimum	450										
Maximum	750	0.300	500 mA								

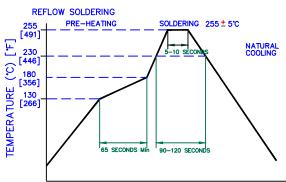
NOTES: UNLESS OTHERWISE SPECIFIED

- TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 4000 PCS/REEL, PAPER TAPE.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. TERMINATION FINISH IS 100% TIN.
- 4. OPERATING TEMP. RANGE: -55°C~+125°C (INCLUDING SELF-HEATING)
- 5. COSMETIC SPECIFICATION REFER TO WI-QA-124.

## LAND PATTERNS FOR REFLOW SOLDERING

### RECOMMENDED SOLDERING CONDITIONS





(For wave soldering, add 0.763 ([.030] to this dimension.)



	DIMENSIONS ARE IN mm [INCHE	This print is the property of Lair	d						
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				copies shall be made without the written consent of Laird Tech. A					
Ε	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	rights to design or invention are reserved.					
D	CHANGE SOLDERING TEMPERATURE	08/23/10	JUN	PROJECT/PART NUMBER:	ď	REV	CO-FIRE		DRAWN BY:
С	CHANGE TO PAPER TAPE	03/03/10	JUN	HZ0805E601R-10	-	E			JRK
В	UPDATE COMPANY LOGO ADD ROHS	03/21/08	JRK	DATE: 03/01/04	SCALE: N		TS	SHEET:	
Α	ORIGINAL DRAFT	03/01/04	JRK	CAD #	TOOL		13	1	of 1
REV	DESCRIPTION	DATE	INT	T   HZ0805E601R-10-E	-		-		01 1