

KKHWC0006L1010  
(CPLD-Based)  
(TOP VIEW)

NC	1	24	VCC
/VBI	2	23	V
/AL	3	22	f
B	4	21	g
C	5	20	h
E	6	19	a
/LT	7	18	b
/BI	8	17	c
/RBI	9	16	d
D	10	15	e
A	11	14	/RBO
GND	12	13	NC



In-Range	0	1	2	3	4	5	6	7	8	9
	10	11	12	13	14	15	16	17	18	19
Overflow	20	21	22	23	24	25	26	27	28	29

SIGNAL	DESCRIPTION
/AL	If HIGH, outputs will be HIGH when lit. If LOW, outputs will be LOW when lit.
/BI	Blanking input. When LOW, no segments will be lit regardless of other inputs.
/LT	Lamp test. When /BI is HIGH and /LT is low, all segments will be lit.
/RBI	Ripple blanking input. When A, B, C, D, E, and /RBI are LOW, c will not be lit.
/VBI	Overflow blanking input. If BCD input $\geq 20$ and /VBI is low, no segments will be lit.
A	Ones value (least significant bit) of BCD input.
B	Twos value of BCD input.
C	Fours value of BCD input.
D	Eights value of BCD input.
E	Sixteens value (most significant bit) of BCD input.
a	Output for segment a; a numeric value of 1 when alone. (See diagram.)
b	Output for segment b; with segments a and c, a numeric value of 2.
c	Output for segment c; a numeric value of 0 (or 2 with a and b).
d	Output for segment d; with segments abc, a numeric value of 3.
e	Output for segment e; with segments abcd, a numeric value of 4.
f	Output for segment f; a numeric value of 5 when alone. (See diagram.)
g	Output for segment g; with segment f, a numeric value of 10.
h	Output for segment h; with segments f and g, a numeric value of 15.
/RBO	Ripple blanking output. HIGH when BCD input is nonzero or /RBI is HIGH.
V	Overflow bit. LOW when BCD input $< 20$ . HIGH when BCD input $\geq 20$ .

