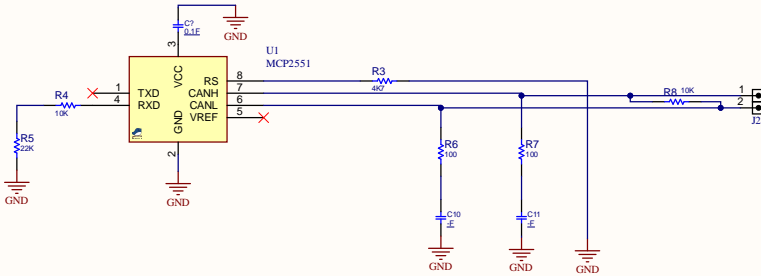

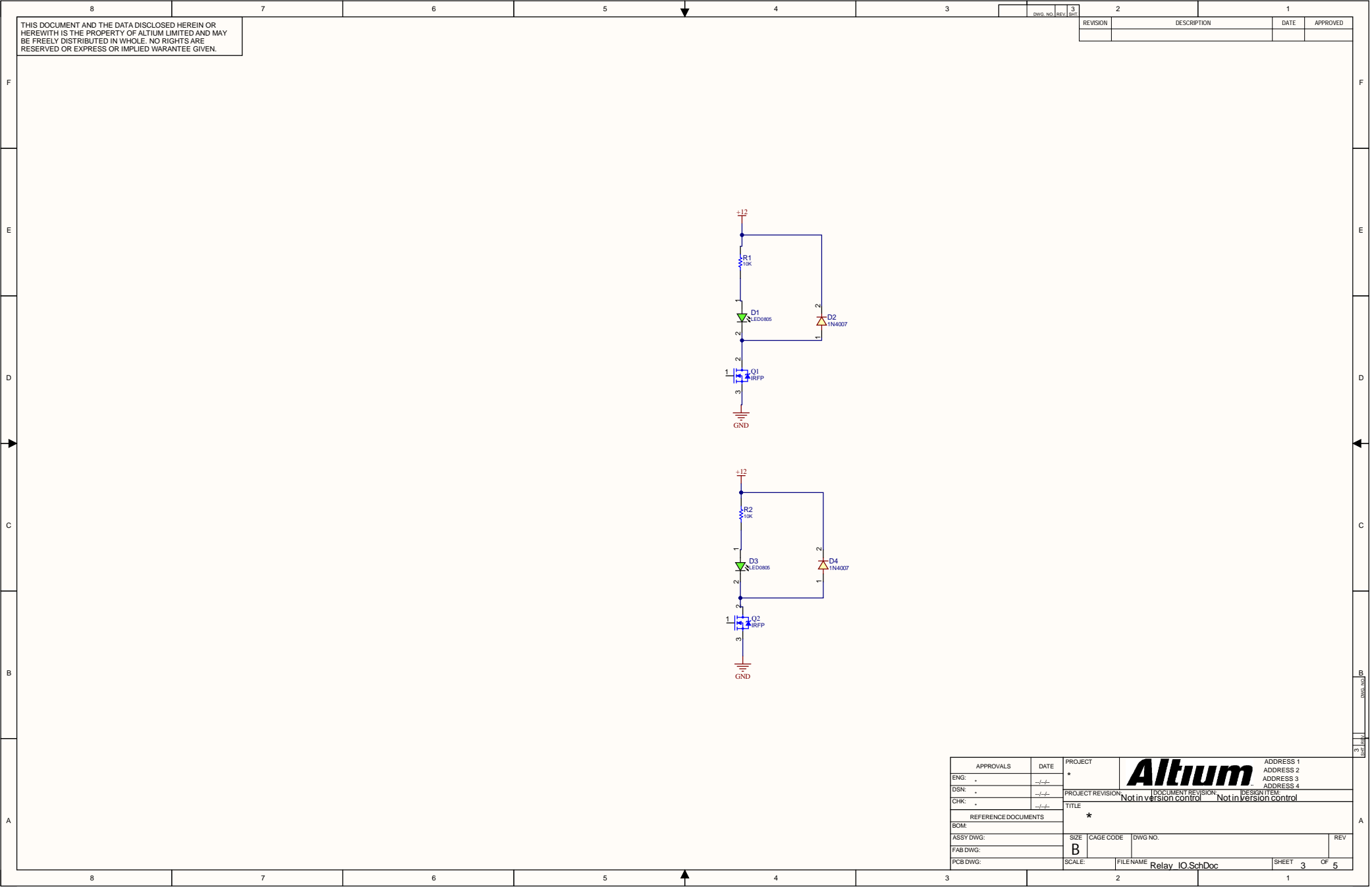


APPROVALS		DATE	PROJECT	Altum		ADDRESS 1
ENG: Rechard Nabo		19-02-2022	*			ADDRESS 2
DSN: Rechard Nabo		19-02-2022	PROJECT REVISION	DOCUMENT REVISION		ADDRESS 3
CHK: Rechard Nabo		19-02-2022		DESIGN ITEM		ADDRESS 4
			TITLE	Not in version control		
REFERENCE DOCUMENTS				Not in version control		
BOM:				Rechard Topping		
ASSY DWG:			SIZE	CAGE CODE	DWG NO.	REV
FAB DWG:			B			
PCB DWG:			SCALE:	FILE NAME	Processor Interface.SchDoc	SHEET 1 OF 5


REVISION	DESCRIPTION	DATE	APPROVED



APPROVALS		DATE		PROJECT				ADDRESS 1	
ENG: Richard Nabo		19-02-2022		Recharge Top				ADDRESS 2	
DSN: Richard Nabo		19-02-2022		PROJECT REVISION		DOCUMENT REVISION		ADDRESS 3	
CHK: Richard Nabo		19-02-2022		Not in version control		Not in version control		ADDRESS 4	
REFERENCE DOCUMENTS				TITLE		Recharge Topping			
BOM:				SIZE		CAGE CODE		DWG NO.	
ASSY DWG:				B:				REV	
FAB DWG:				SCALE		FILE NAME		SHEET	
PCB DWG:						CAN_IO_SchDoc		2 OF 5	




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REVISION	DESCRIPTION	DATE	APPROVED

APPROVALS		DATE		PROJECT				ADDRESS 1	
ENG: *		-/-/-		*				ADDRESS 2	
DSN: *		-/-/-						ADDRESS 3	
CHK: *		-/-/-						ADDRESS 4	
				PROJECT REVISION		DOCUMENT REVISION		PRESENTATION	
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REFERENCE DOCUMENTS				TITLE					
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BOM:				SIZE		CAGE CODE		DWG NO.	
ASSY DWG:				B				REV	
FAB DWG:									
PCB DWG:				SCALE:		FILE NAME		SHEET 4 OF 5	
						Digital_Io_SchDoc			

DWG. NO.	REV.	5 BHT	2	1
REVISION		DESCRIPTION		DATE

The left diagram illustrates a 5V supply circuit. It features a +12V input connected to the VIN pin of a UA78M05DC voltage regulator (U2). The GND pin is connected to ground, and the VO pin provides the +5V output. The input is filtered by capacitor C8 (10μF), and the output is filtered by capacitor C6 (10μF). A +5V output is shown, and the circuit is labeled "5V Supply @ 1A".

The right diagram illustrates a 3V3 supply circuit. It features a +5V input connected to the VI pin of a TCR3D voltage regulator (U3). The EN pin is connected to ground, and the VO pin provides the +3V3 output. The input is filtered by capacitor C3 (10μF), and the output is filtered by capacitor C4 (33μF). A +3V3 output is shown, and the circuit is labeled "3V3 Supply @ 500mA".

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REFERENCE DOCUMENTS				PROJECT REVISION		DOCUMENT REVISION		DESIGN FILE	
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ASSY DWG:				B				REV	
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PCB DWG:						Power_Supply.SchDoc		5 OF 5	