

Board Placement

Name

November 29, 2012

Placement Diagrams, automatically generated.

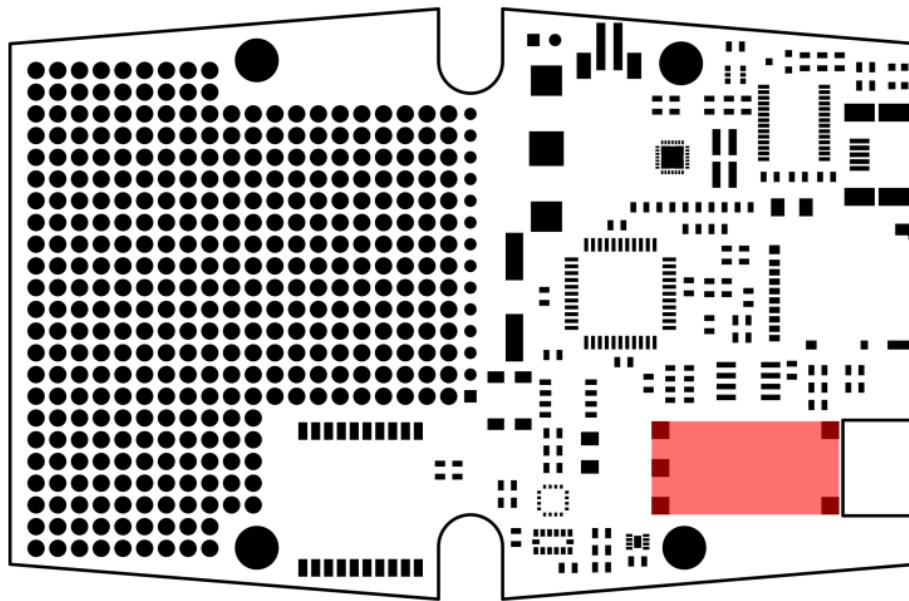
Contents

1	ELUMOASAQ2	2	3	0.01uF	4
2	CELL	3	4	0.1uF	5

5	0.22uF	6	15	Lipo Batt	16
6	1uF	7	16	CONN_14	17
7	4.7uF	8	17	Lipo Batt	18
8	10uF	9	18	NPN	19
9	22uF	10	19	75	20
10	RED	11	20	270	21
11	BLUE	12	21	1k	22
12	USB_SMD	13	22	3k	23
13	4.7uH	14	23	10k	24
14	CONN_1	15	24	20k	25

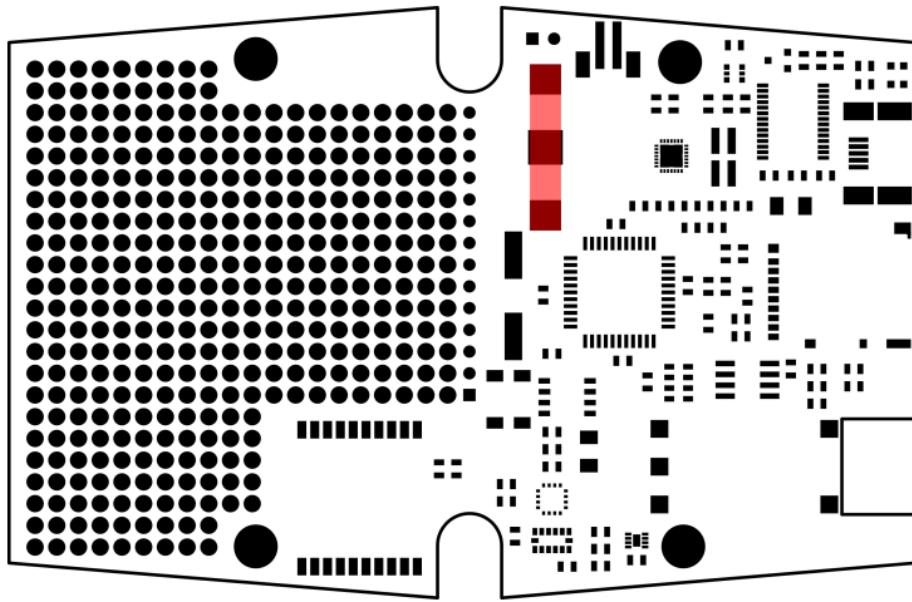
25	100k	26	34	74LVC2G07	35
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28	MICROSD	29	37	MAX17048	38
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1 ELUMOASAQ2



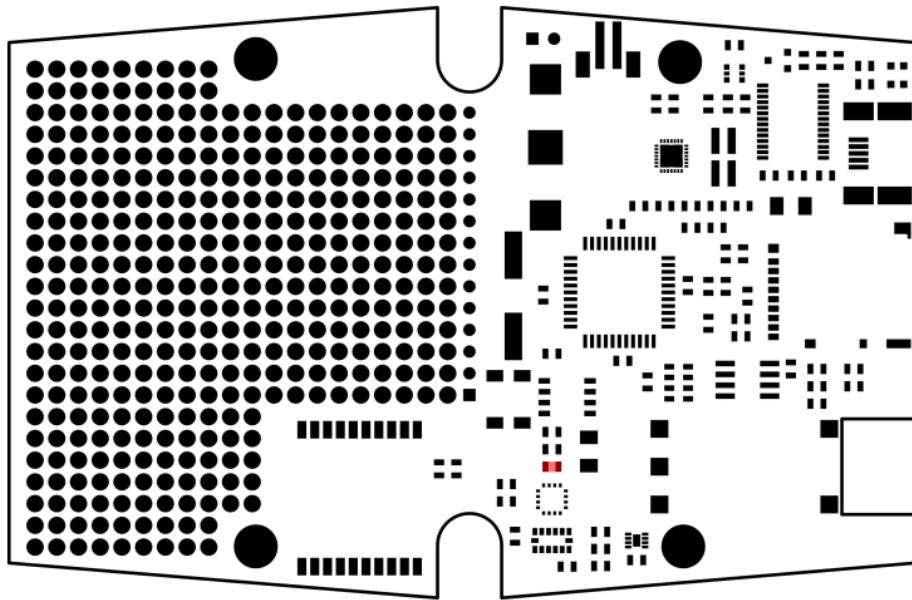
Place 1 of the **ELUMOASAQ2** component with the **ELUM-XX-SA** footprint at positions:
B1

2 CELL



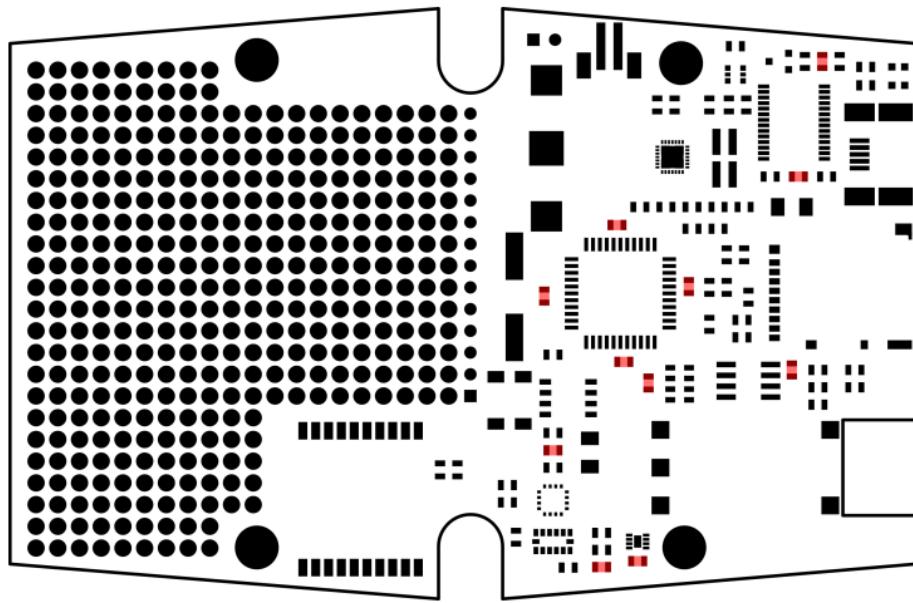
Place 1 of the **CELL** component with the **CR1225** footprint at positions:
BT1

3 0.01uF



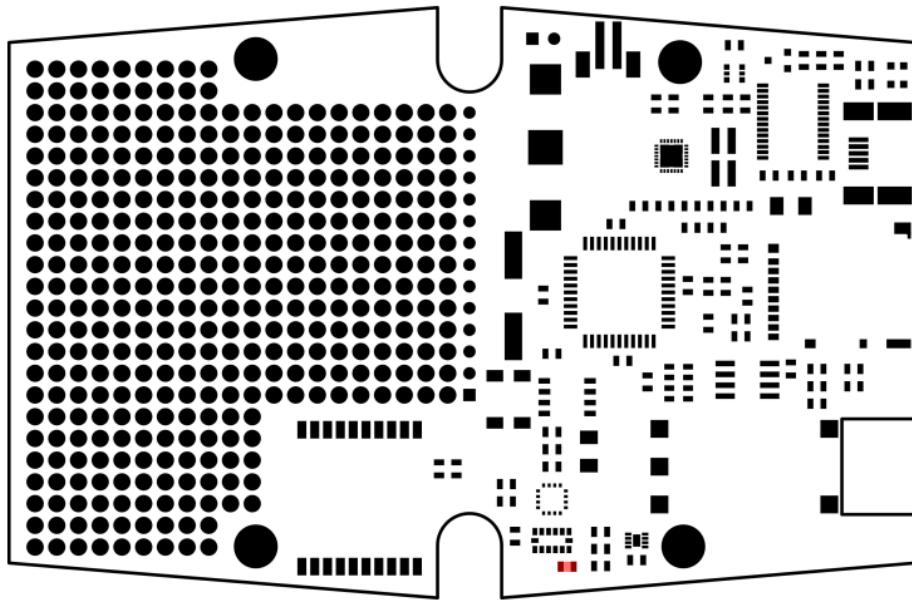
Place **1** of the **0.01uF** component with the **SM0603_Capa** footprint at positions:
C15

4 0.1uF



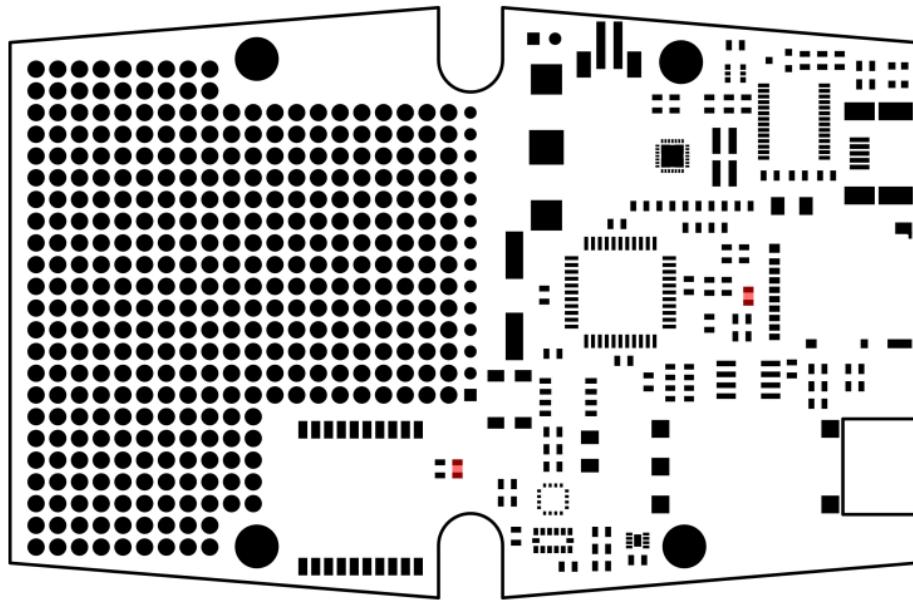
Place **11** of the **0.1uF** component with the **SM0603_Capa** footprint at positions:
C2 C4 C5 C6 C9 C10 C11 C12 C18 C25 C26

5 0.22uF



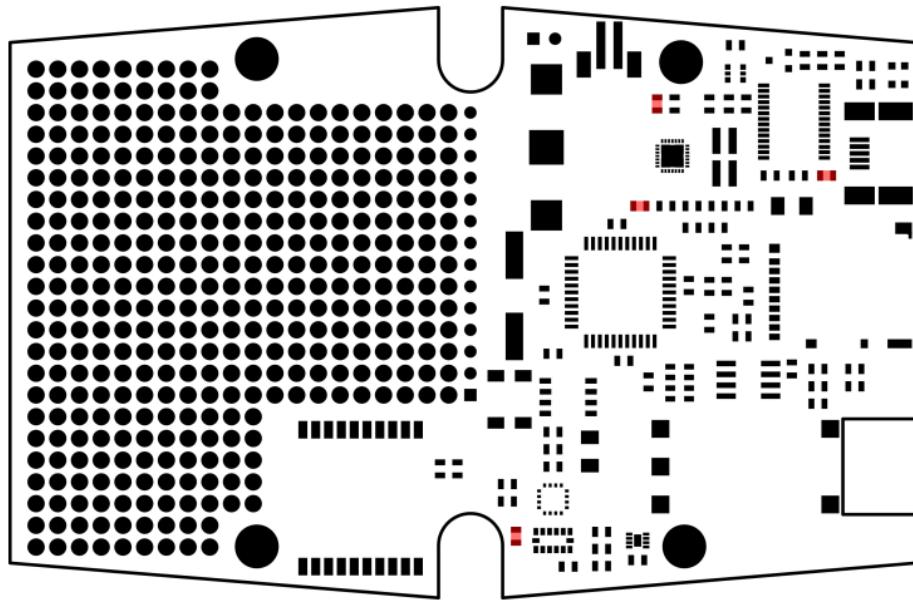
Place 1 of the **0.22uF** component with the **SM0603_Capa** footprint at positions:
C17

6 1uF



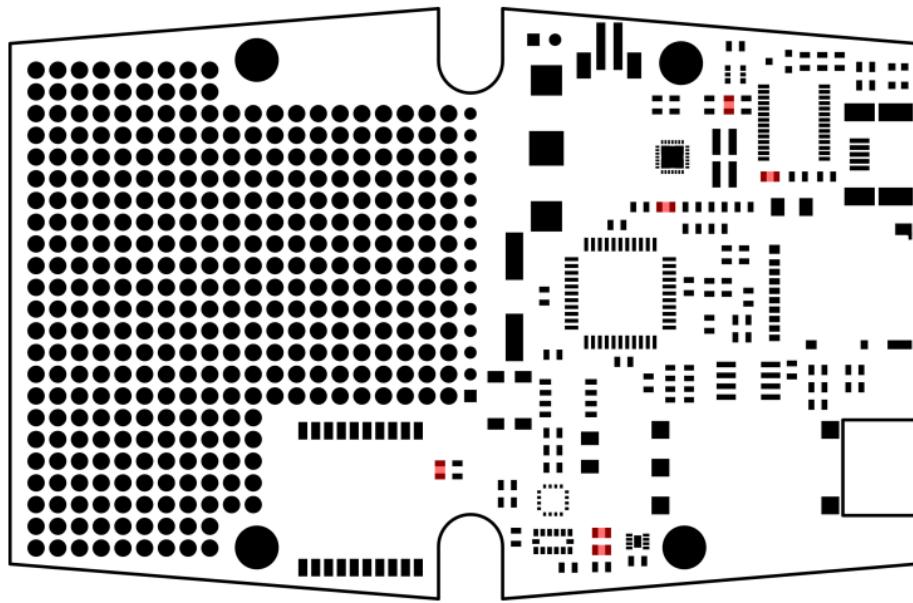
Place **2** of the **1uF** component with the **SM0603_Capa** footprint at positions:
C8 C13

7 4.7uF



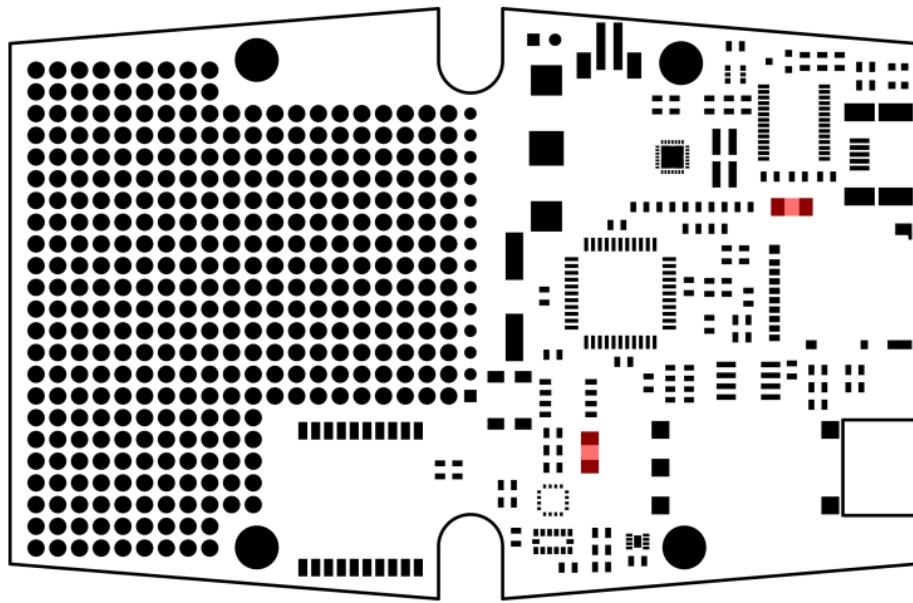
Place 4 of the **4.7uF** component with the **SM0603_Capa** footprint at positions:
C16 C20 C22 C27

8 10uF



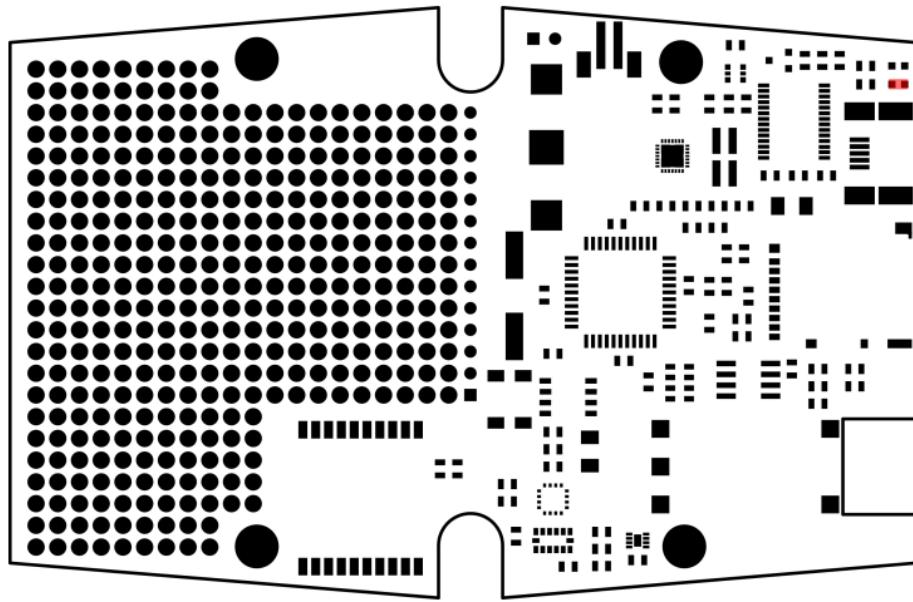
Place **6** of the **10uF** component with the **SM0603_Capa** footprint
at positions:
C1 C3 C14 C21 C23 C24

9 22uF



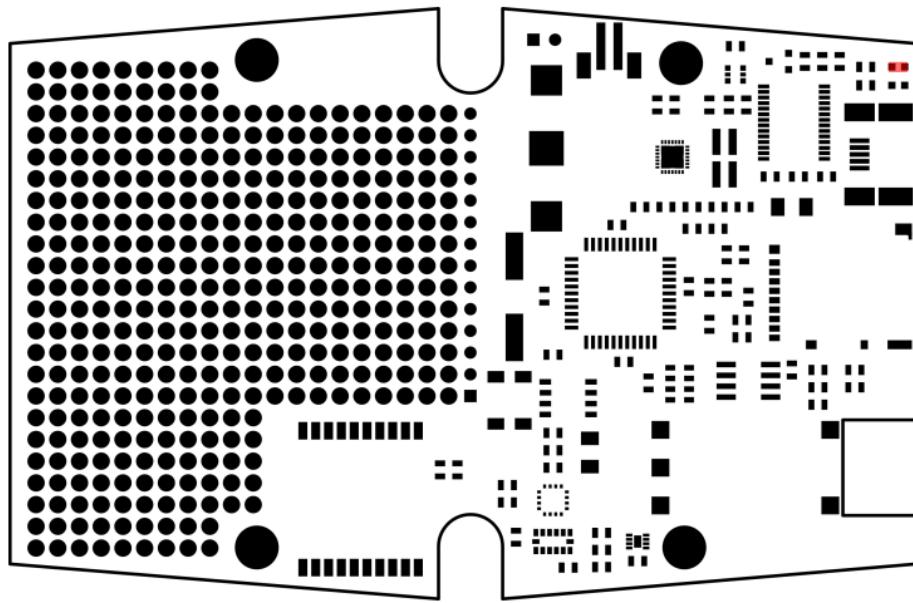
Place **2** of the **22uF** component with the **SM1206POL** footprint at positions:
C7 C19

10 RED



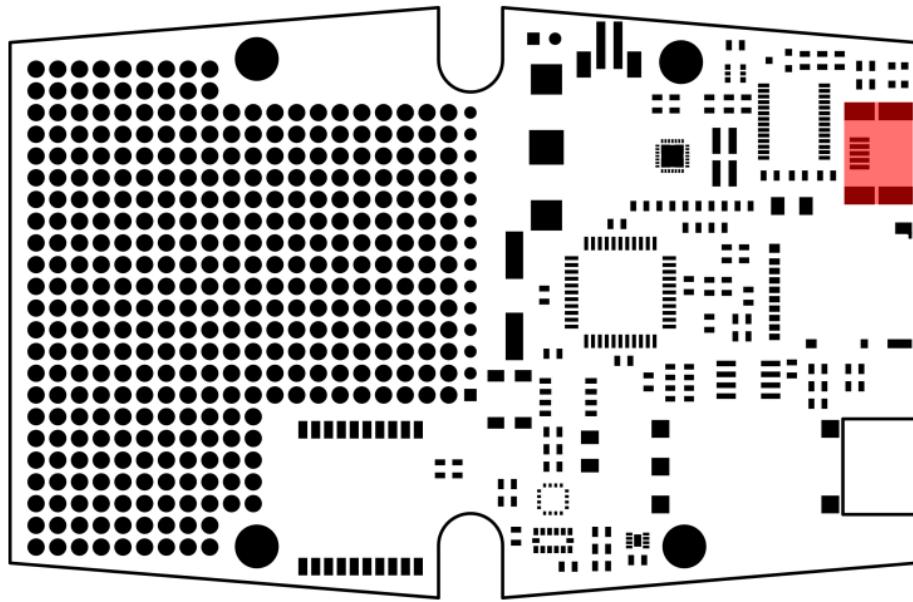
Place **1** of the **RED** component with the **LED-0603** footprint at positions:
D1

11 BLUE



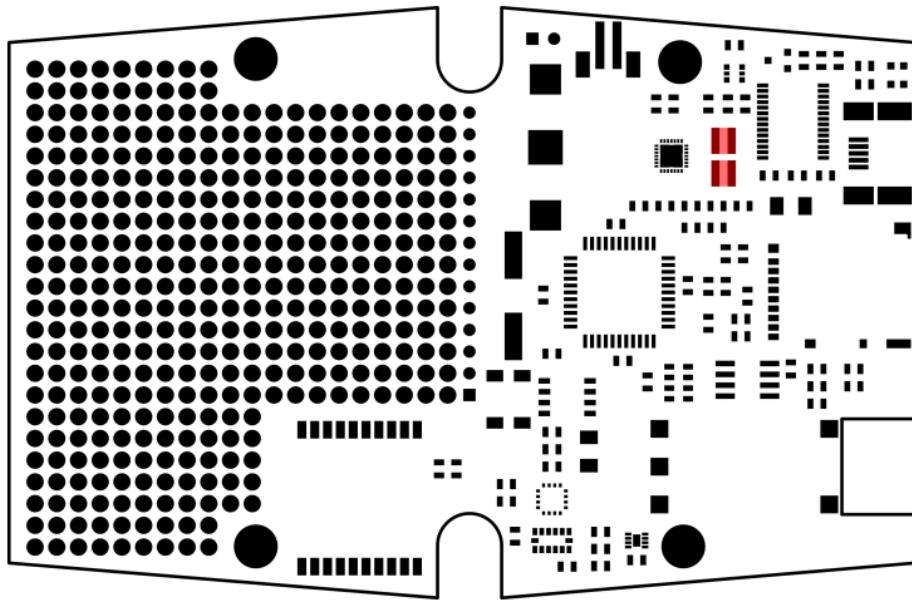
Place **1** of the **BLUE** component with the **LED-0603** footprint at positions:
D2

12 USB_SMD



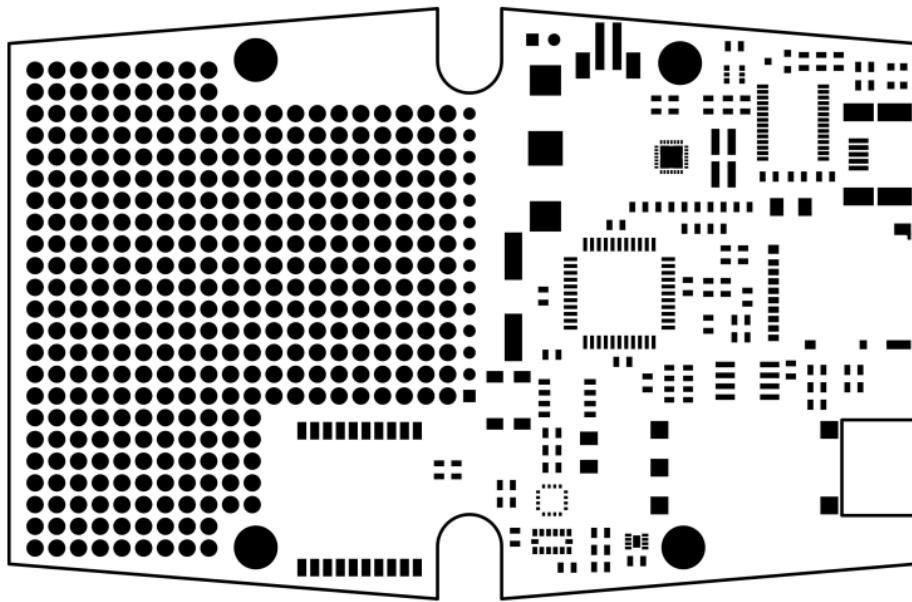
Place **1** of the **USB_SMD** component with the **Mini-B-USB** footprint at positions:
J1

13 4.7uH



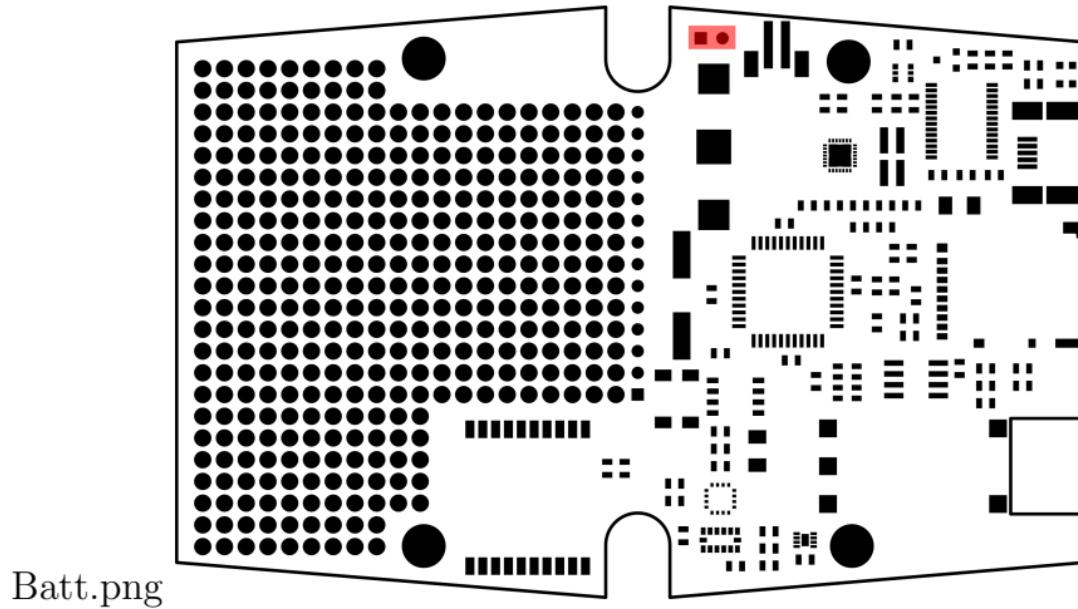
Place **2** of the **4.7uH** component with the **NR3012** footprint at positions:
L1 L2

14 CONN_1



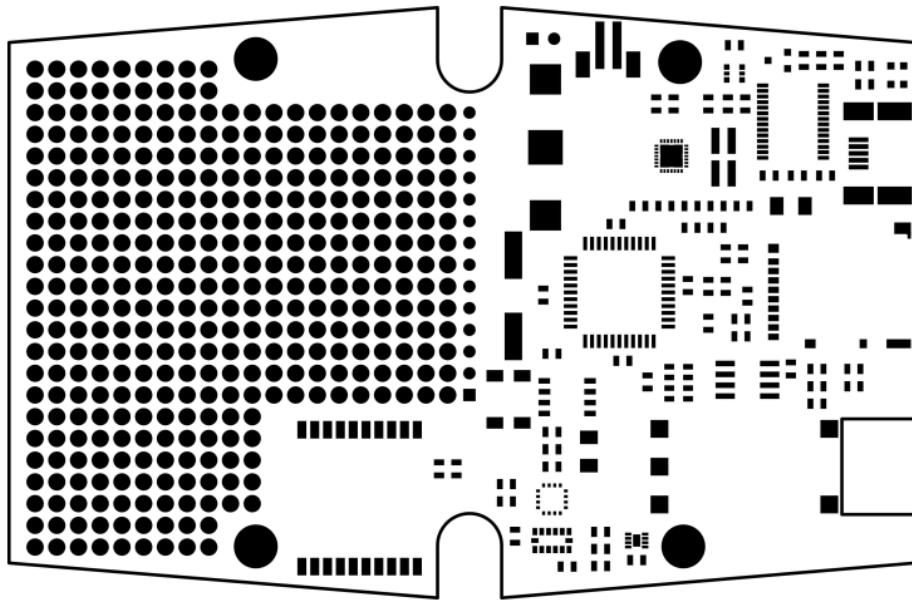
Place **4** of the **CONN_1** component with the **4-40** footprint at positions:
P4 P5 P6 P7

15 Lipo Batt



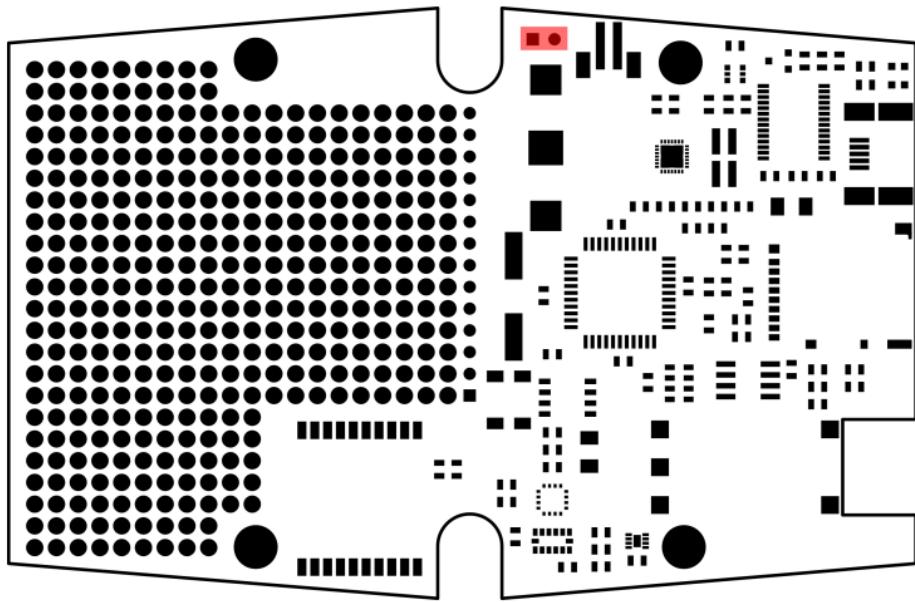
Place **1** of the **Lipo Batt** component with the **JST-2** footprint at positions:
P3

16 CONN_14



Place **1** of the **CONN_14** component with the **SIL-14** footprint at positions:
P1

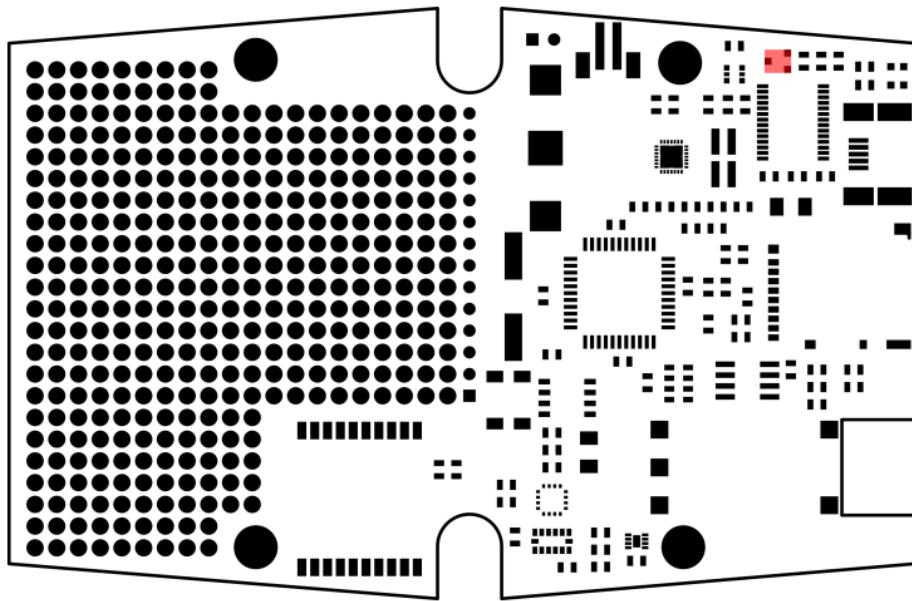
17 Lipo Batt



Batt.png

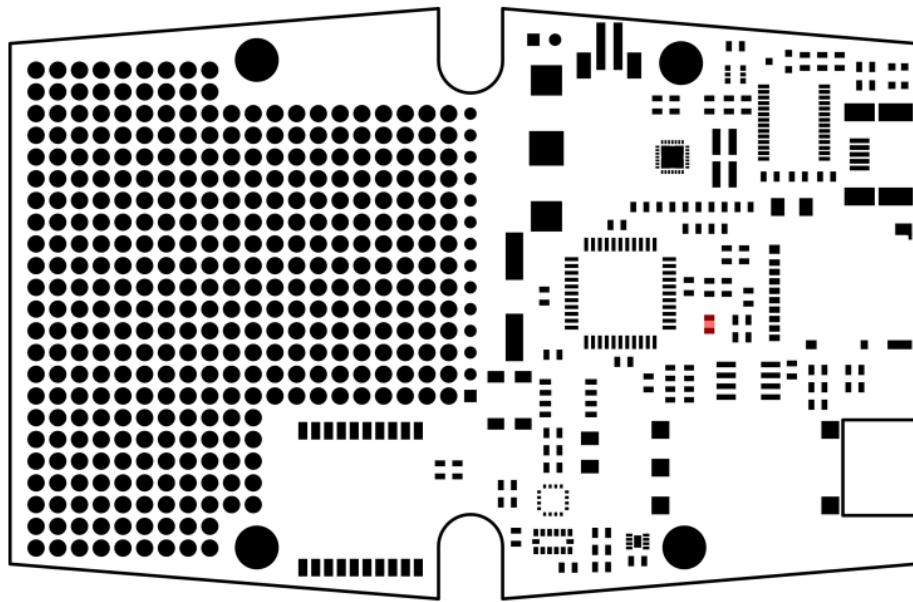
Place **1** of the **Lipo Batt** component with the **SIL-2** footprint at positions:
P2

18 NPN



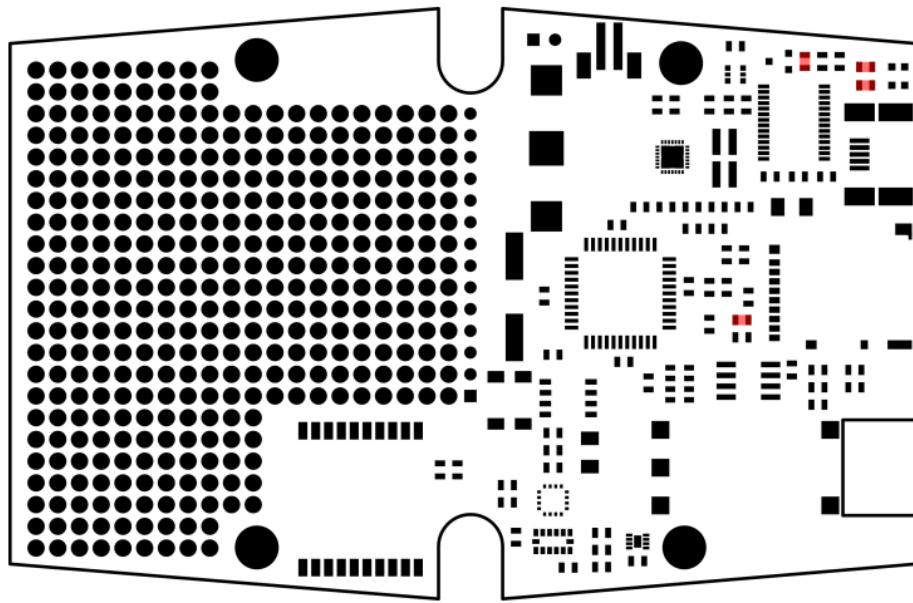
Place **1** of the **NPN** component with the footprint at positions:
Q1

19 75



Place 1 of the **75** component with the **SM0603** footprint at positions:
R13

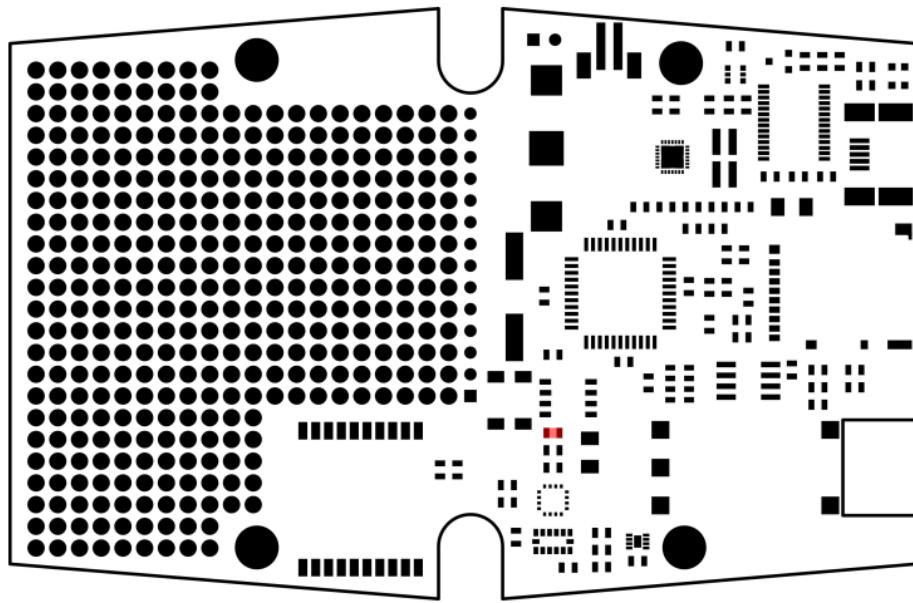
20 270



Place 4 of the **270** component with the **SM0603** footprint at positions:

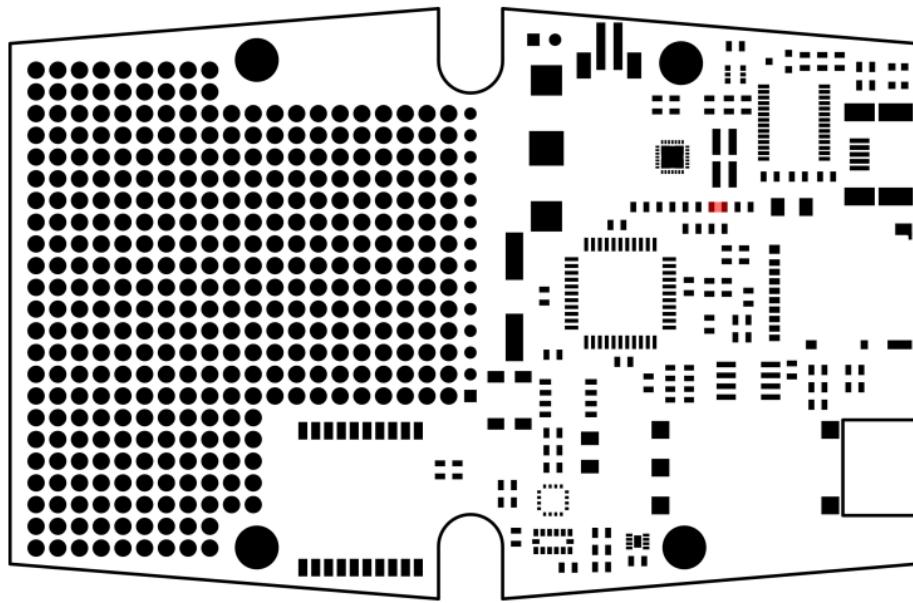
R5 R25 R26 R28

21 1k



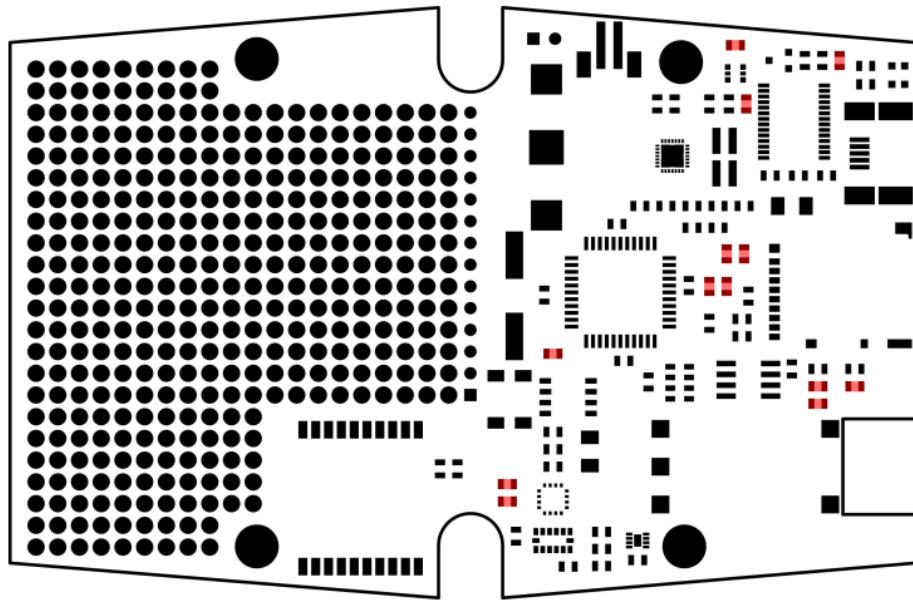
Place 1 of the **1k** component with the **SM0603** footprint at positions:
R9

22 3k



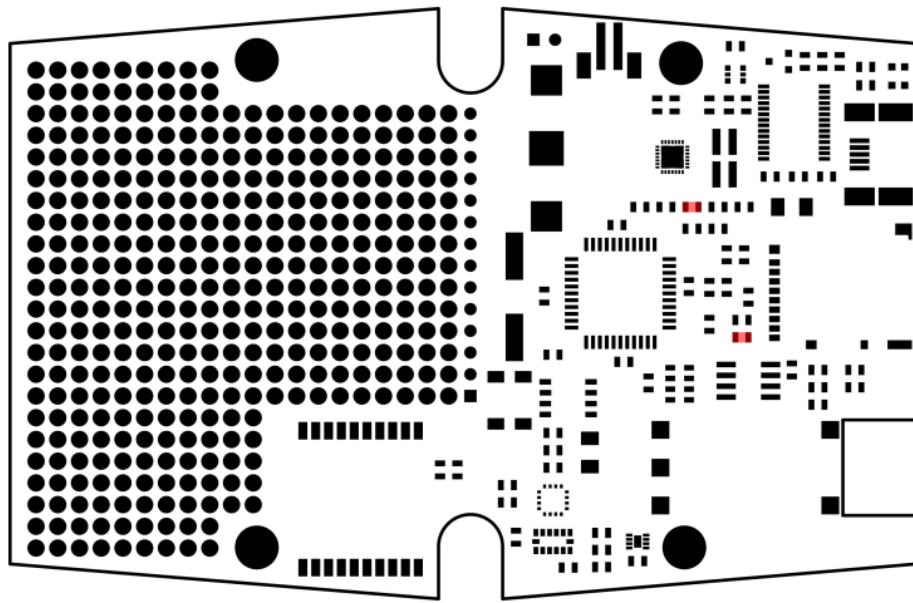
Place 1 of the **3k** component with the **SM0603** footprint at positions:
R17

23 10k



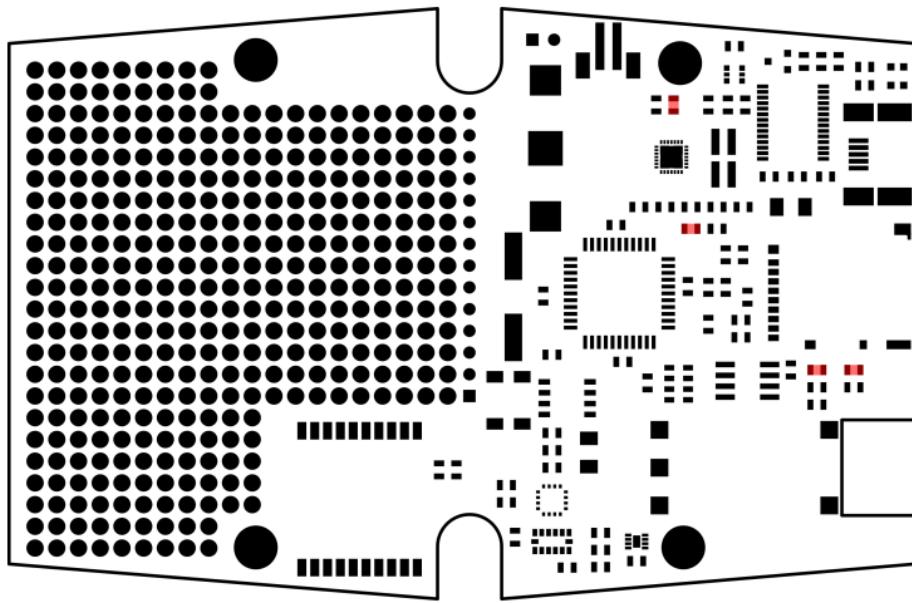
Place **13** of the **10k** component with the **SM0603** footprint at positions:
R1 R2 R3 R4 R6 R7 R10 R14 R15 R16 R24 R27 R29

24 20k



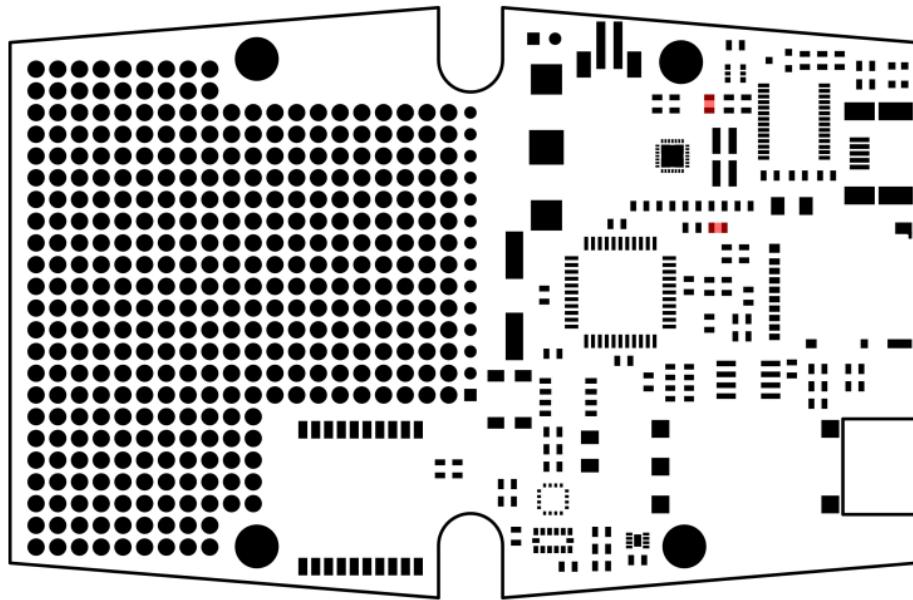
Place **2** of the **20k** component with the **SM0603** footprint at positions:
R8 R11

25 100k



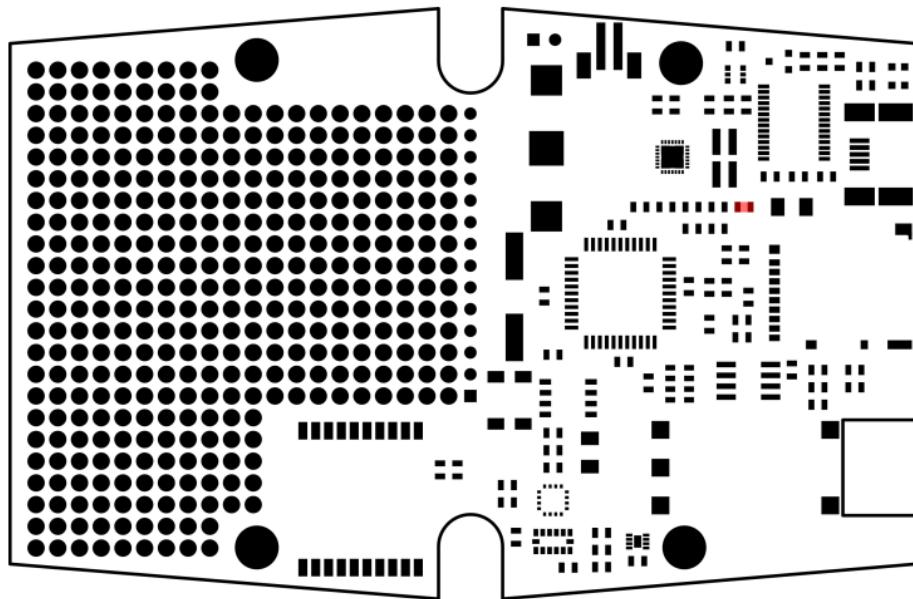
Place 4 of the **100k** component with the **SM0603** footprint at positions:
R18 R19 R21 R23

26 230k



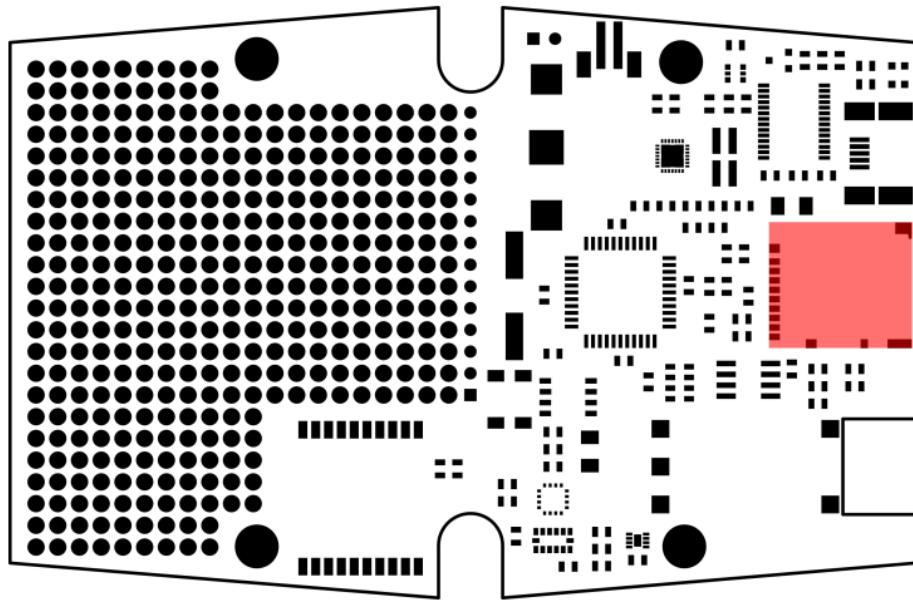
Place **2** of the **230k** component with the **SM0603** footprint at positions:
R20 R22

27 470k



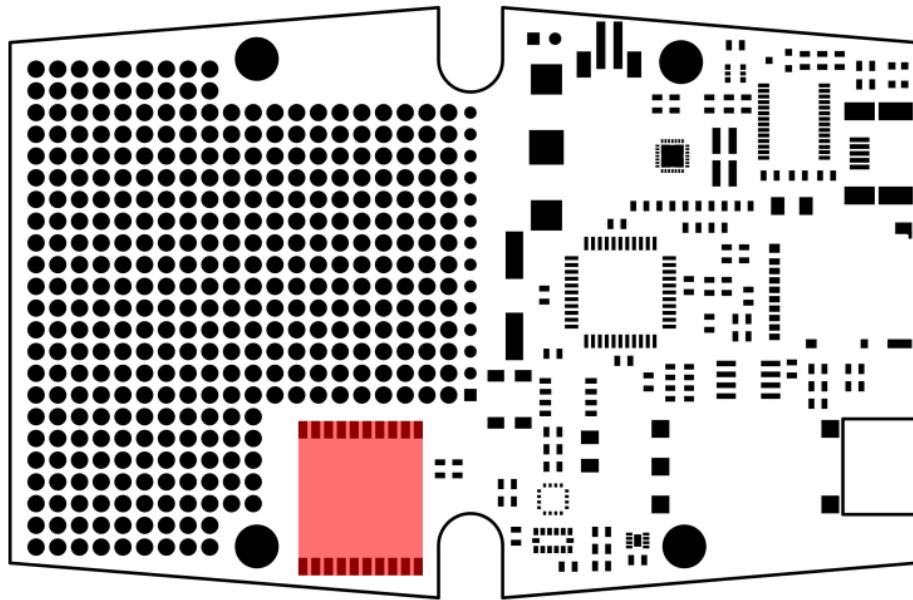
Place **1** of the **470k** component with the **SM0603** footprint at positions:
R12

28 MICROSD



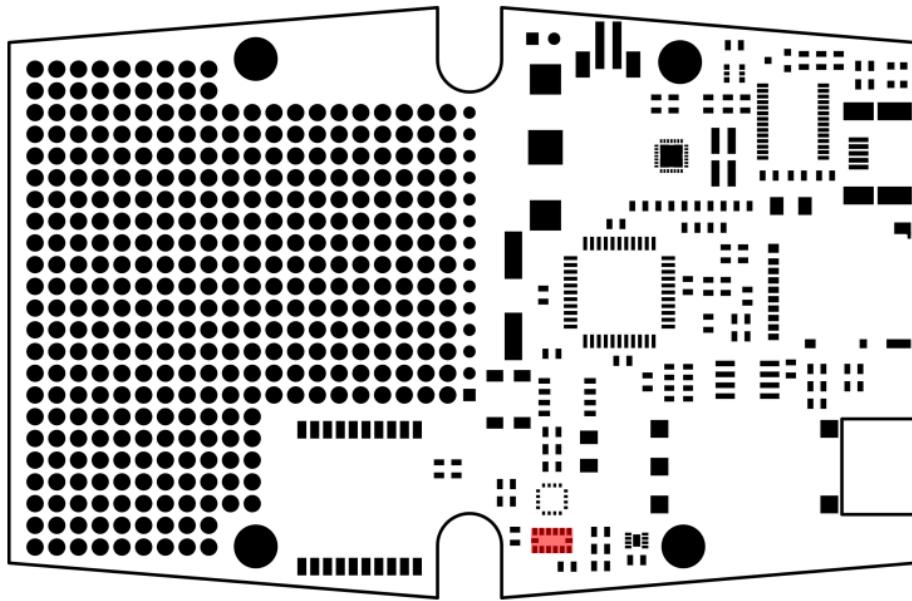
Place 1 of the **MICROSD** component with the **DM3AT-SF-PEJM5** footprint at positions:
U3

29 FGPMMOPA6H



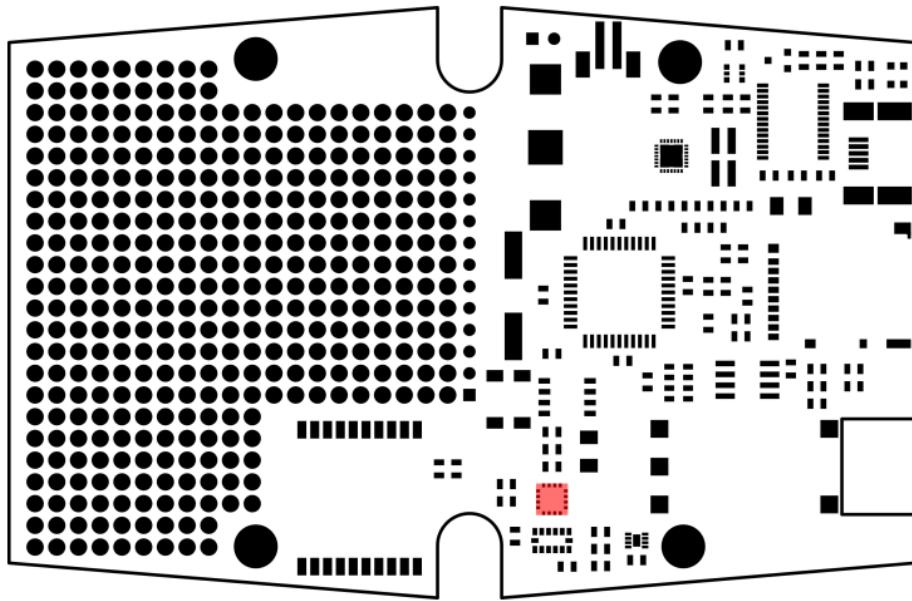
Place 1 of the **FGPMMOPA6H** component with the **FGPM-MOPA6H** footprint at positions:
U2

30 LSM303DLHC



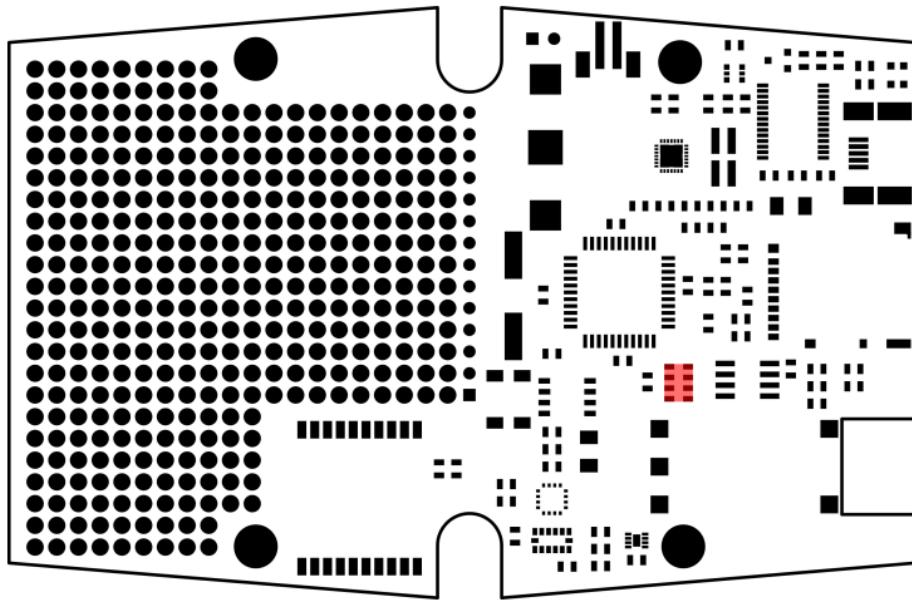
Place 1 of the **LSM303DLHC** component with the **LGA-14** footprint at positions:
U4

31 L3GD20



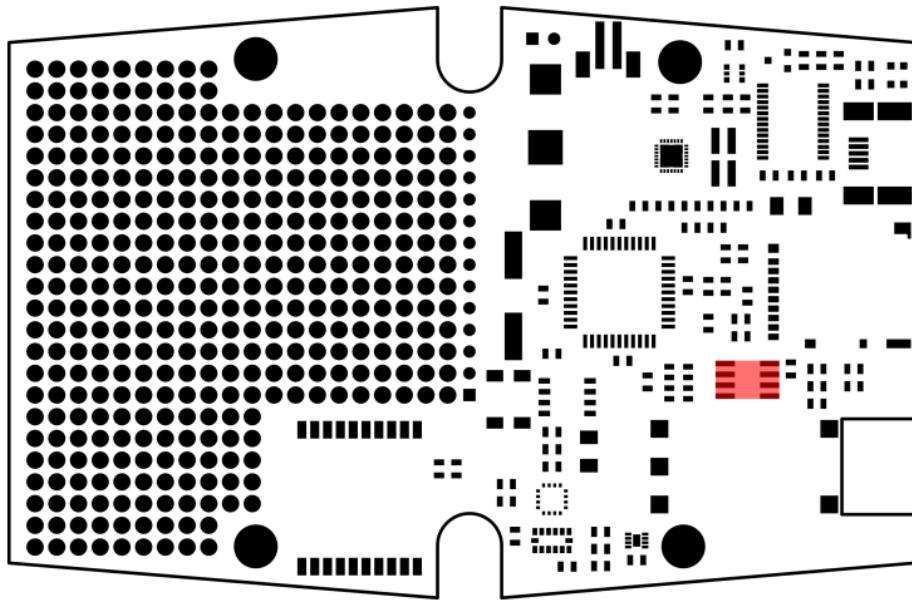
Place **1** of the **L3GD20** component with the **LGA-16** footprint at positions:
U5

32 MS5611



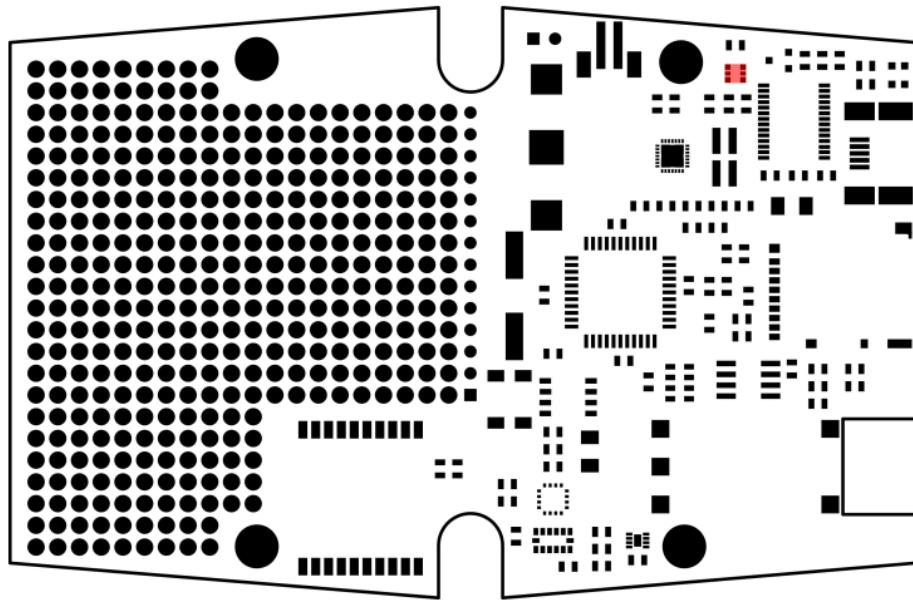
Place **1** of the **MS5611** component with the **MS5611** footprint at positions:
U6

33 AT24C512



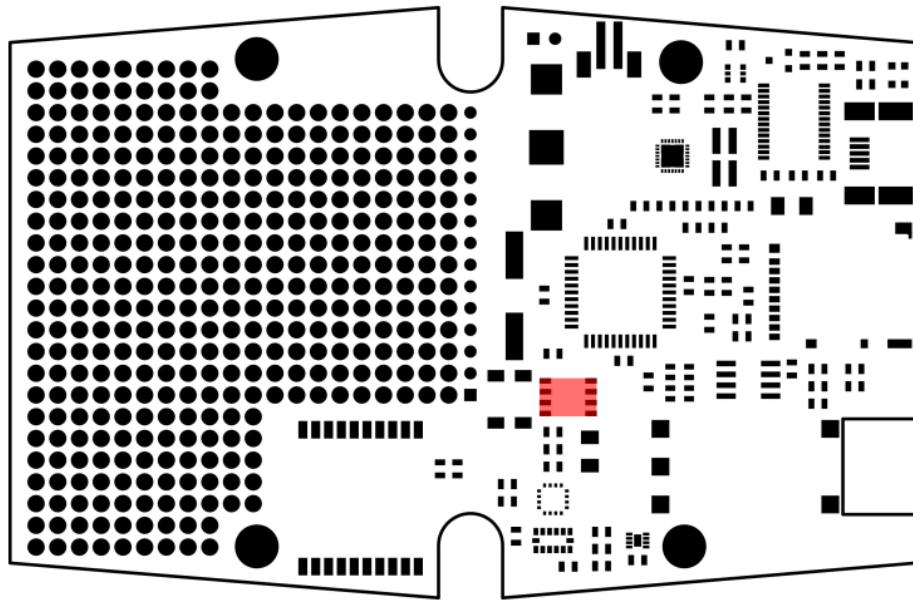
Place **1** of the **AT24C512** component with the **SOIC-8** footprint at positions:
U9

34 74LVC2G07



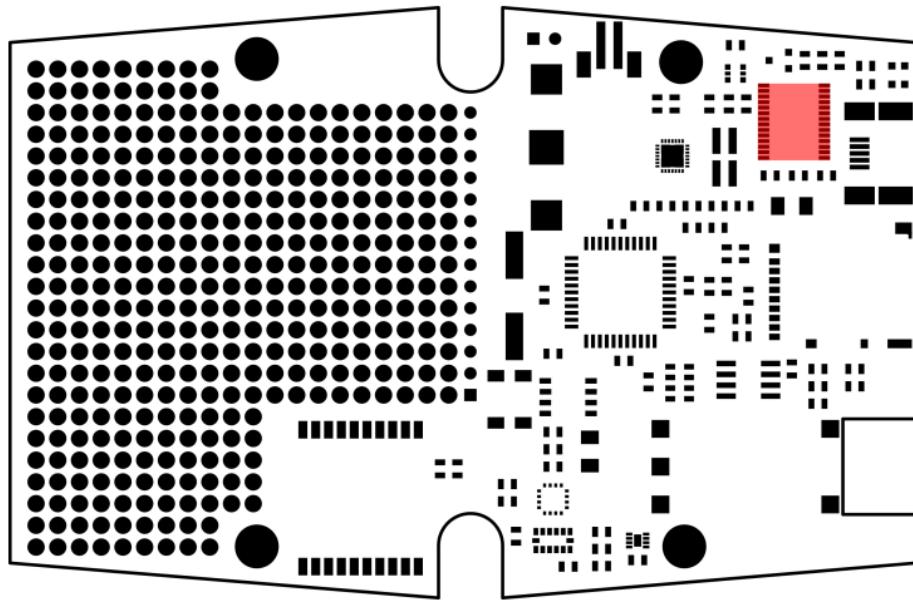
Place 1 of the **74LVC2G07** component with the **SOT363-6** footprint at positions:
U12

35 PCF8523



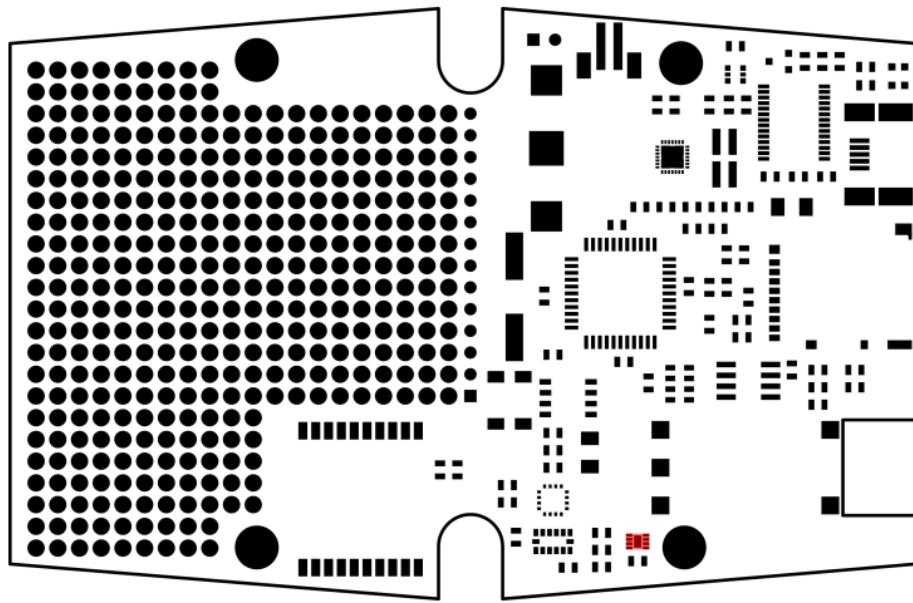
Place **1** of the **PCF8523** component with the **SOT96-1** footprint at positions:
U7

36 FT232RL



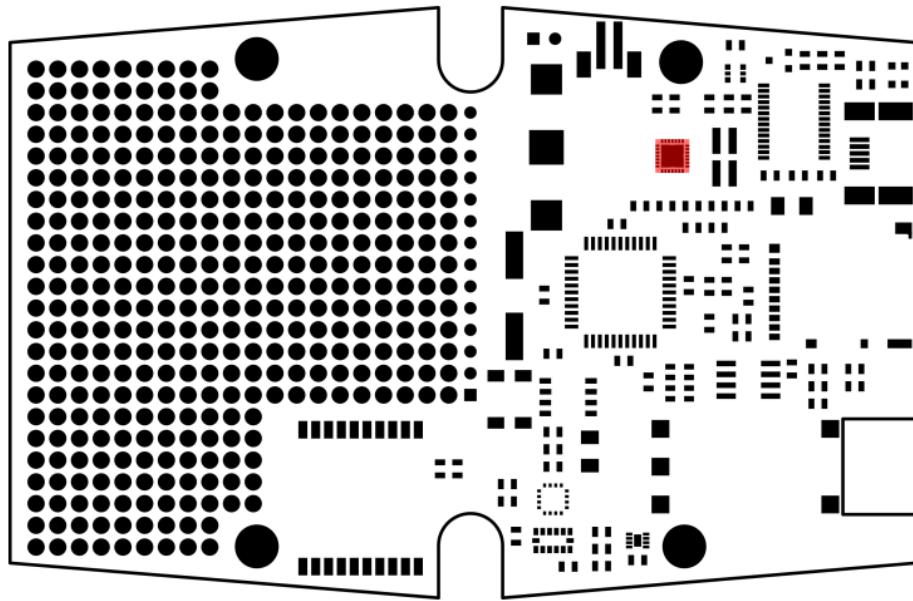
Place 1 of the **FT232RL** component with the **SSOP28** footprint at positions:
U11

37 MAX17048



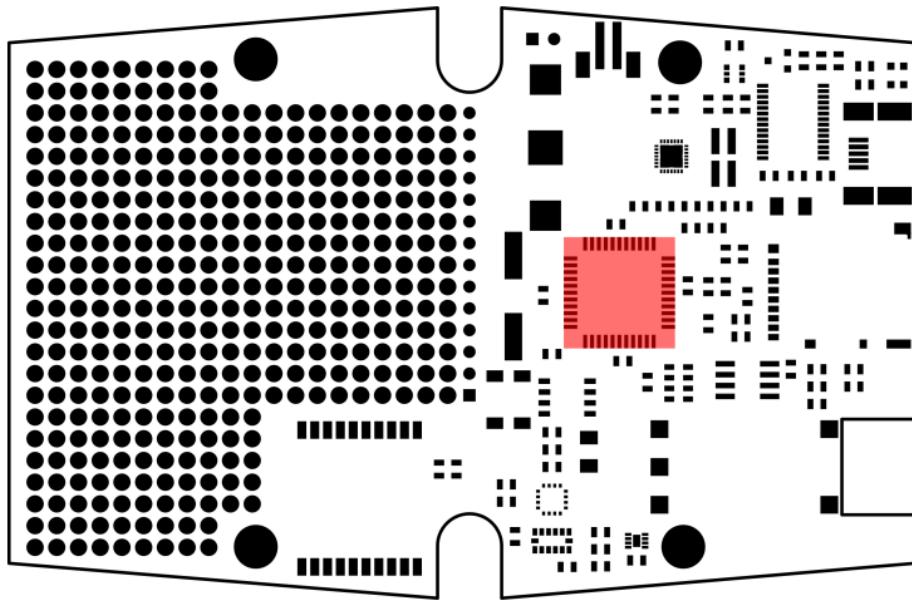
Place 1 of the **MAX17048** component with the **TDFN-EP8** footprint at positions:
U8

38 MAX8819



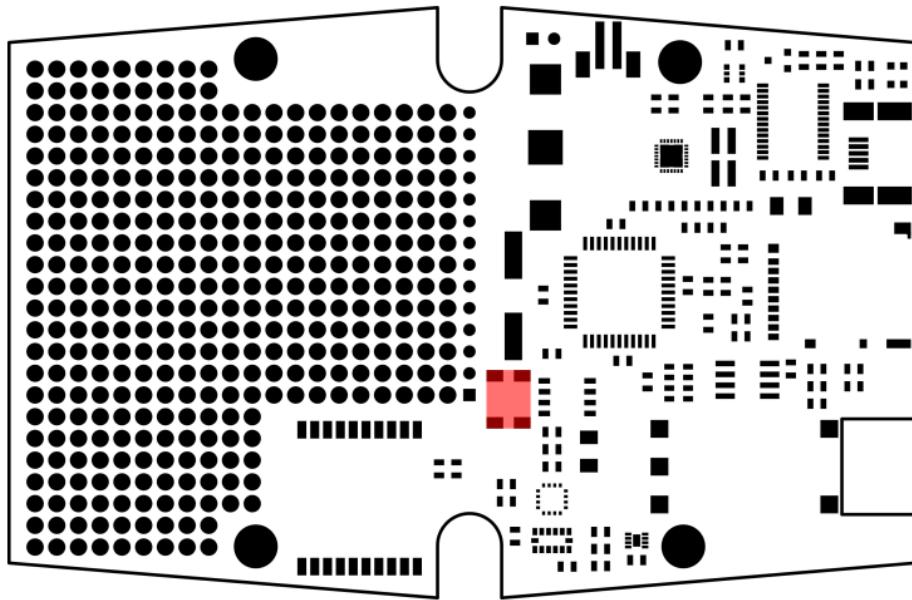
Place 1 of the **MAX8819** component with the **TQFN-EP28** footprint at positions:
U10

39 P8X32A-Q44



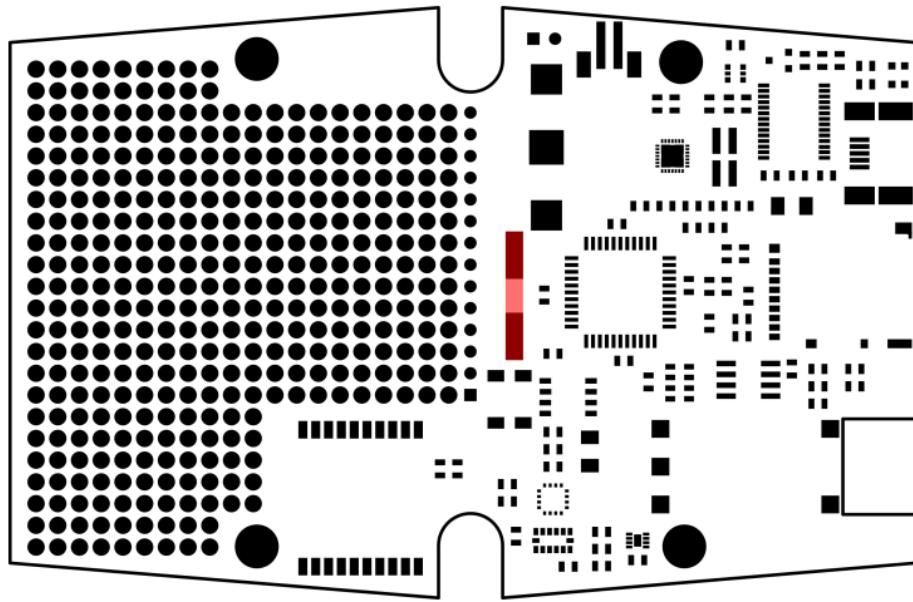
Place **1** of the **P8X32A-Q44** component with the **TQFP44** footprint at positions:
U1

40 CM200C



Place **1** of the **CM200C** component with the **CM200C** footprint at positions:
X2

41 CRYSTAL



Place 1 of the **CRYSTAL** component with the **HC-49US** footprint at positions:
X1