* There is possibility of an error howing occured either in the wiring or my measuring techniques > I am unable to determine it at this time.

* Though "self-sustained" induction (what is show on pg 0/2) seems promising, I am insure if we will continue on this pash or move to a new method or platform for V3. Abd.

* Other rectification methods are being traditional conservation of the capacitors (-11-); but I am seeing traditional conservation of the (<100% ESS) results.

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