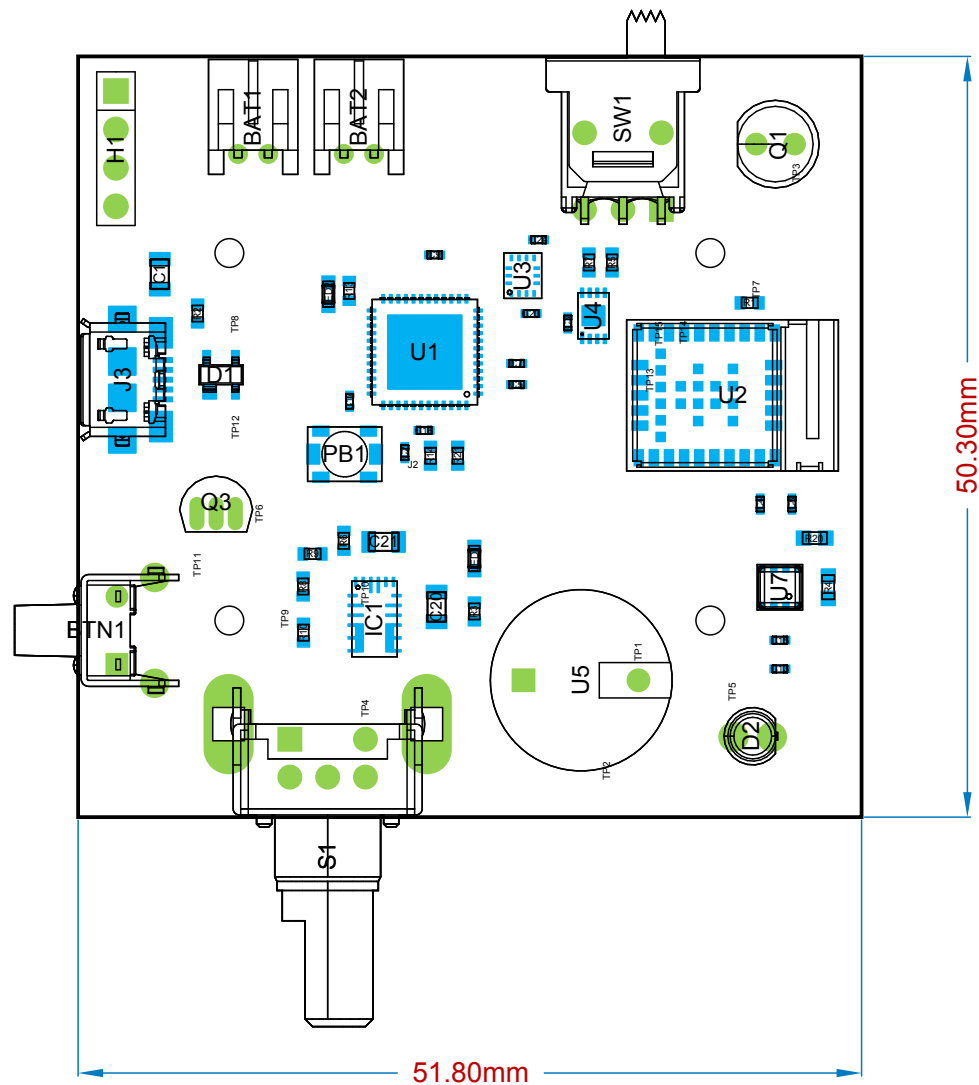
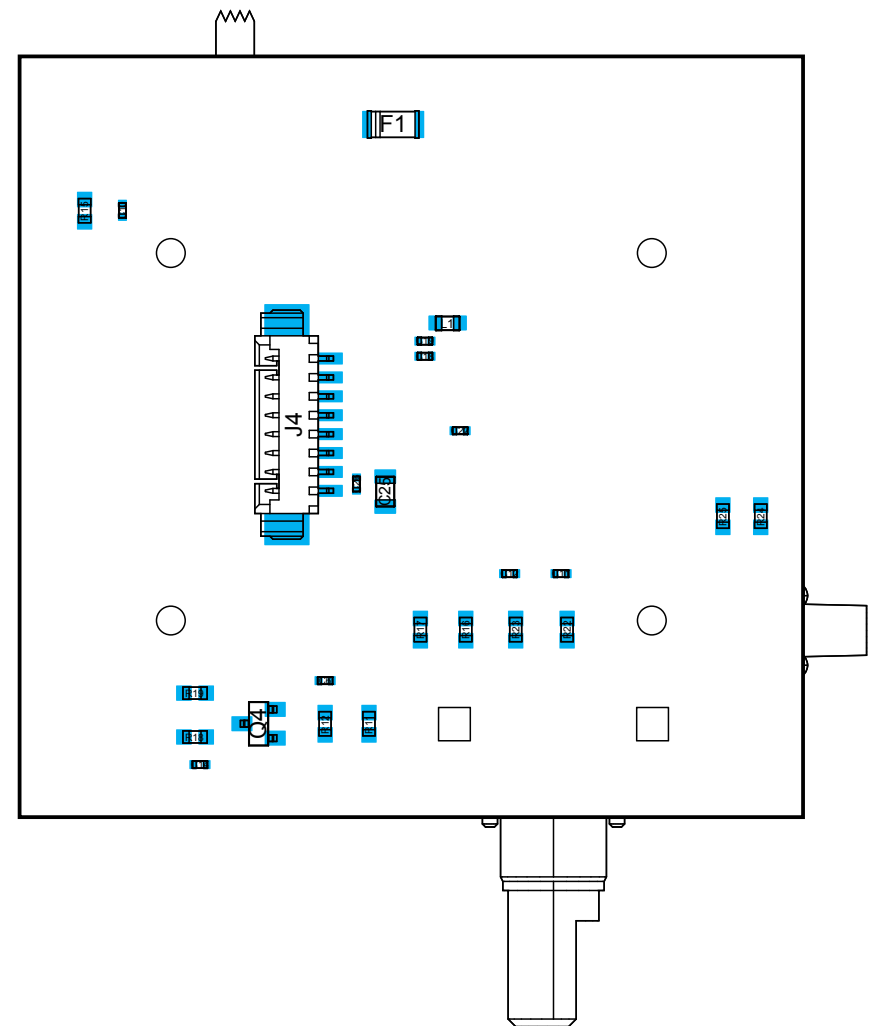


View from Top side (Scale 2:1)



View from Bottom side (Scale 2:1)



A

B

C

D

E

1

1

2

2

3

3

4

4

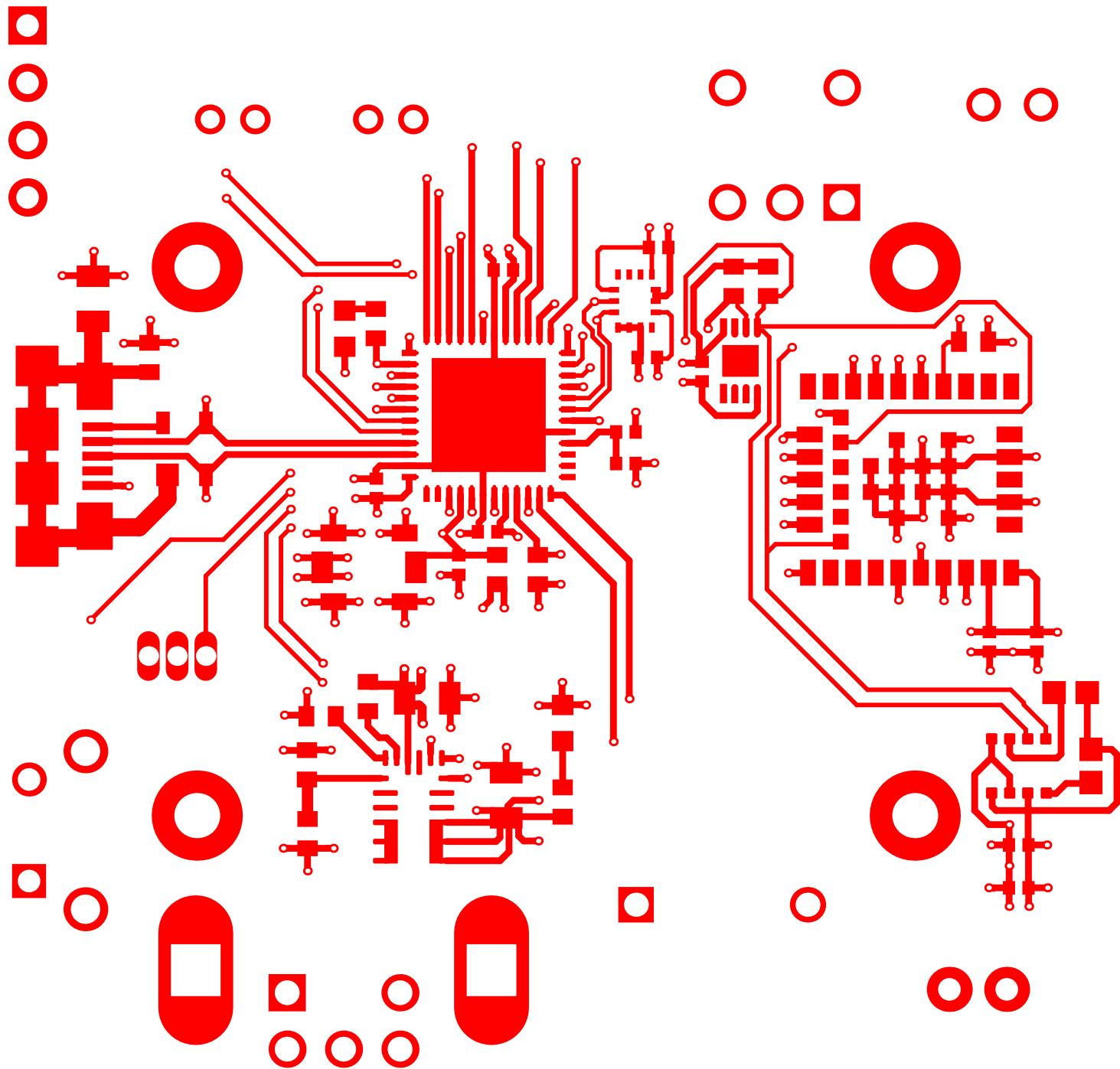
A

B

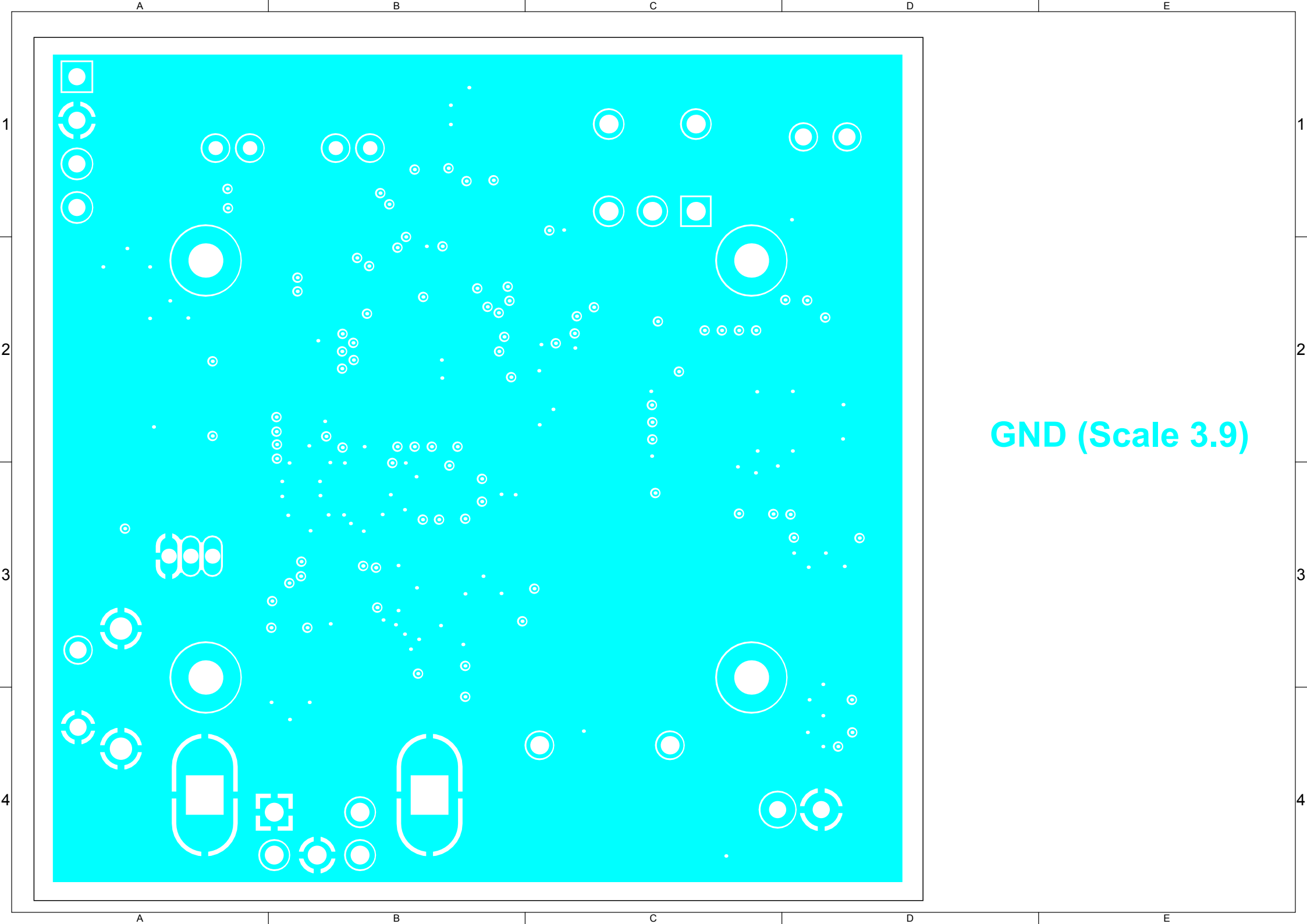
C

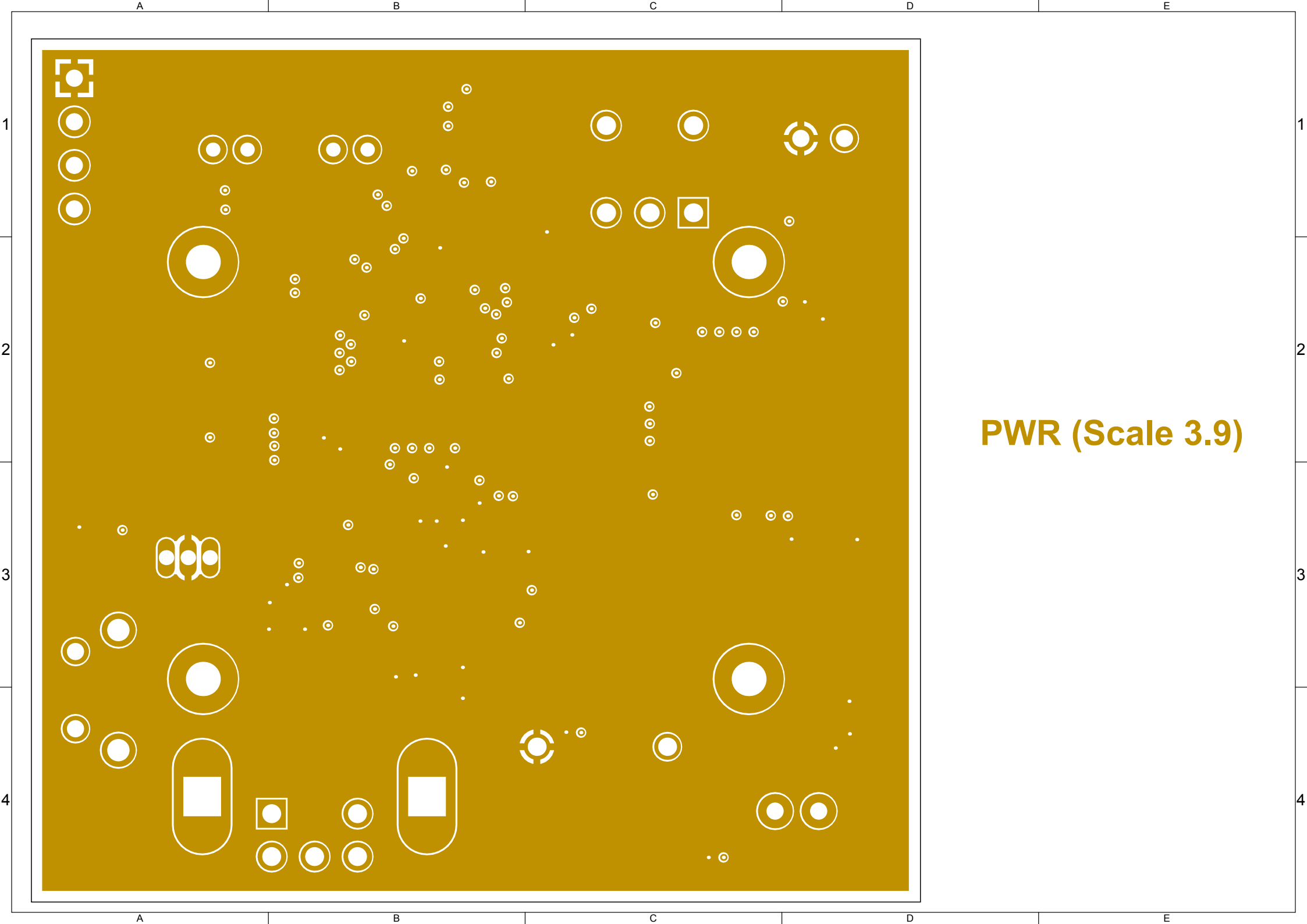
D

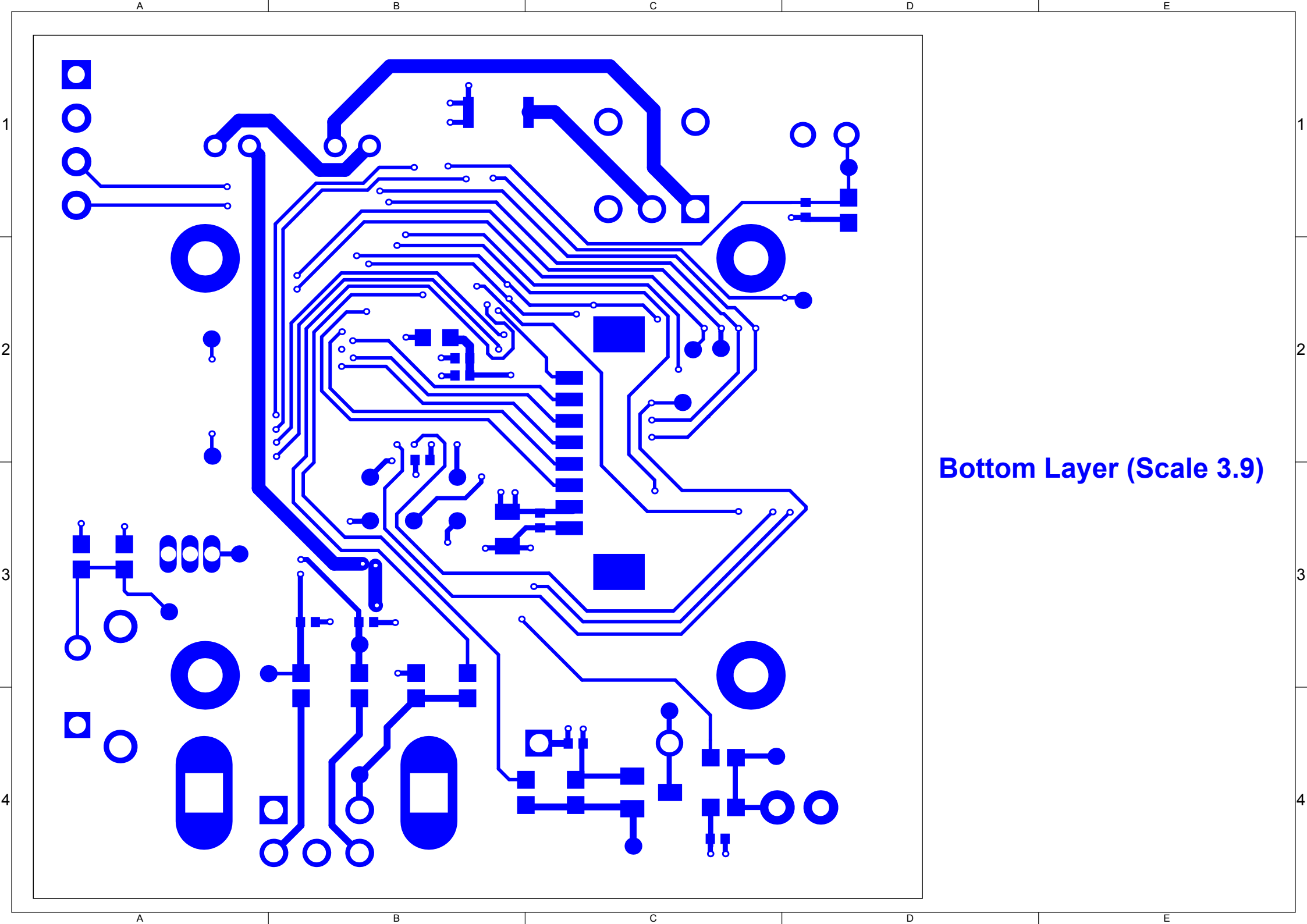
E



Top (Scale 3.9)







A

B

C

D

E

1

1

2

2

3

3

4

4

A

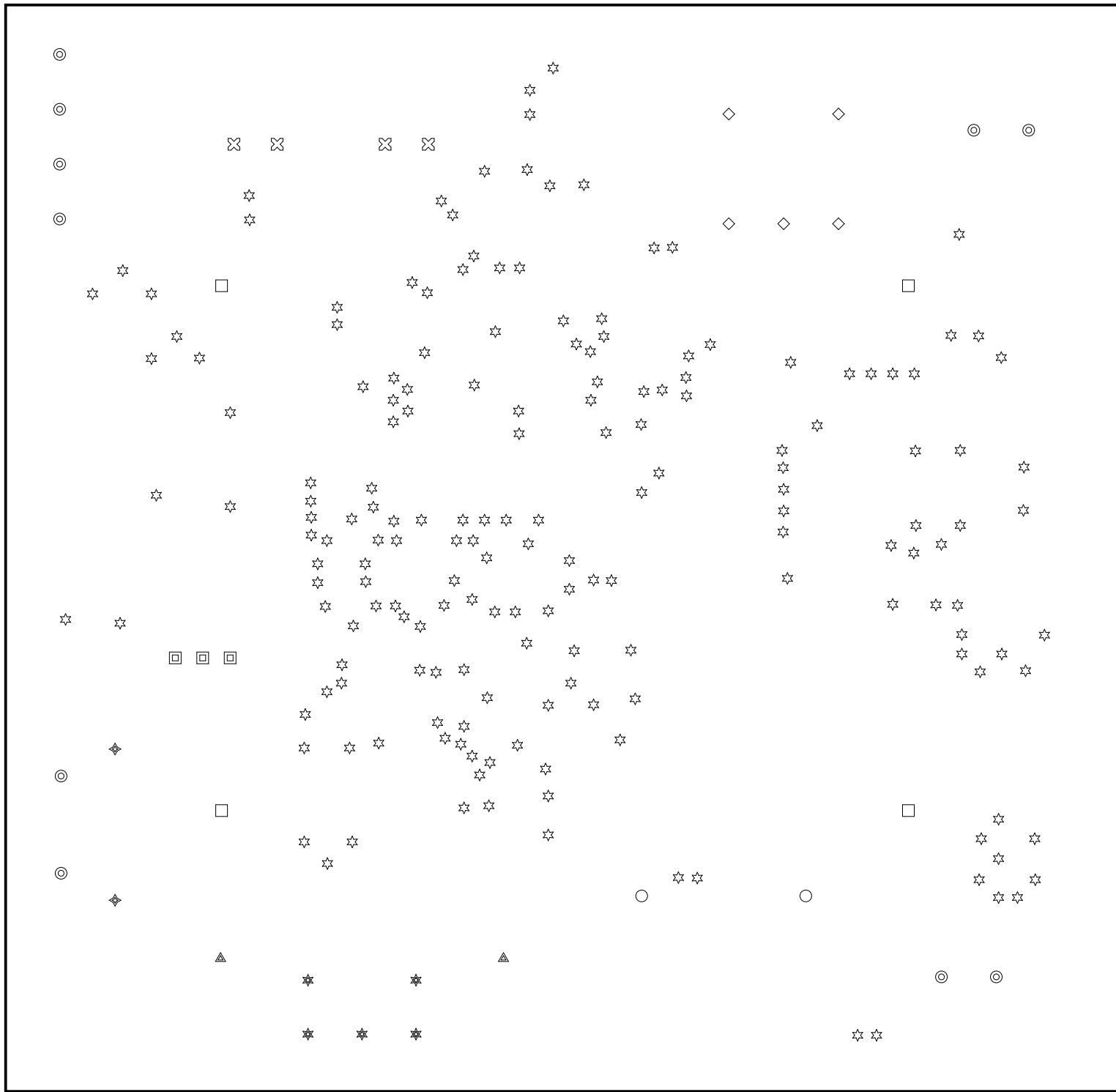
B

C

D

E

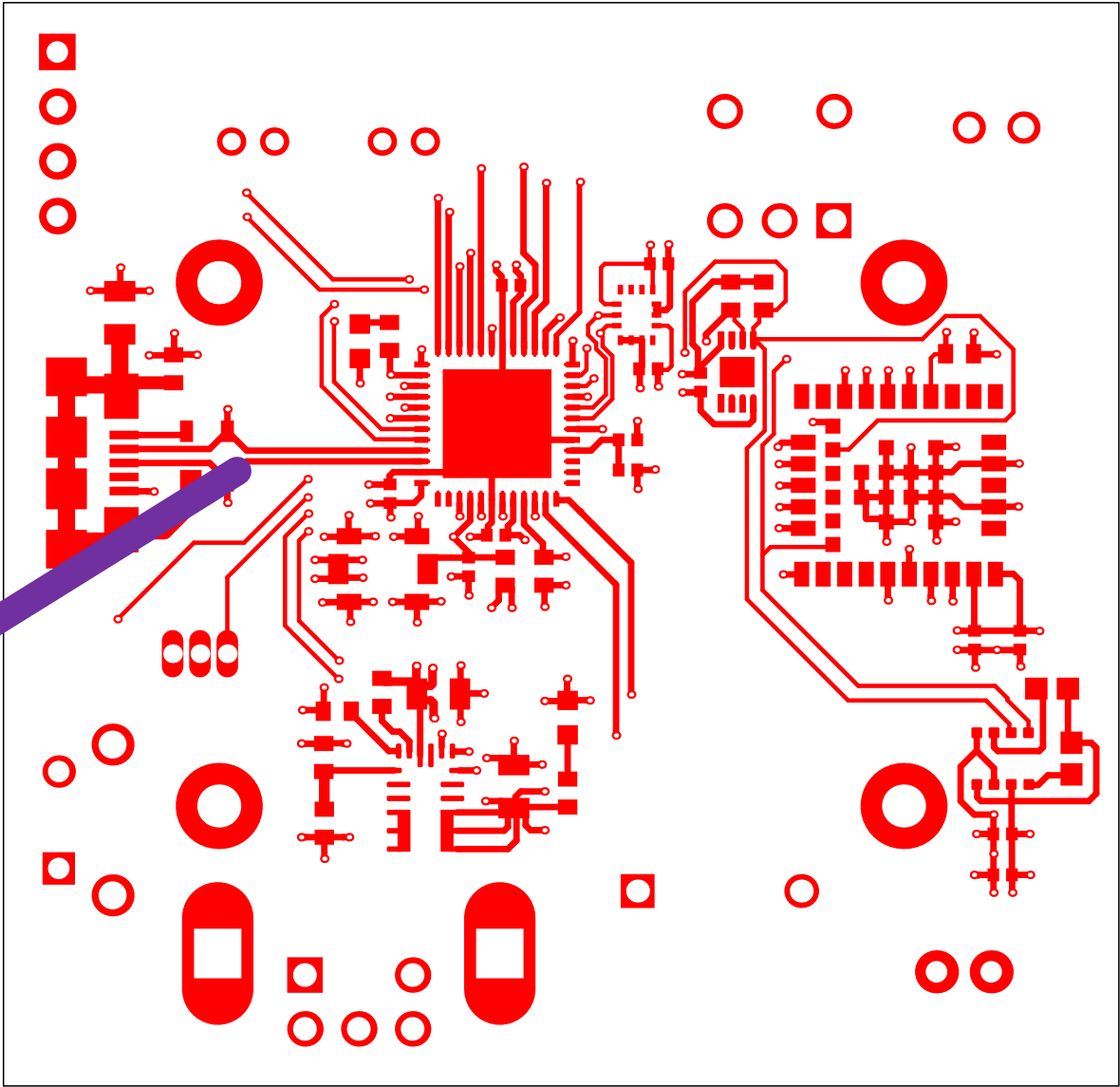
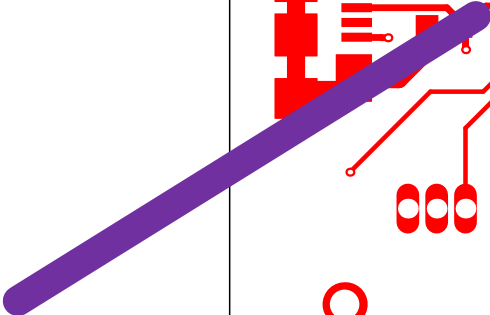
Drill Drawing View (Scale 3.9)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
☆	181	0.20mm	Plated	
⊠	4	0.81mm	Plated	
▣	3	0.87mm	Plated	
◎	10	1.00mm	Plated	
○	2	1.07mm	Plated	
◇	5	1.09mm	Plated	
✱	5	1.10mm	Plated	
◆	2	1.30mm	Plated	
□	4	2.00mm	Plated	
△	2	2.20mm	Plated	
	218 Total			

Transmisson Line ID 1
90Ohm Target Differential
Characteristic Impedence
USB



Transmission Line Structure Table

Impedance Id	Transmission Line	Target Impedance	Calculated Impedance	Trace layer	Wide Trace Width	Narrow Trace Width	Gap	Reference layers	Substack
1	Edge-Coupled Coated Microstrip	90	89.80	Top	0.30mm	0.30mm	0.15mm	GND	Board Layer Stack
2	Edge-Coupled Offset Stripline	90	90.19	GND	0.12mm	0.12mm	0.15mm	Top,PWR	Board Layer Stack
3	Edge-Coupled Offset Stripline	90	90.19	PWR	0.12mm	0.12mm	0.15mm	GND,Bottom Layer	Board Layer Stack
4	Edge-Coupled Coated Microstrip	90	89.80	Bottom Layer	0.30mm	0.30mm	0.15mm	PWR	Board Layer Stack

Layer Stack Legend

