SC20100S/B Pinout

100S	100B	I/O	Function	100S	100B	I/O	Function	100S	100B	I/O	Function	100S	100B	I/O	Function
1	A3	PE2	QSPI IO2	26	C2		VSS	51	K8	PB12	CAN2 RX / UART5 RX / ETH TXD0	76	A9	PA14	
2	B3	PE3	LDR	27	D2		VDD	52	J8	PB13	CAN2 TX / UART5 TX / ETH TXD1	77	A8	PA15	UART4 RTS / PWM2.1
3	C3	PE4		28	G3	PA4	ADC12.18 / DAC1	53	H10		USBH N	78	B9	PC10	SDMMC1 D2
4	D3	PE5	PWM15.1	29	H3	PA5	SPI6 SCK / ADC12.19 / DAC2	54	G10		USBH P	79	B8	PC11	SDMMC1 D3
5	E3	PE6	PWM15.2	30	J3	PA6	SPI6 MISO / PWM13.1 / ADC12.3	55	K9	PD8	UART3 TX	80	C8	PC12	SDMMC1 CK
6	B2		VBAT	31	K3	PA7	SPI6 MOSI / PWM14.1 / ADC12.7 / ETH DV	56	J9	PD9	UART3 RX	81	D8	PD0	UART4 RX / CAN1 RX
7	A2	PC13	TAMPER	32	G4	PC4	ETH RXD0	57	H9	PD10		82	E8	PD1	UART4 TX / CAN1 TX
8	A1		OSC32 IN	33	H4	PC5	ETH RXD1	58	G9	PD11	QSPI IO0	83	B7	PD2	SDMMC1 CMD
9	B1		OSC32 OUT	34	J4	PB0	UART4 CTS / PWM3.3 / ADC12.9	59	K10	PD12	QSPI IO1	84	C7	PD3	UART2 CTS
10	J1		VSS	35	K4	PB1	PWM3.4 / ADC12.5	60	J10	PD13	QSPI IO3	85	D7	PD4	UART2 RTS
11	K1		VDD	36	G5	PB2	QSPI CLK	61	H8	PD14		86	B6	PD5	UART2 TX
12	C1		OSC IN	37	H5	PE7	UART7 RX	62	G8	PD15		87	C6	PD6	UART2 RX
13	D1		OSC OUT	38	J5	PE8	UART7 TX	63	F10	PC6	UART6 TX / PWM3.1	88	D6	PD7	MOD
14	E1		NRST	39	K5	PE9	UART7 RTS	64	E10	PC7	UART6 RX / PWM3.2	89	A7	PB3	SPI3 SCK / PWM2.2
15	F1	PC0	ADC123.10	40	G6	PE10	UART7 CTS	65	F9	PC8	SDMMC1 D0 / UART5 RTS	90	A6	PB4	SPI3 MISO
16	F2	PC1	ADC123.11 / ETH MDC	41	H6	PE11	PWM1.2	66	E9	PC9	SDMMC1 D1 / UART5 CTS	91	C5	PB5	SPI3 MOSI
17	E2	PC2	ADC3.0	42	J6	PE12	SPI4 SCK	67	D9	PA8		92	B5	PB6	QSPI NCS
18	F3	PC3	ADC3.1	43	K6	PE13	SPI4 MISO	68	C9	PA9	UART1 TX[2]	93	A5	PB7	PWM4.2 / APP
19	G1		VSSA	44	G7	PE14	SPI4 MOSI	69	D10	PA10	UART1 RX[2]	94	D5		RESERVED pull to GND
20	F7		VREF+(S)/PDR_ON(B)[1]	45	H7	PE15		70	C10		USBC N	95	B4	PB8	I2C1 SCL / PWM16.1
21	H1		VDDA	46	J7	PB10	I2C2 SCL / ETH RX ER	71	B10		USBC P	96	A4	PB9	I2C1 SDA / PWM17.1
22	G2	PA0	PWM5.1 / ADC1.16 / WKUP	47	K7	PB11	I2C2 SDA / ETH TX EN	72	A10	PA13		97	D4	PE0	UART8 RX
23	H2	PA1	ETH REF CLK	48	E7		VCAP	73	F8		VCAP	98	C4	PE1	UART8 TX
24	J2	PA2	ETH MDIO	49	E5		VSS	74	E4		VSS	99	E6		Vss
25	K2	PA3	PWM2.4 / ADC12.15	50	F5		VDD	75	F4		VDD(S)/VDDLDO(B)*	100	F6		VDD(S)/VDD33USB(B)[1]

[1] Connect to VDD on both S or B packages

[2] These pins are used when in serial interface mode