

# SC20260B Pinout

Ball	I/O	Function	Ball	I/O	Function	Ball	I/O	Function	Ball	I/O	Function
A1		GND	E6		VDD	J11		VSS	N16		VSS
A2	PI6	PWM8.2	E7		PDR_ON	J12		NC	N17		NC
A3	PI5	PWM8.1	E8		RESERVED pull to GND	J13		VDD	P1		VSSa
A4	PI4	DC D5	E9		VDD	J14		SPI5 SCK	P2	PH3	FMC SDNE0
A5	PB5	SPI3 MOSI	E10	PJ13		J15	PK1	LCD G6 PWM1.1	P3	PH4	LCD G5 ADC3.15
A6		VDDLDO	E11		VDD	J16		VSS	P4	PH5	FMC SDNWE
A7		VCAP	E12	PD1	FMC D3	J17		VSS	P5	PI15	LCD G2
A8	PK5	LCD B6	E13	PC8	SDMMC1 D0 UART5 RTS	K1		NRST	P6	PJ1	
A9	PG10	DC D2	E14	PC9	SDMMC1 D1 UART5 CTS	K2	PF6	UART7 RX ADC3.8	P7	PF13	FMC A7
A10	PG9	DC V5	E15	PA8	DC XCLK	K3	PF7	UART7 TX ADC3.3	P8	PF14	FMC A8
A11	PD5	UART2 TX	E16		USBC P	K4	PF8	UART7 RTS PWM13.1 ADC3.7	P9	PE9	FMC D6
A12	PD4	UART2 RTS	E17		USBC N	K5		VDD	P10	PE11	FMC D8
A13	PC10	SDMMC1 D2 UART3 TX	F1		NC	K6		NC	P11	PB10	I2C2 SCL
A14	PA15	PWM2.1 UART4 RTS	F2		NC	K7		VSS	P12	PB11	I2C2 SDA
A15	PI1	SPI2 SCK	F3	PI10	ETH RX ER	K8		VSS	P13	PH10	DC D1
A16	PI0	PWM5.4	F4	PI11		K9		VSS	P14	PH11	PWM5.2
A17		VSS	F5		VDD	K10		VSS	P15	PD15	FMC D1
B1		VBAT	F6		NC	K11		VSS	P16	PD14	FMC D0
B2		VSS	F7		NC	K12		NC	P17		VDD
B3	PI7	PWM8.3	F8		NC	K13		VDD	R1	PC2_C*	ADC3.0
B4	PE1	FMC NBL1	F9		NC	K14	PJ11	SPI5 MISO PWM1.2	R2	PC3_C*	ADC3.1
B5	PB6	QSPI NCS	F10		NC	K15		VSS	R3	PA6	DC PIXCLK PWM13.1 ADC12.3
B6		VSS	F11		NC	K16		NC	R4		VSS
B7	PB4	SPI3 MISO	F12		NC	K17		NC	R5	PA7	ETH CRS DV PWM14.1 ADC12.7
B8	PK4	LCD B5	F13	PC7	UART6 RX PWM3.2	L1		VDDA	R6	PB2	QSPI CLK
B9	PG11	ETH TX EN	F14	PC6	UART6 TX PWM3.1	L2	PC0	ADC123.10	R7	PF12	FMC A6
B10	PJ15	LCD B3	F15	PG8	FMC SDCLK	L3	PF10	ADC3.6	R8		VSS
B11	PD6	UART2 RX	F16	PG7		L4	PF9	UART7 CTS PWM14.1 ADC3.2	R9	PF15	FMC A9
B12	PD3	UART2 CTS	F17		VDD33USB	L5		VDD	R10	PE12	FMC D9
B13	PC11	SDMMC1 D3 UART3 RX	G1	PF2	FMC A2	L6		NC	R11	PE15	FMC D12
B14	PA14		G2		NC	L7		VSS	R12	PJ5	LCD R6
B15	PI2	SPI2 MISO PWM8.4	G3	PF1	FMC A1	L8		VSS	R13	PH9	DC D0 PWM12.2
B16	PH15	LCD G4	G4	PF0	FMC A0	L9		VSS	R14	PH12	DC D3 PWM5.3
B17	PH14	CAN1 RX UART4 RX	G5		VDD	L10		VSS	R15	PD11	QSPI IO0
C1	PC15	OSC32 OUT	G6		NC	L11		VSS	R16	PD12	QSPI IO1
C2	PC14	OSC32 IN	G7		VSS	L12		NC	R17	PD13	QSPI IO3
C3	PE2	QSPI IO2	G8		VSS	L13		VDD	T1	PA0_C*	ADC12.0
C4	PE0	FMC NBL0	G9		VSS	L14	PJ10	SPI5 MOSI	T2	PA1_C*	ADC12.1
C5	PB7	PWM4.2 APP	G10		VSS	L15		VSS	T3	PA5	ADC12.19 DAC2
C6	PB3	SPI3 SCK PWM2.2	G11		VSS	L16		NC	T4	PC4	ETH RXD0
C7	PK6	LCD B7	G12		NC	L17		NC	T5	PB1	PWM3.4 ADC12.5
C8	PK3	LCD B4	G13		VDD	M1		VREF+	T6	PJ2	LCD R3
C9	PG12		G14	PG5	FMC BA1	M2	PC1	ETH MDC	T7	PF11	FMC SDNRAS
C10		VSS	G15	PG6		M3	PC2	ADC123.12	T8	PG0	FMC A10
C11	PD7	MOD	G16		VSS	M4	PC3	ADC12.13	T9	PE8	FMC D5
C12	PC12	SDMMC1 CK	G17		VDD50USB	M5		VDD	T10	PE13	FMC D10
C13		VSS	H1	PI12	LCD HSYNC	M6		NC	T11	PH6	PWM12.1
C14	PI3	SPI2 MOSI	H2	PI13	LCD VSYNC	M7		NC	T12		VSS
C15	PA13		H3	PI14	LCD CLK	M8		NC	T13	PH8	I2C3 SDA
C16		VSS	H4	PF3	FMC A3	M9		NC	T14	PB12	CAN2 RX UART5 RX
C17		VDDLDO	H5		VDD	M10		NC	T15		USBH P
D1	PE5	DC D6 PWM15.1	H6		NC	M11		NC	T16	PD10	FMC D15
D2	PE4	DC D4	H7		VSS	M12		NC	T17	PD9	FMC D14
D3	PE3	LDR	H8		VSS	M13		VDD	U1		VSS
D4	PB9	I2C1 SDA PWM17.1	H9		VSS	M14	PJ9	UART8 RX PWM1.3	U2	PA3	PWM2.4 ADC12.15
D5	PB8	I2C1 SCL PWM16.1	H10		VSS	M15		VSS	U3	PA4	DC H5 ADC12.18 DAC1
D6	PG15	FMC SDNCAS	H11		VSS	M16		NC	U4	PC5	ETH RXD1
D7	PK7	LCD DE	H12		NC	M17		NC	U5	PB0	PWM3.3 ADC12.9 UART4 CTS
D8	PG14	ETH TXD1	H13		VDD	N1		VREF-	U6	PJ3	LCD R4
D9	PG13	ETH TXD0	H14	PG4	FMC BA0	N2	PH2	FMC SDCKE0	U7	PJ4	LCD R5
D10	PJ14		H15	PG3		N3	PA2	ETH MDIO	U8	PG1	FMC A11
D11	PJ12	LCD G3	H16	PG2	FMC A12	N4	PA1	ETH REF CLK	U9	PE7	FMC D4
D12	PD2	SDMMC1 CMD	H17	PK2	LCD G7	N5	PA0	PWM5.1 ADC1.16 WKUP	U10	PE14	FMC D11
D13	PD0	FMC D2	J1	PH1	OSC OUT	N6	PJ0		U11		Vcap
D14	PA10	UART1 RX	J2	PH0	OSC IN	N7		VDD	U12		VDDLDO
D15	PA9	UART1 TX	J3		VSS	N8		VDD	U13	PH7	I2C3 SCL
D16	PH13	CAN1 TX UART4 TX	J4	PF5	FMC A5	N9	PE10	FMC D7	U14	PB13	CAN2 TX UART5 TX
D17		Vcap	J5	PF4	FMC A4	N10		VDD	U15		USBH N
E1		NC	J6		NC	N11		VDD	U16	PD8	FMC D13
E2	PI9		J7		VSS	N12		VDD	U17		VSS
E3	PC13	TAMPER	J8		VSS	N13	PJ8	UART8 TX			
E4	PI8		J9		VSS	N14	PJ7				
E5	PE6	DC D7 PWM15.2	J10		VSS	N15	PJ6	LCD R7			

\* No GPIO - Analog Input Only