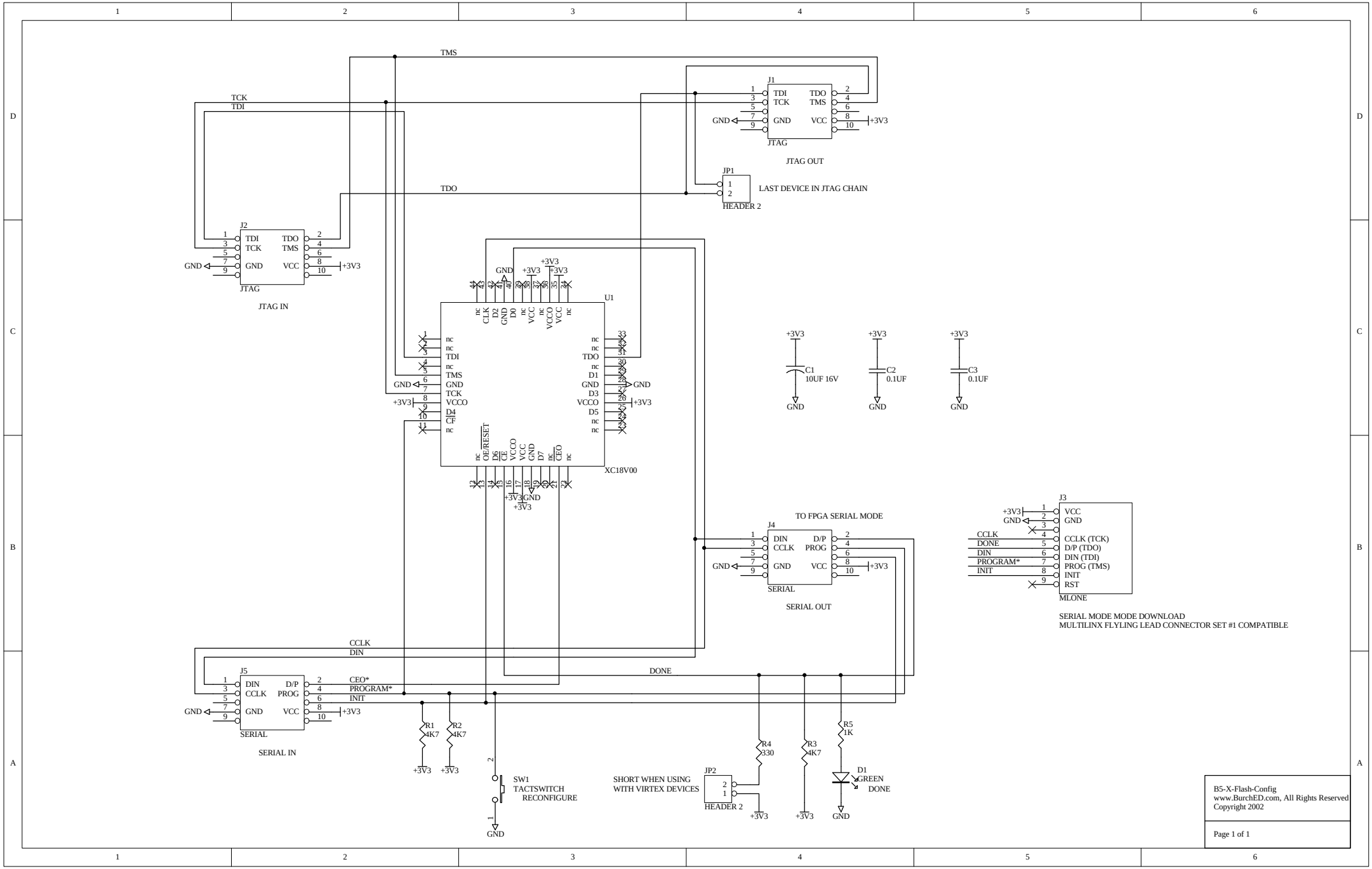
TMS
TCK
TDI
TDO
J1
TDI TDO
TCK TMS
GND GND VCC
+3V3
JTAG
JTAG OUT
JP1
LAST DEVICE IN JTAG CHAIN
HEADER 2
J2
JTAG
JTAG IN
U1
XC18V00
C1
10UF 16V
C2
0.1UF
C3
0.1UF
TO FPGA SERIAL MODE
J4
DIN D/P
CCLK PROG
SERIAL
SERIAL OUT
J3
VCC
GND
CCLK (TCK)
D/P (TDO)
DIN (TDI)
PROG (TMS)
INIT
RST
MLONE
SERIAL MODE MODE DOWNLOAD
MULTILINX FLYLING LEAD CONNECTOR SET #1 COMPATIBLE
CCLK
DIN
DONE
J5
CEO*
PROGRAM*
INIT
SERIAL
SERIAL IN
R1
4K7
R2
4K7
SW1
TACTSWITCH
RECONFIGURE
SHORT WHEN USING
WITH VIRTEX DEVICES
JP2
HEADER 2
R4
330
R3
4K7
R5
1K
D1
GREEN
DONE
B5-X-Flash-Config
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Page 1 of 1