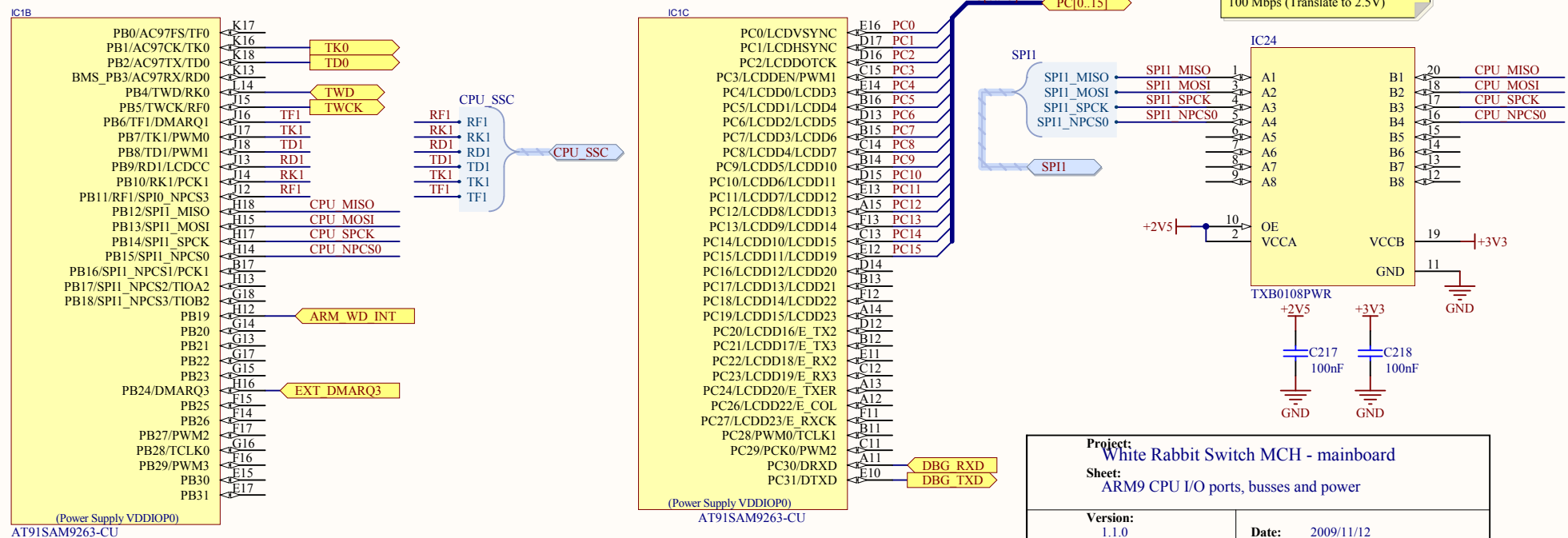
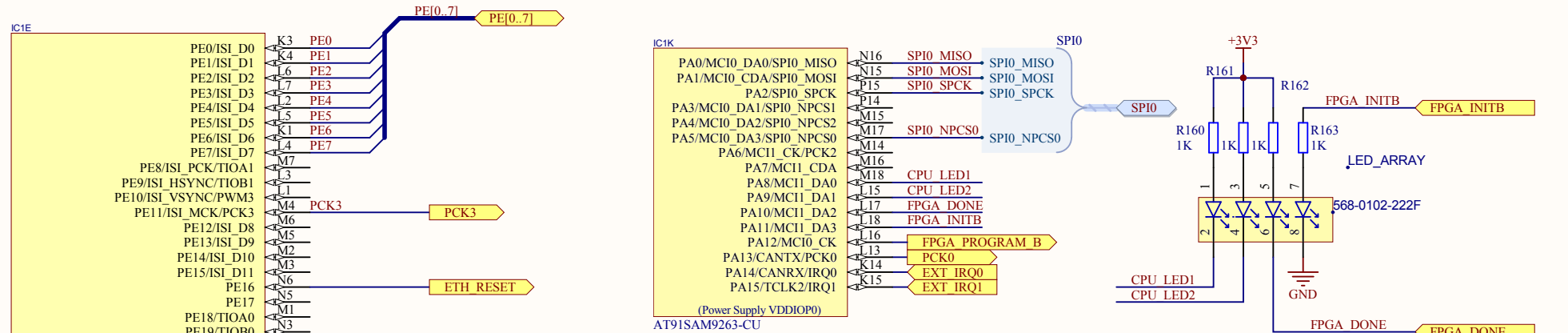


Project: White Rabbit Switch MCH - mainboard	
Sheet: External 100Mbps Ethernet PHY & magnetics	
Version: 1.1.0	Date: 2009/11/12
Author: Tomasz Wlostowski	License: Open Hardware License (OHL)
Company: CERN BE-CO-HT	



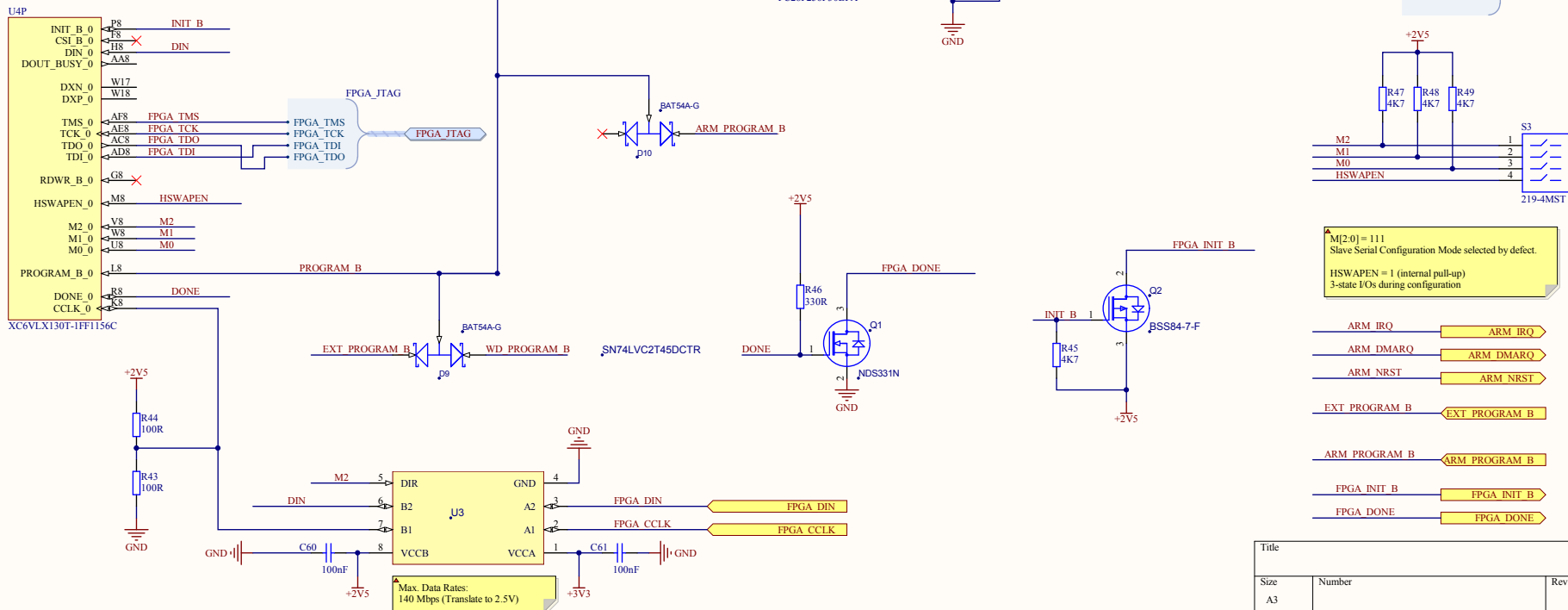
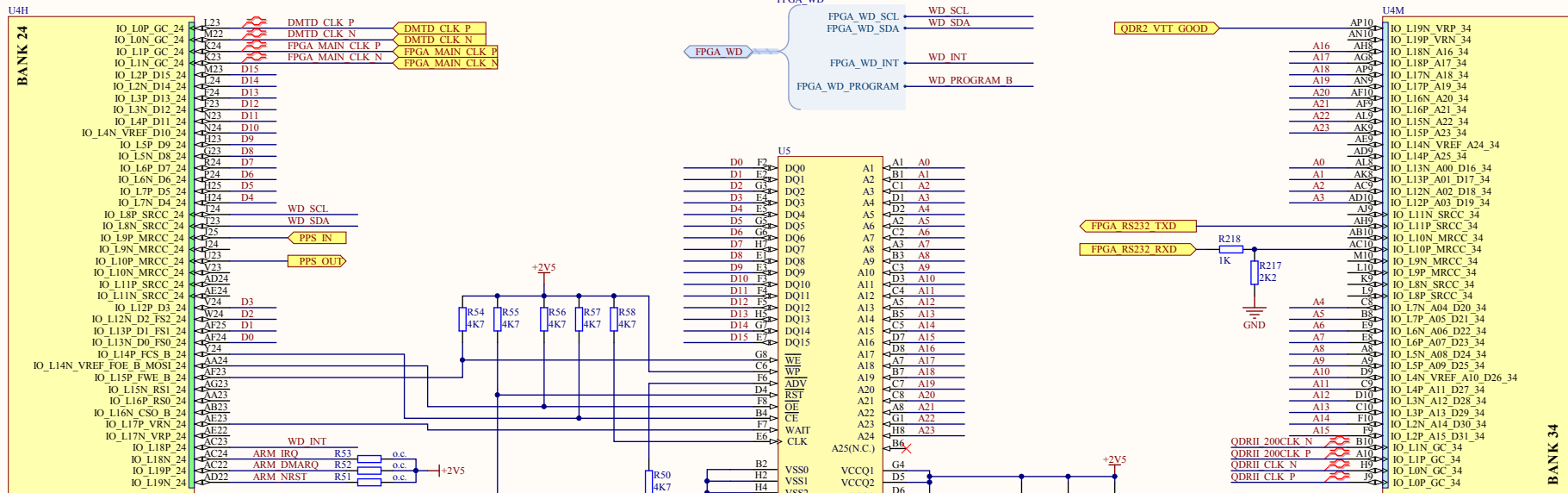
<b>Project:</b> White Rabbit Switch MCH - mainboard	
<b>Sheet:</b> ARM9 CPU I/O ports, busses and power	
<b>Version:</b> 1.1.0	<b>Date:</b> 2009/11/12
<b>Author:</b> Tomasz Wlostowski	<b>License:</b> Open Hardware License (OHL)
<b>Company:</b> CERN BE-CO-HT	











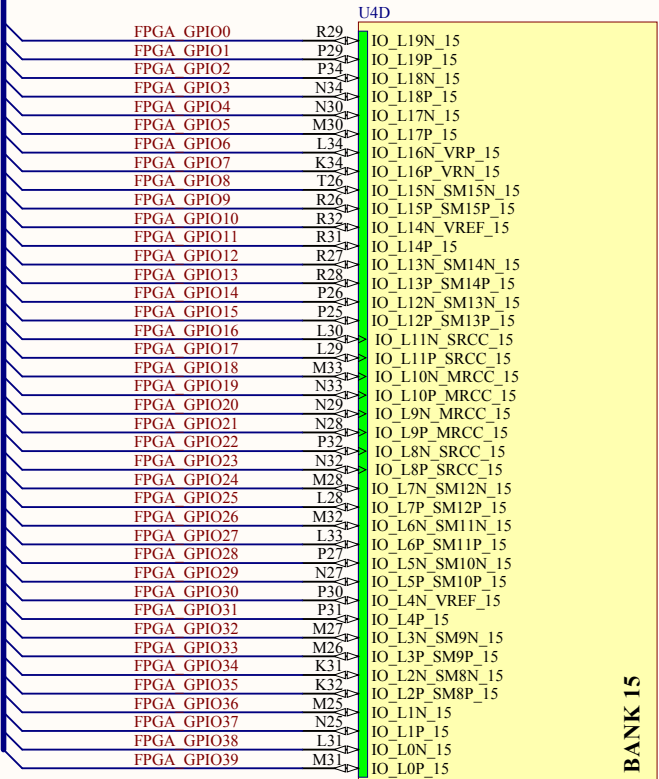
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FPGA\_GPIO[0..39]

FPGA\_GPIO[0..39]

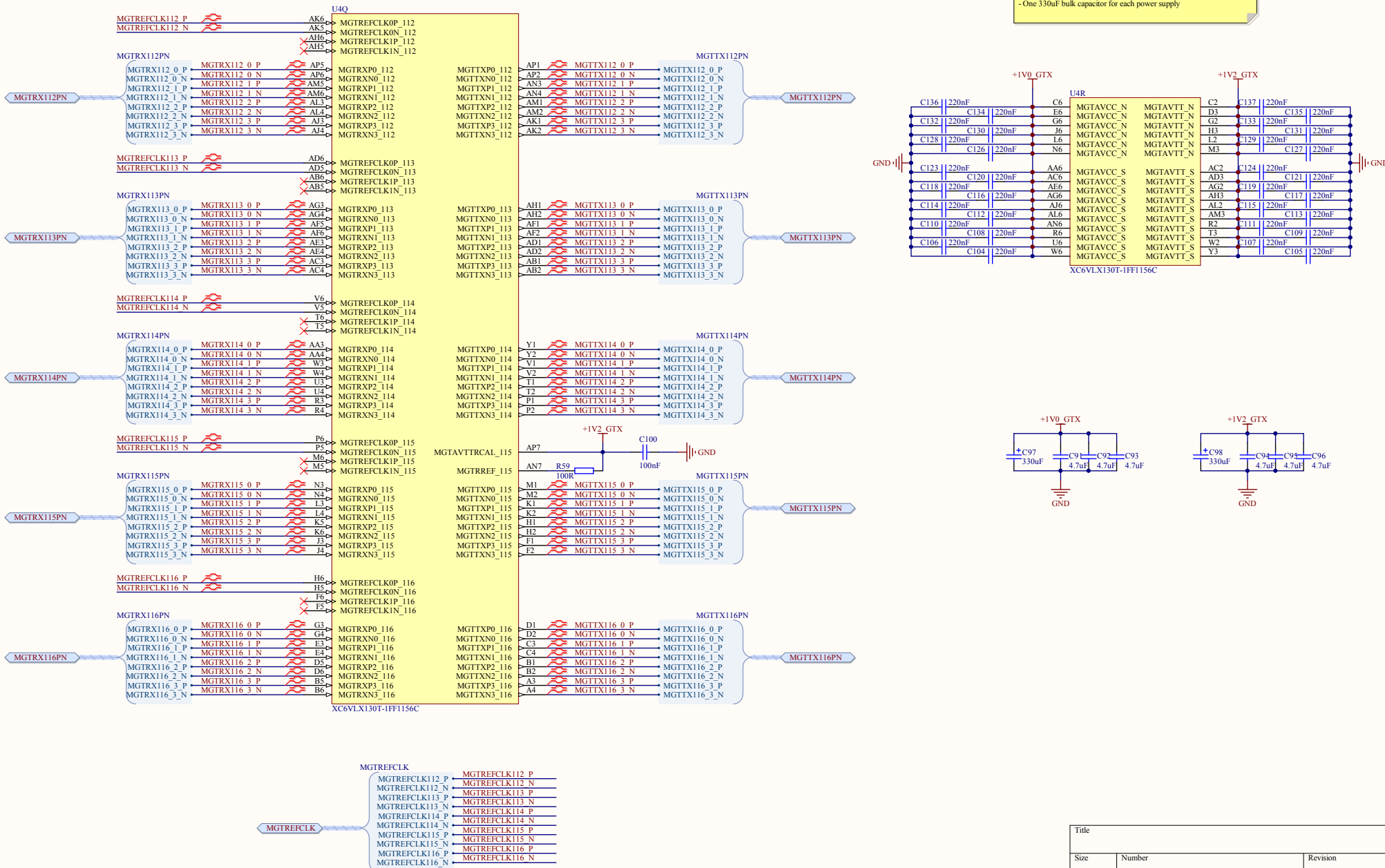


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File:	D:\Proyectos Altium\...\FPGA_GPIOs Sch	Drawn By:

Power Supply Decoupling Capacitors

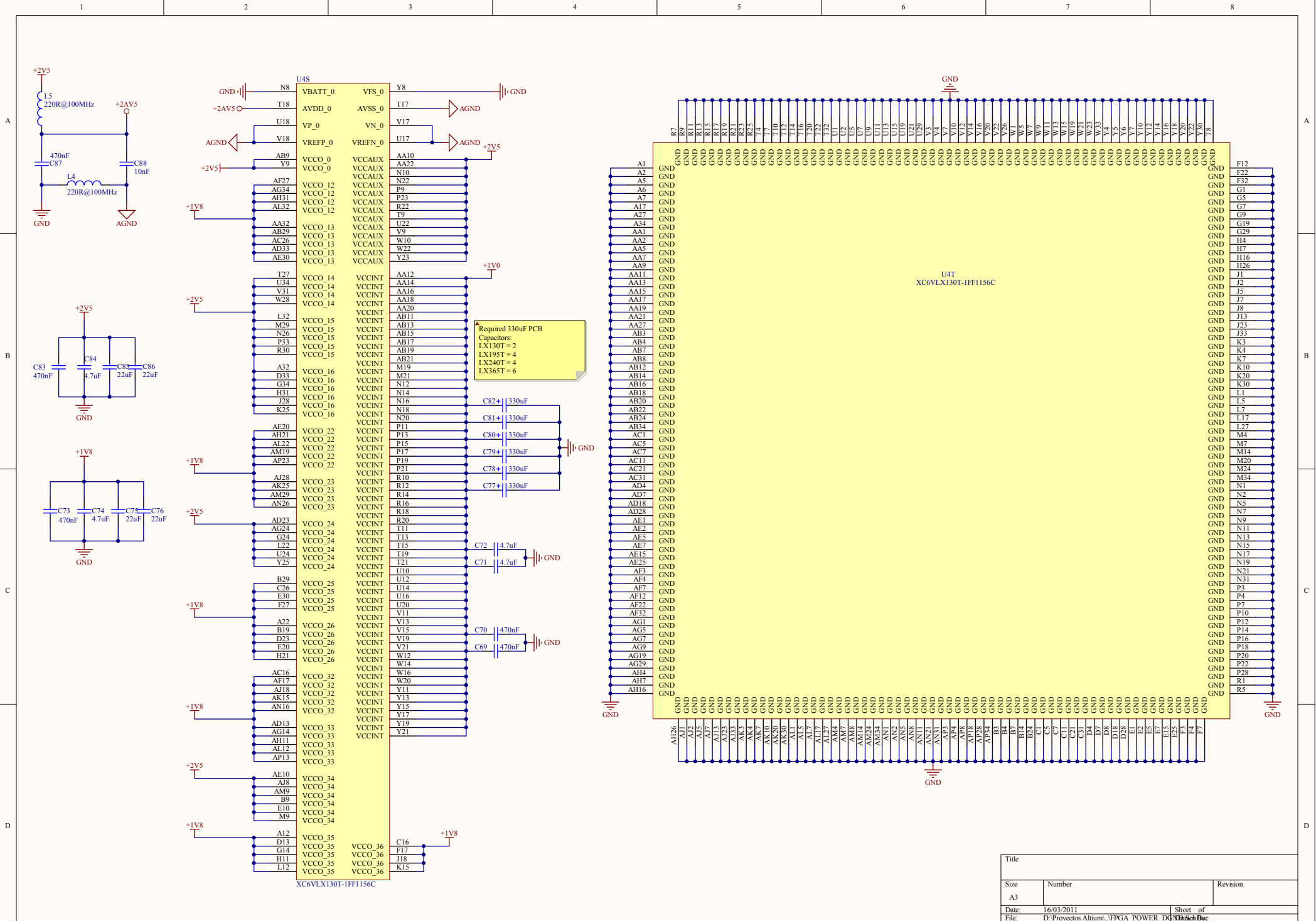
According to Xilinx UG366 (v2.3), page 230, the suggested filtering for the MGTAVCC and MGTAVTT power supplies is:

- One 0.22uF, size 0402, ceramic capacitor per power supply pin
- One 4.7uF, size 0402, ceramic capacitor per two Quads
- One 330uF bulk capacitor for each power supply



Title		
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A3		
Date:	16/03/2011	Sheet of
File:	D:\Proyectos Altium\JPGA GTX SchDoc	Drawn By:

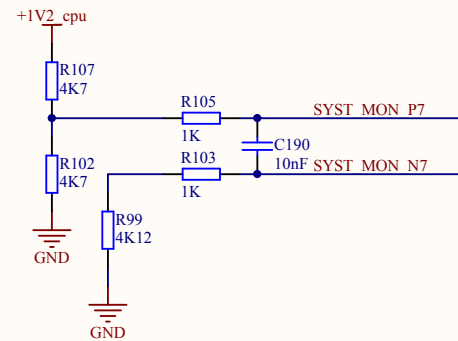
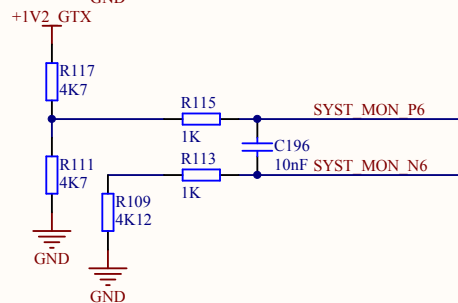
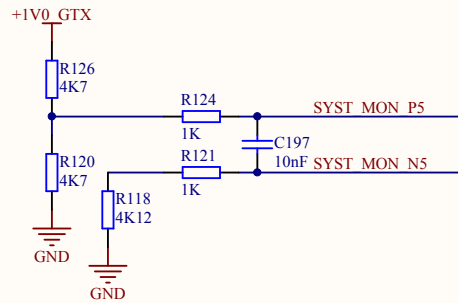
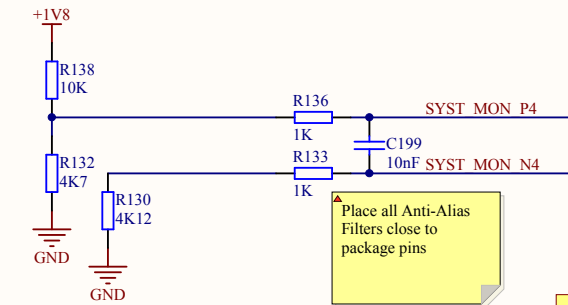
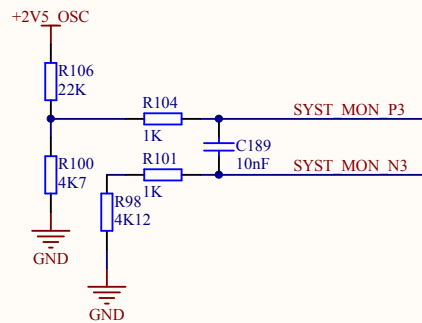
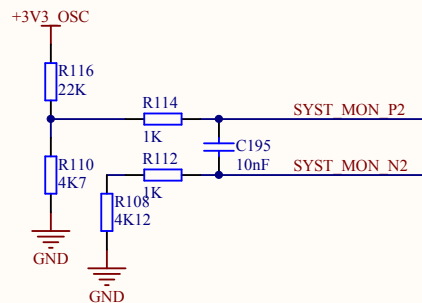
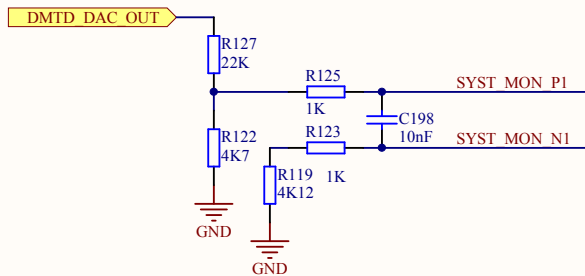
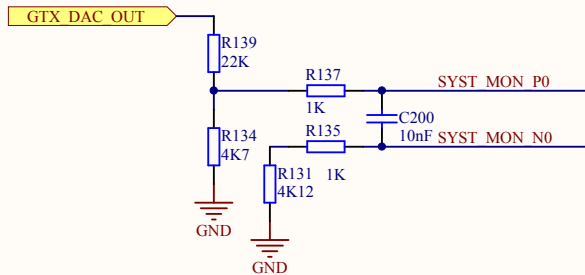




Required 330uF PCB  
Capacitors:  
LX130T = 2  
LX195T = 4  
LX240T = 4  
LX365T = 6

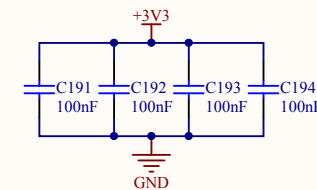
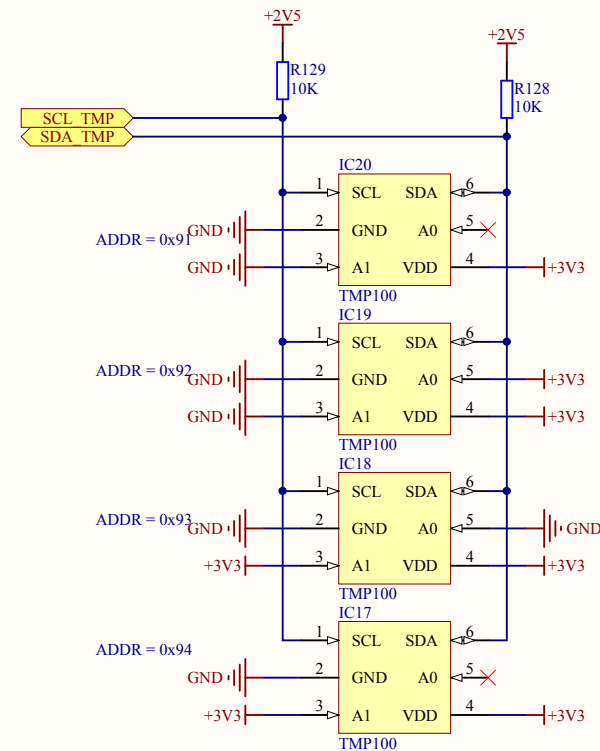
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File:	D:\Proyectos Altium\JPGA POWER DG	ThiSch Doc





SYST MON P[0..7]

SYST MON N[0..7]



Title		
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Date:	16/03/2011	Sheet of
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