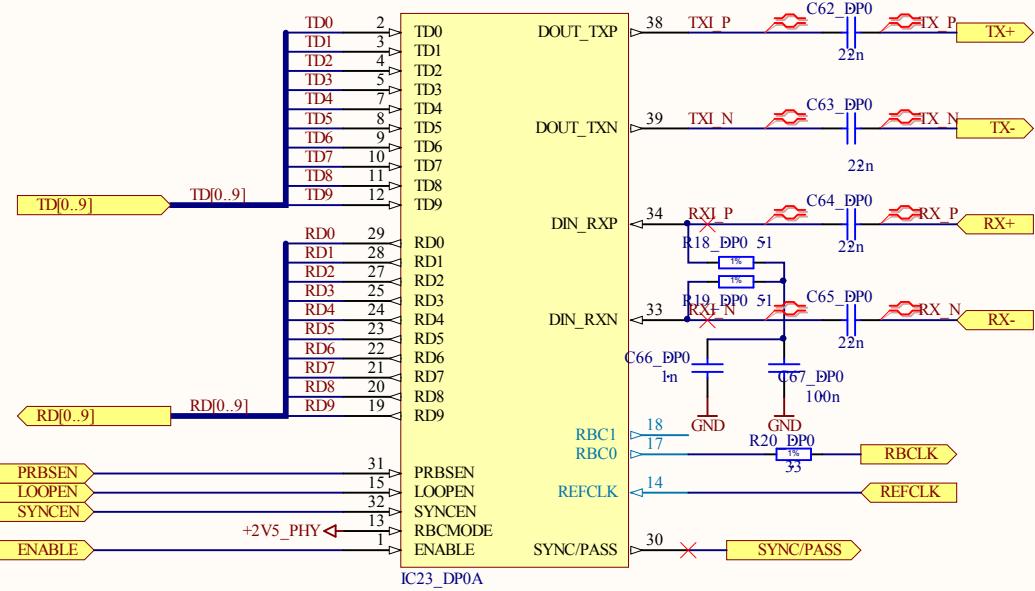
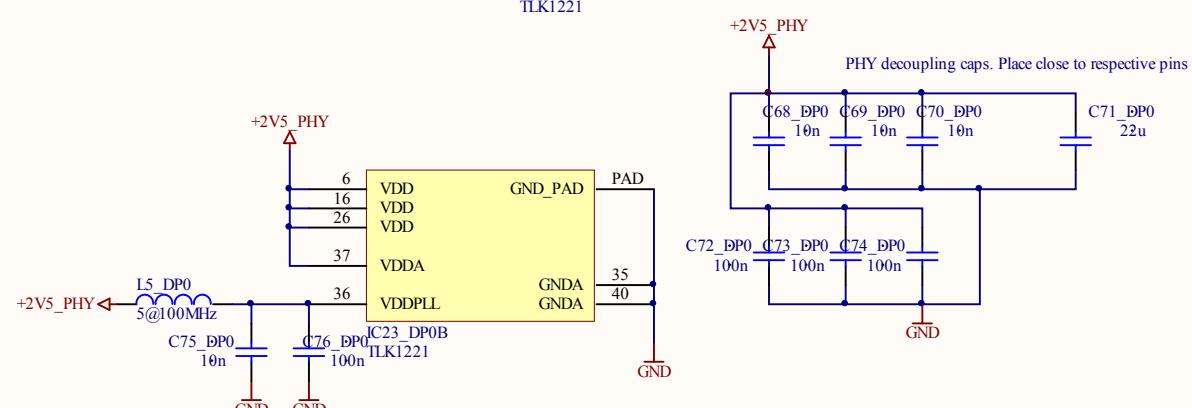


A



B



C

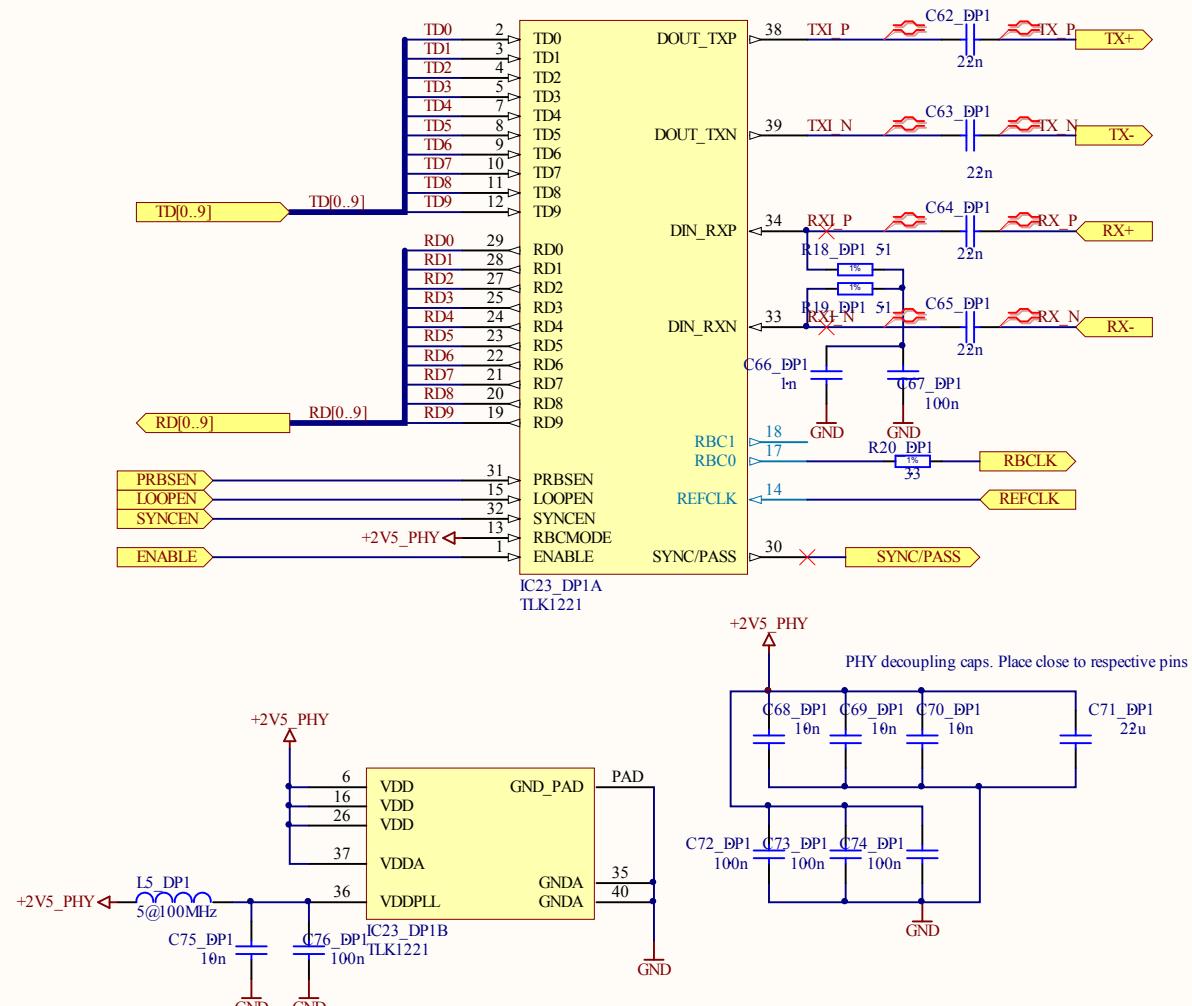
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White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

A

B

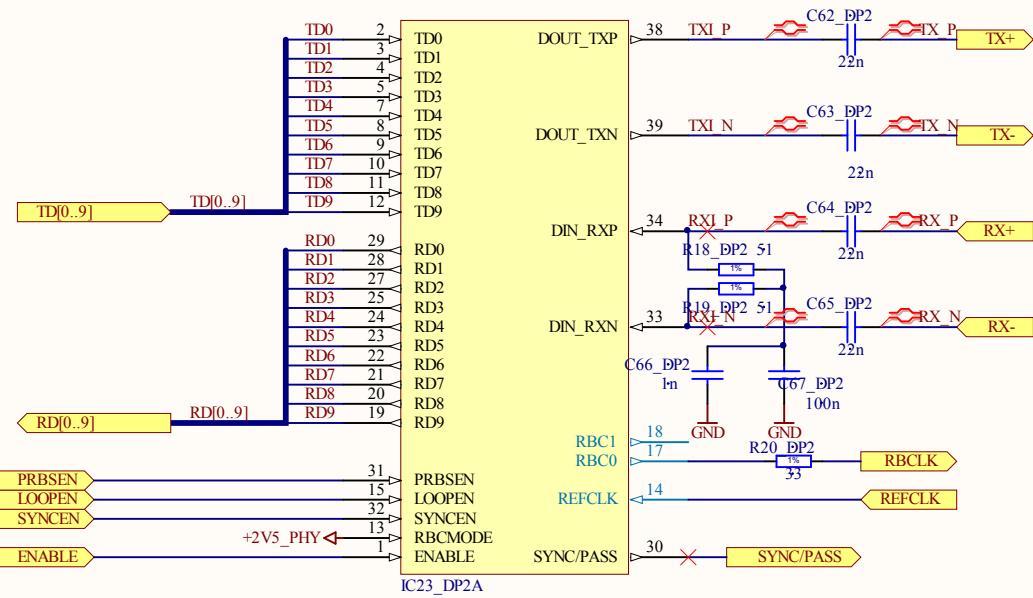
C

D

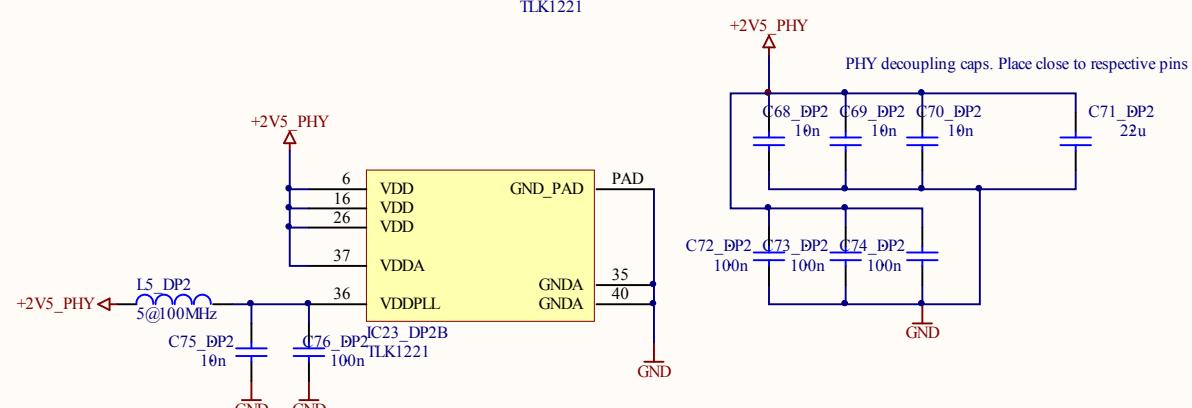


Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

A



B



C

Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

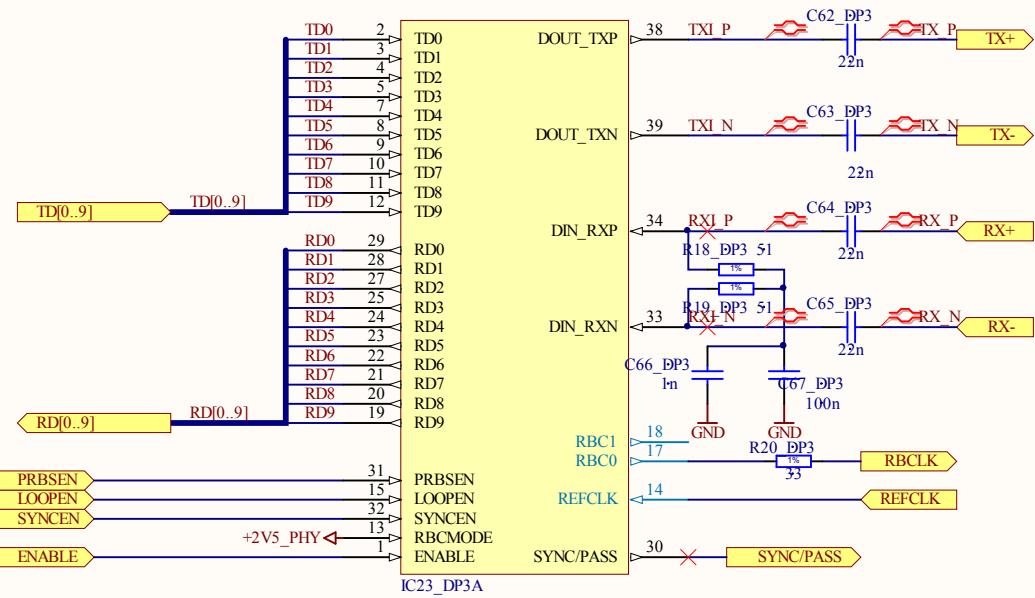
A

B

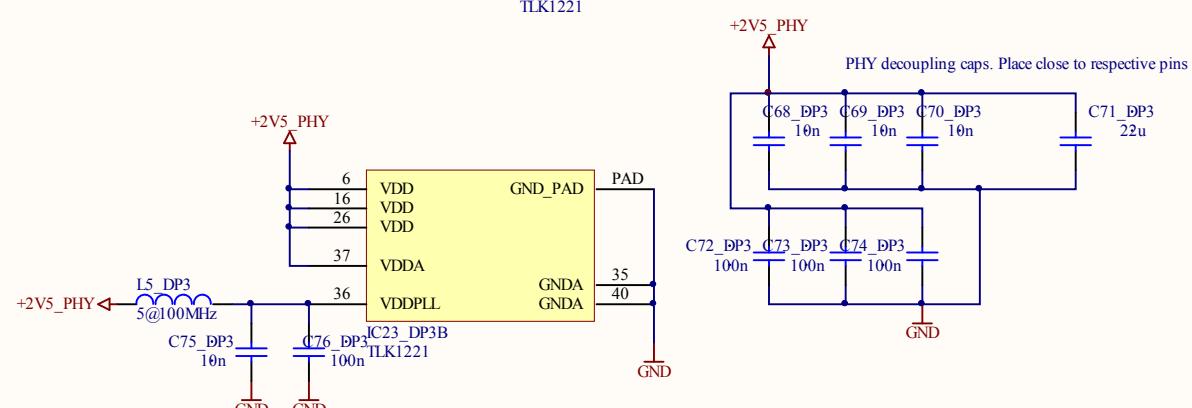
C

D

A



B



C

Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Subs
By:		

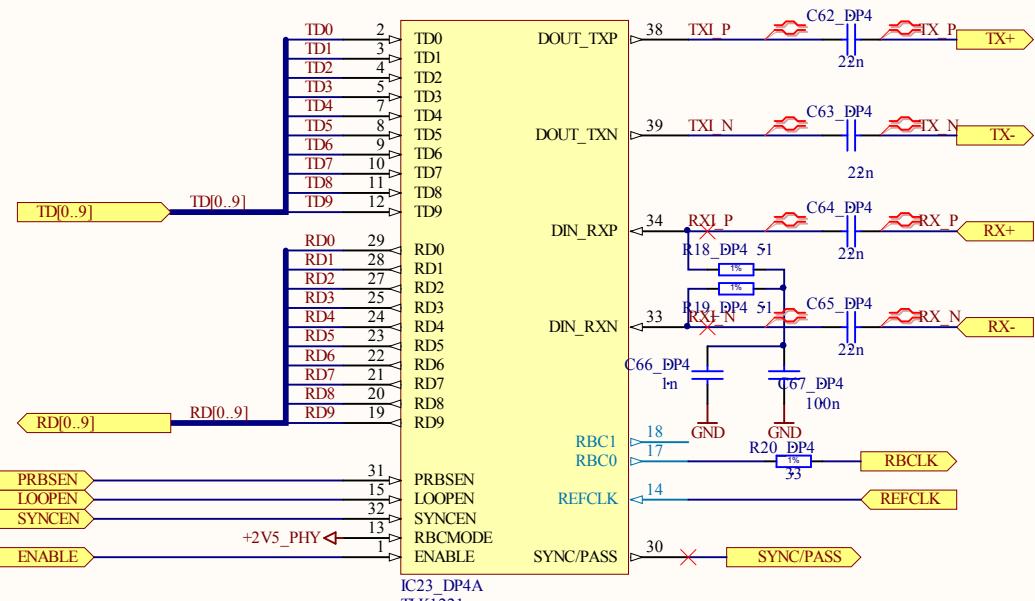
A

B

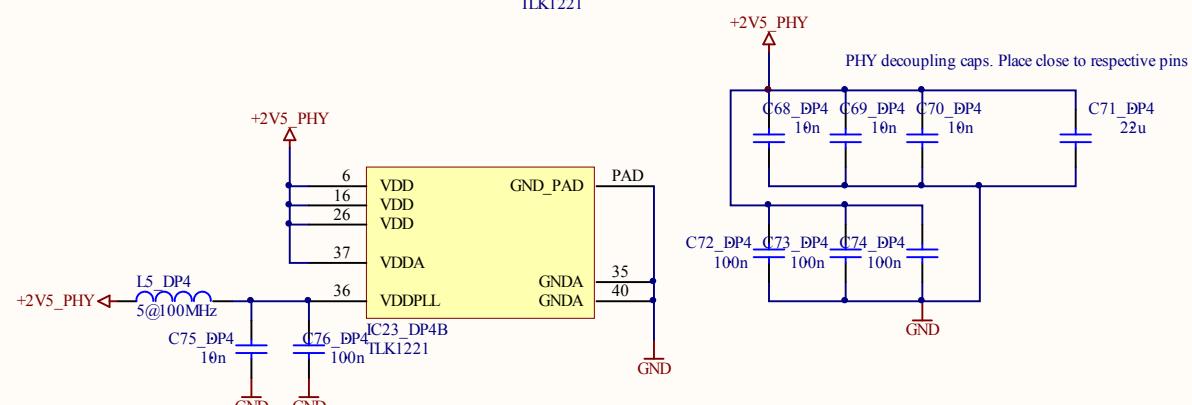
C

D

A



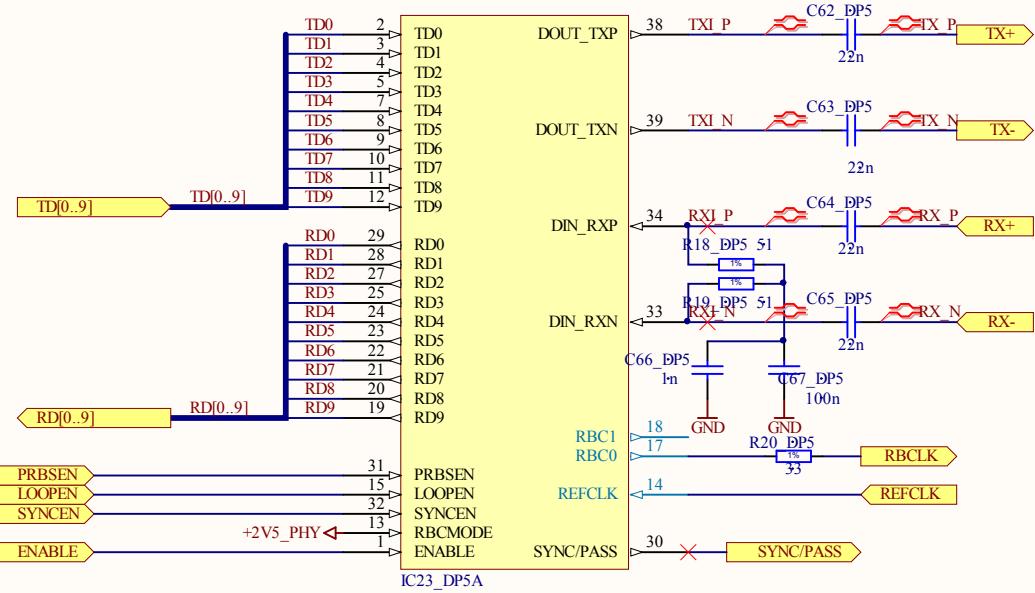
B



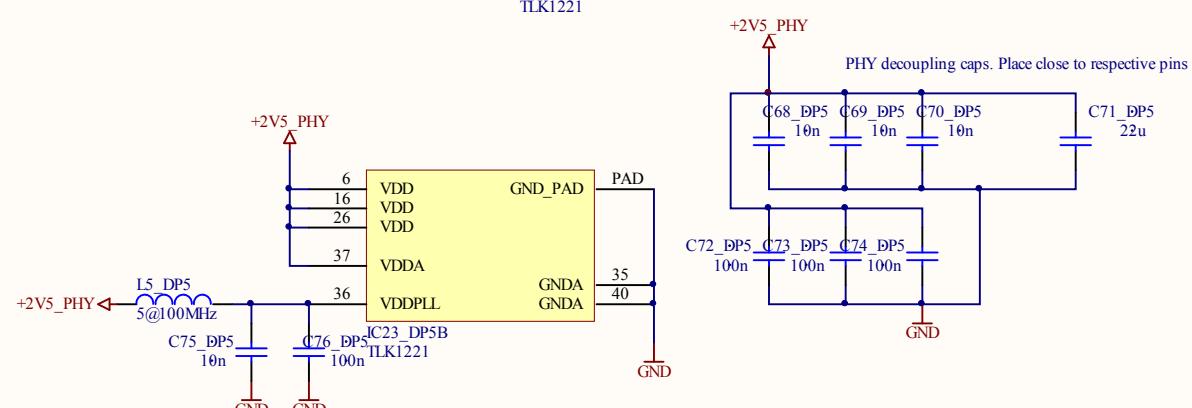
C

Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

A



B



C

Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Subs
Drawn By:		

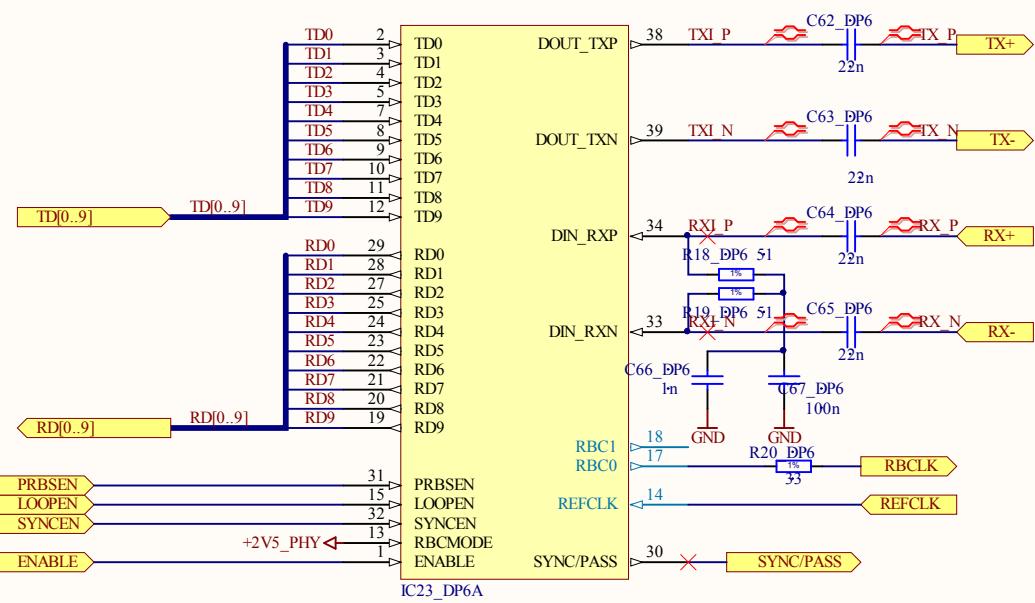
A

B

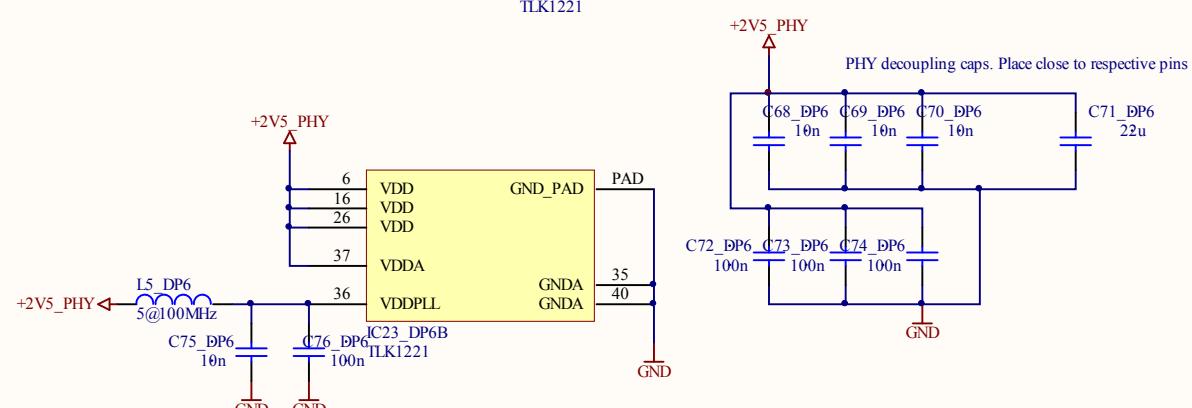
C

D

A



B



C

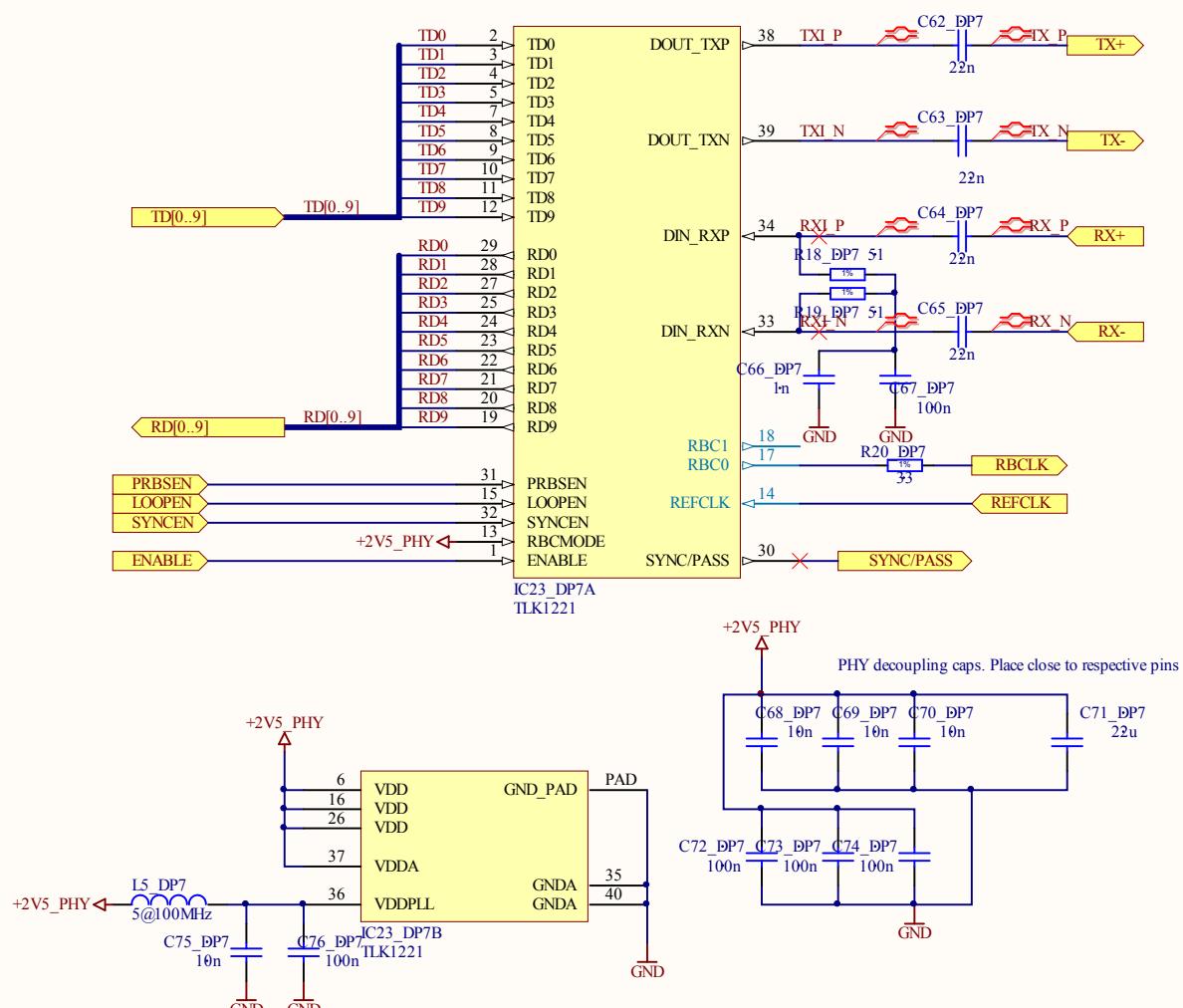
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White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

A

B

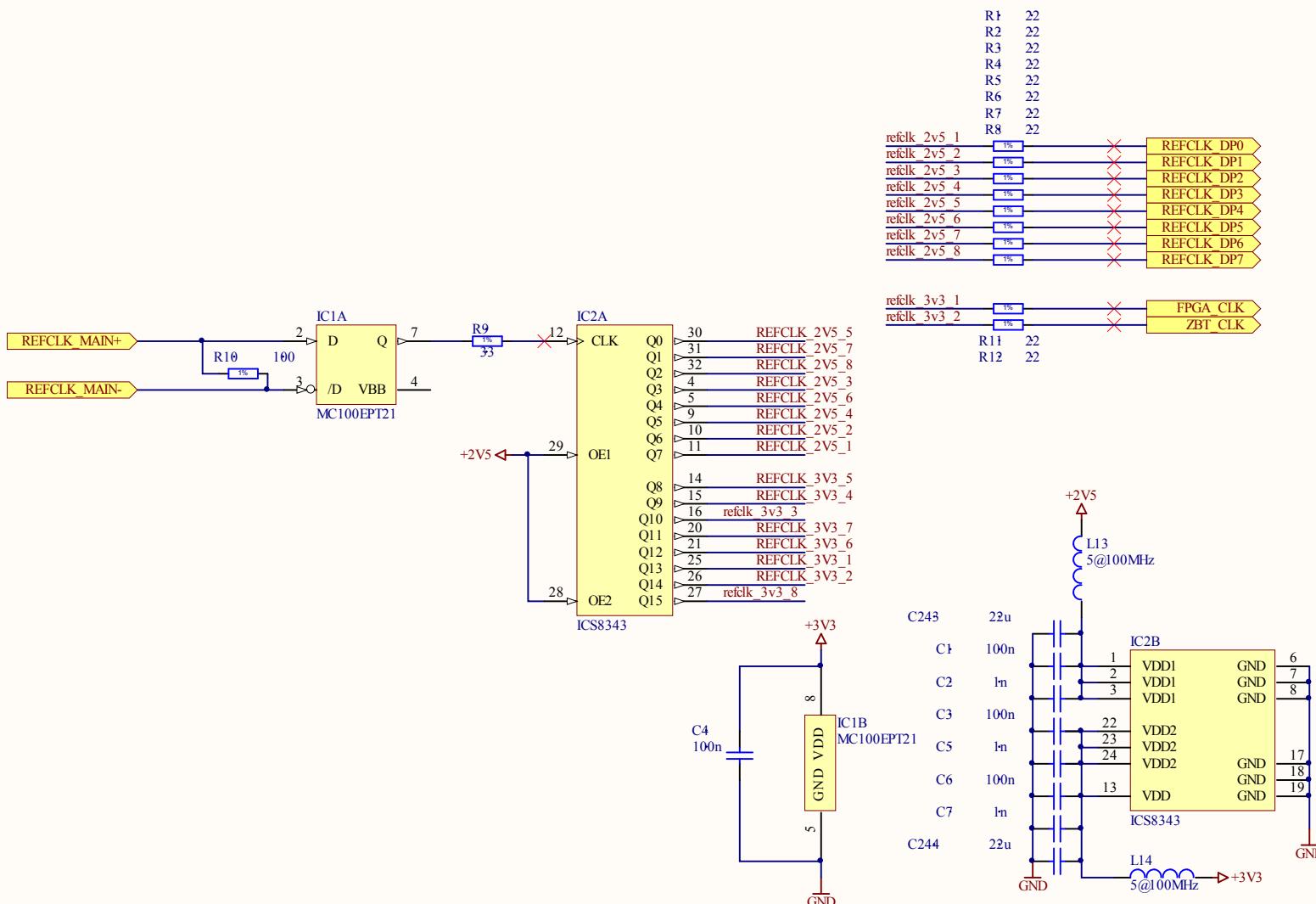
C

D



Title		
White Rabbit Switch MCH - mainboard Backplane downlink PHY		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_downlink_phy	Sub Rev By:

A

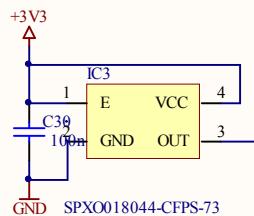


Title
White Rabbit Switch MCH - mainboard
Clock fanout

Size	Number	Revision
A		

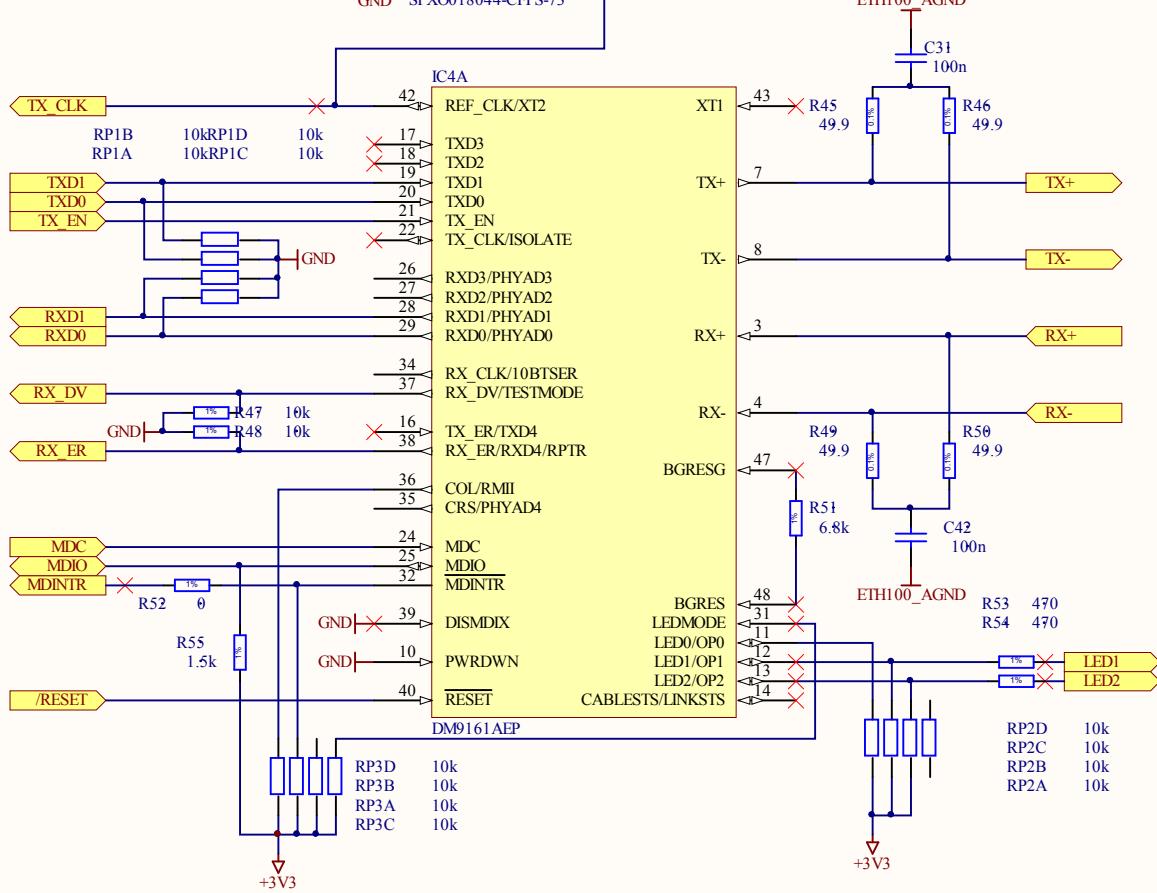
Date: 3/31/2009 Sheet of
File: E:\WhiteRabbit-svn\MB_clocking.SchDoc Drawn By:

A

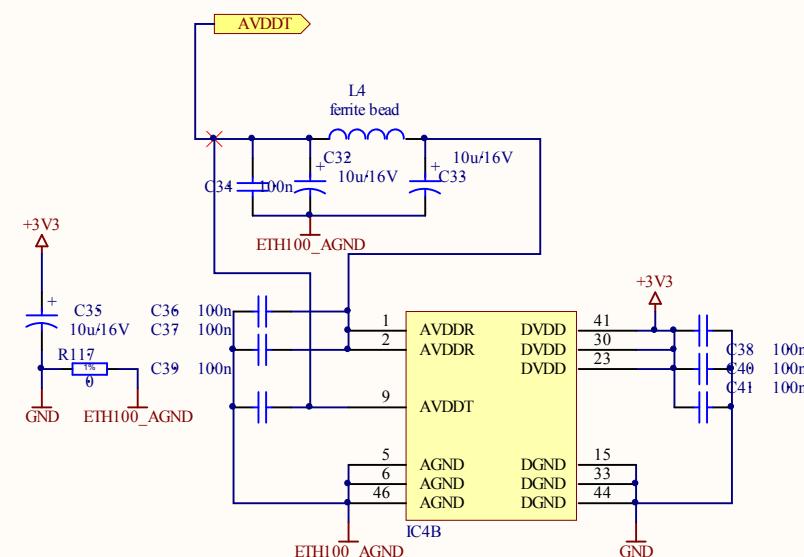


100 Mbit external twisted-pair Ethernet port. Uses DM9161 PHY and RMII interface to communicate with CPU MAC.

B



C



D

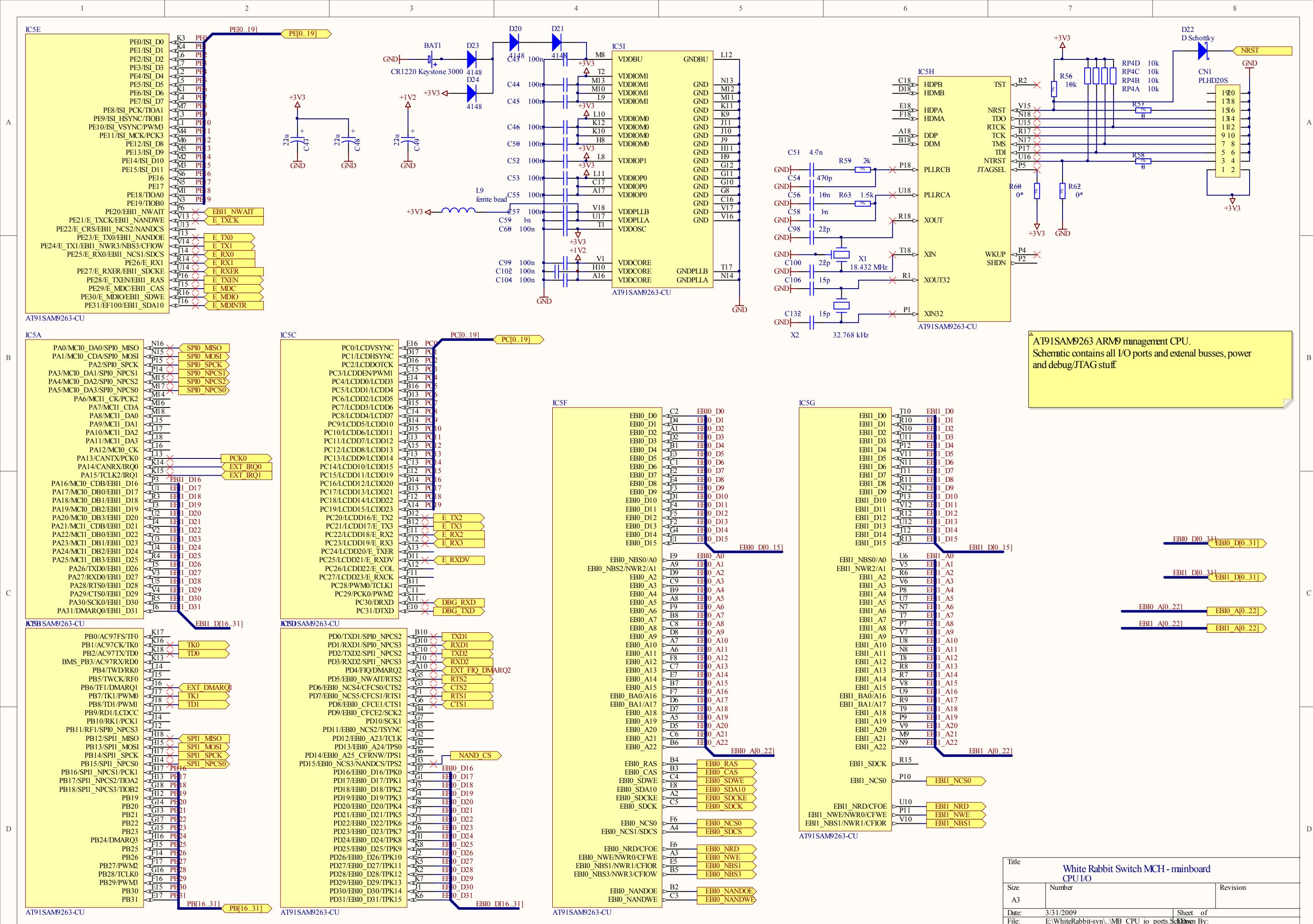
Title		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_CPU_100m_etherDoc	

A

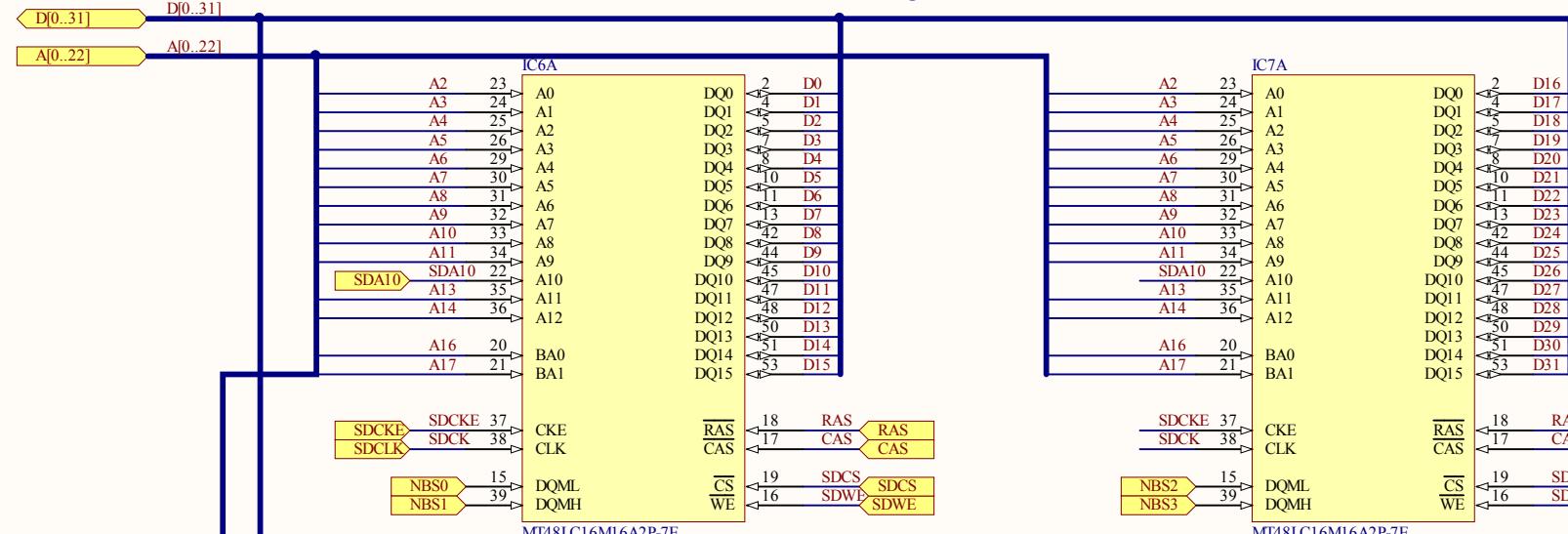
B

C

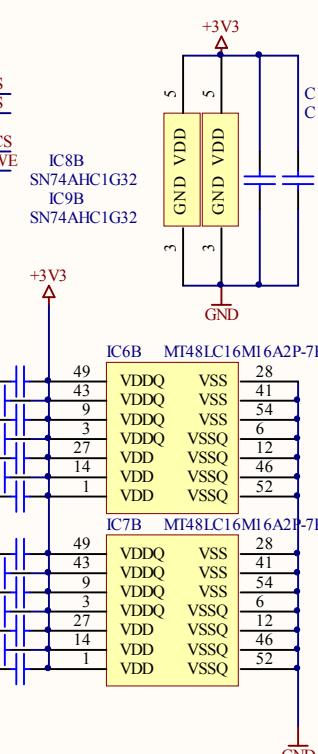
D



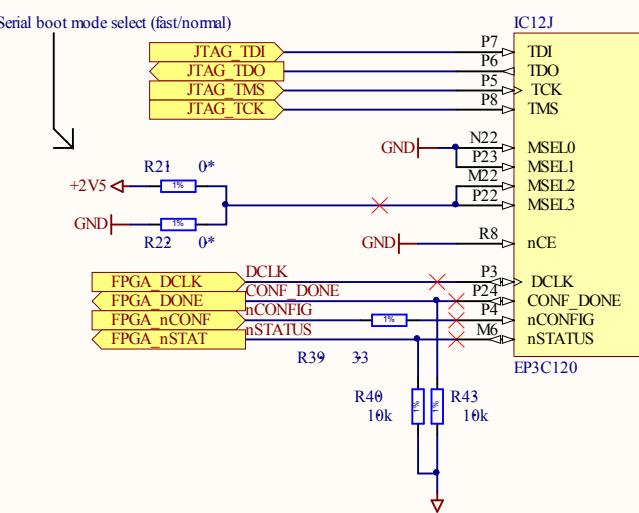
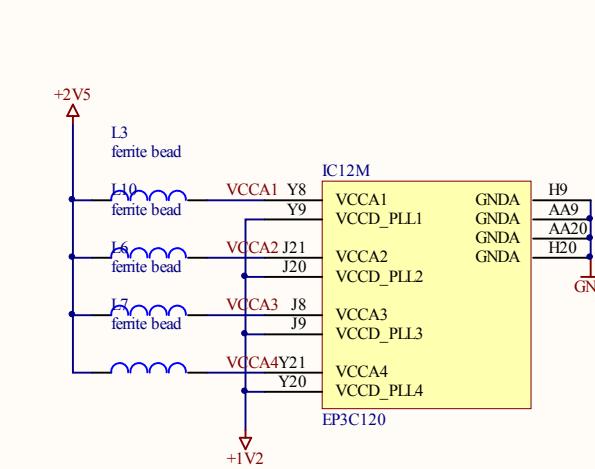
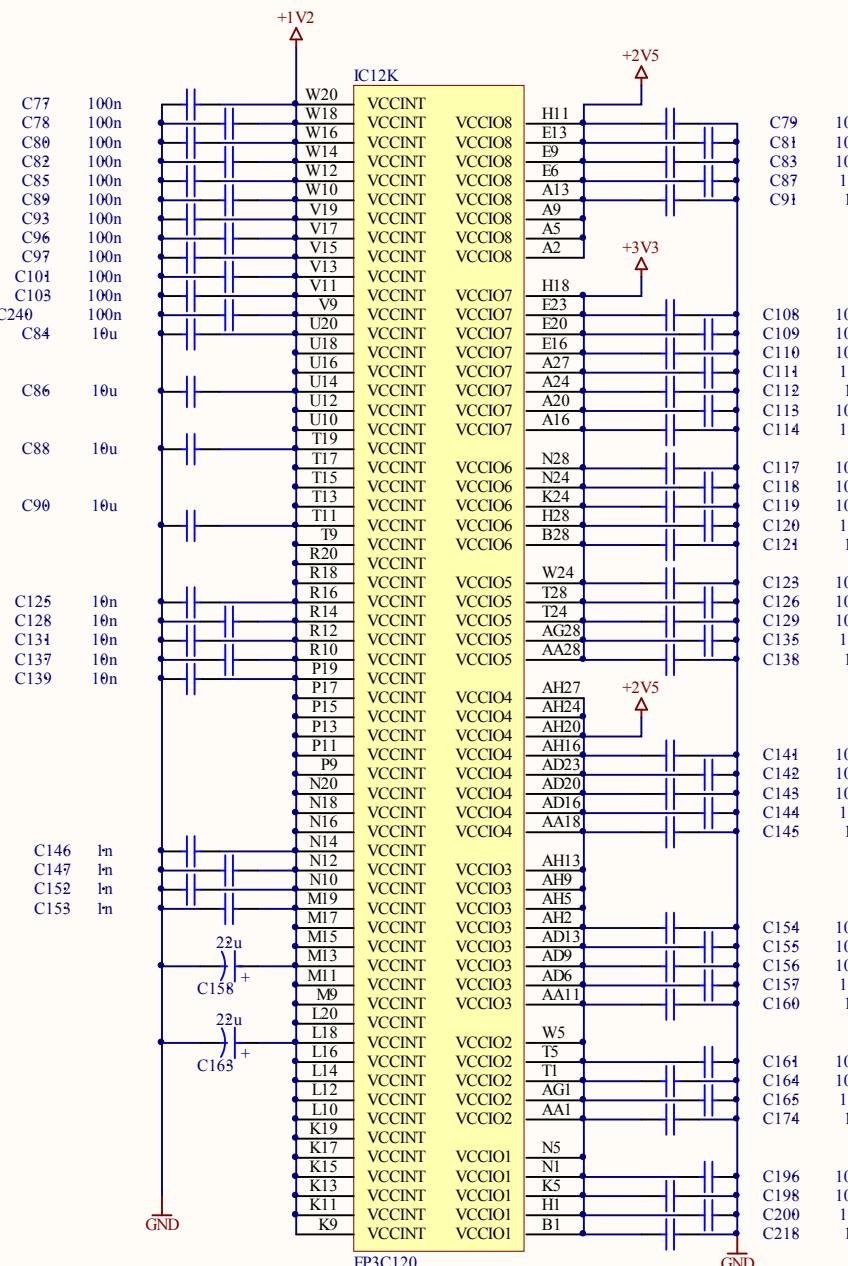
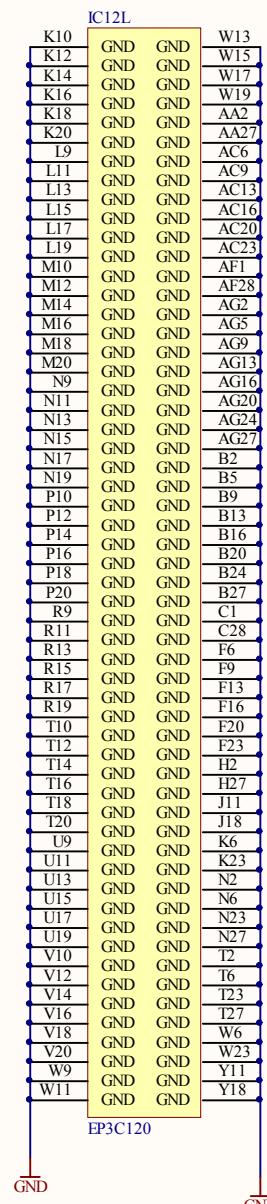
64 MB SDRAM@ 133 MHz(32 bit bus)



CPU memories:
 - 64 MB SDRAM@ 133 MHz
 (32-bit)
 - 256 MB main NAND flash
 - 8 MB boot NAND flash



Title		
White Rabbit Switch MCH - mainboard CPU SDRAM and flash memories		
Size	Number	Revision
A4		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_CPU_memory.Sch	10
Download By:		

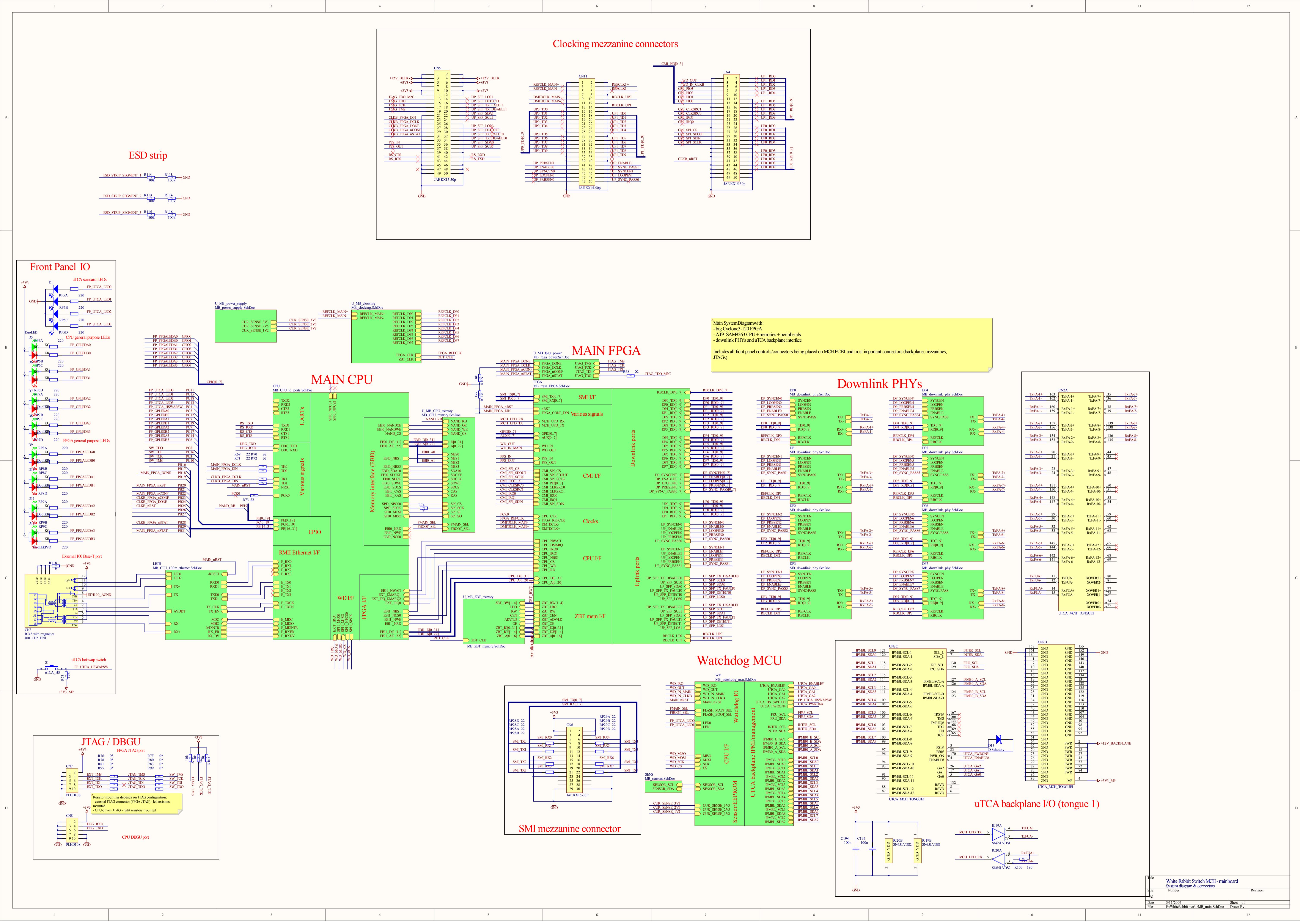


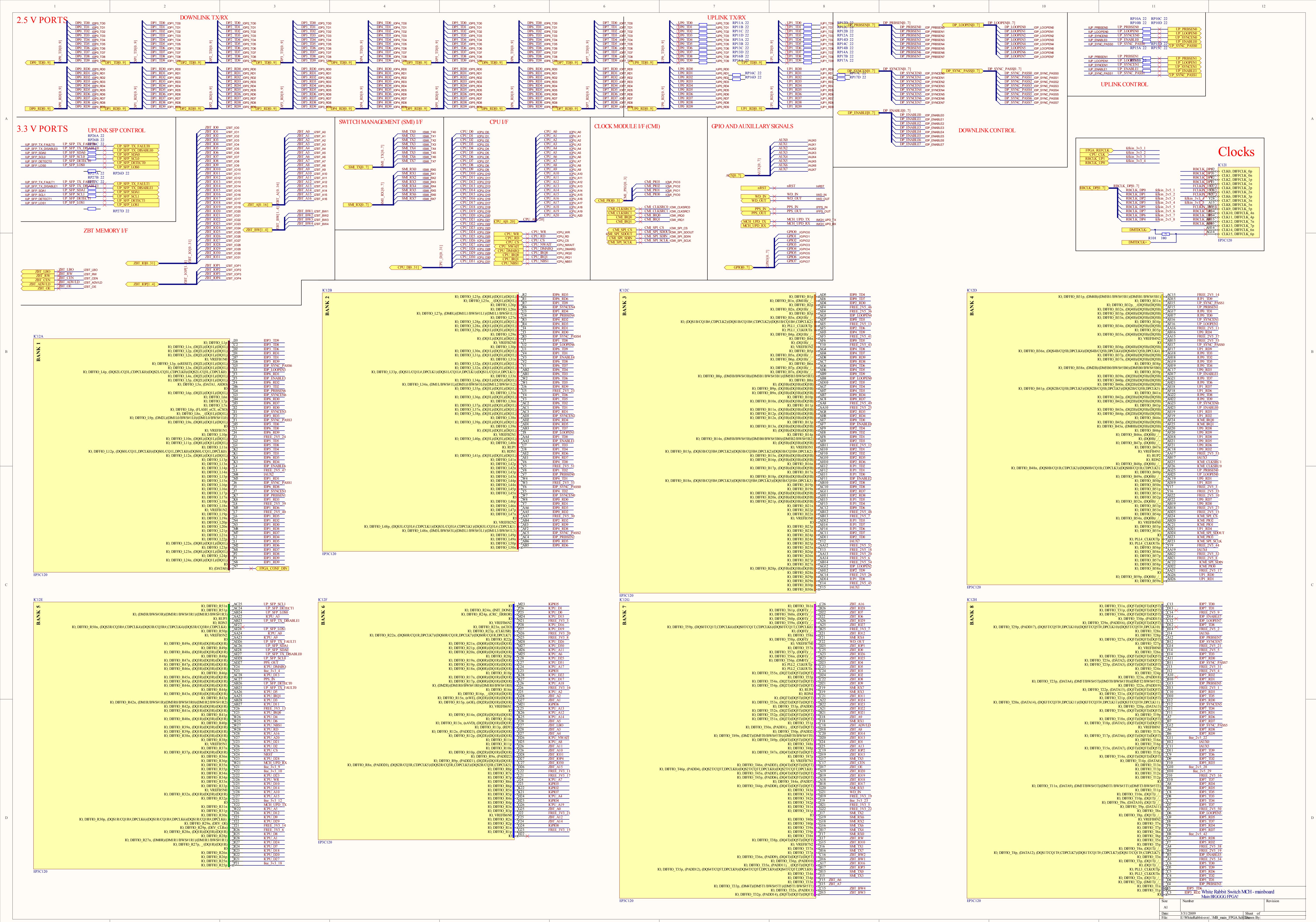
E

Main FPGA power and configuration.

Decoupling caps for VDDINT:
4 x 22u tantalum
24 x 100 n / 0402
10 x 10n / 0402
4x 1n / 0402

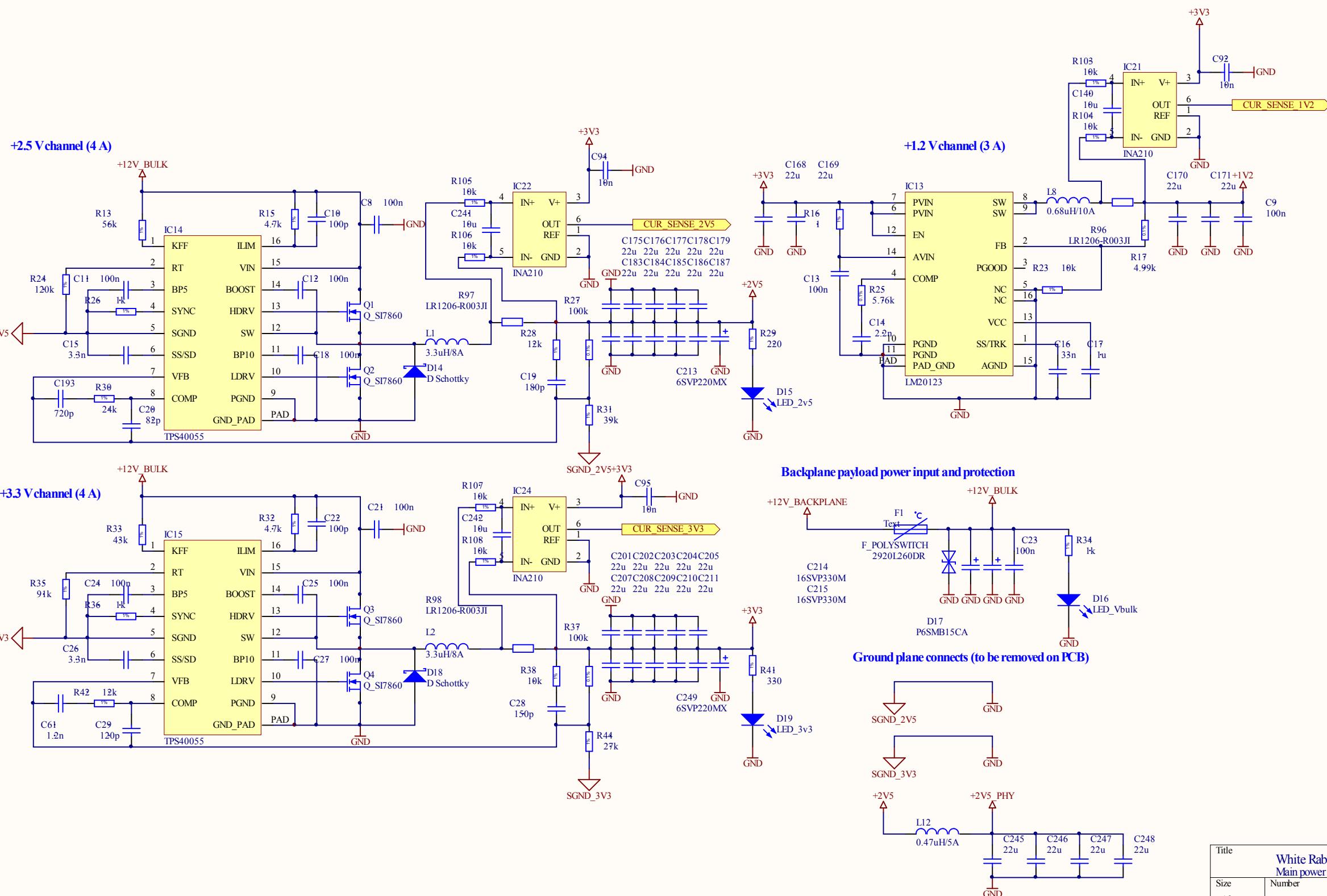
Title		
White Rabbit Switch MCH - mainboard BIGGGG FPGA power and configuration		
Size	Number	Revision
A3		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\..\MB_fpga_power.SchD	Drawn By:





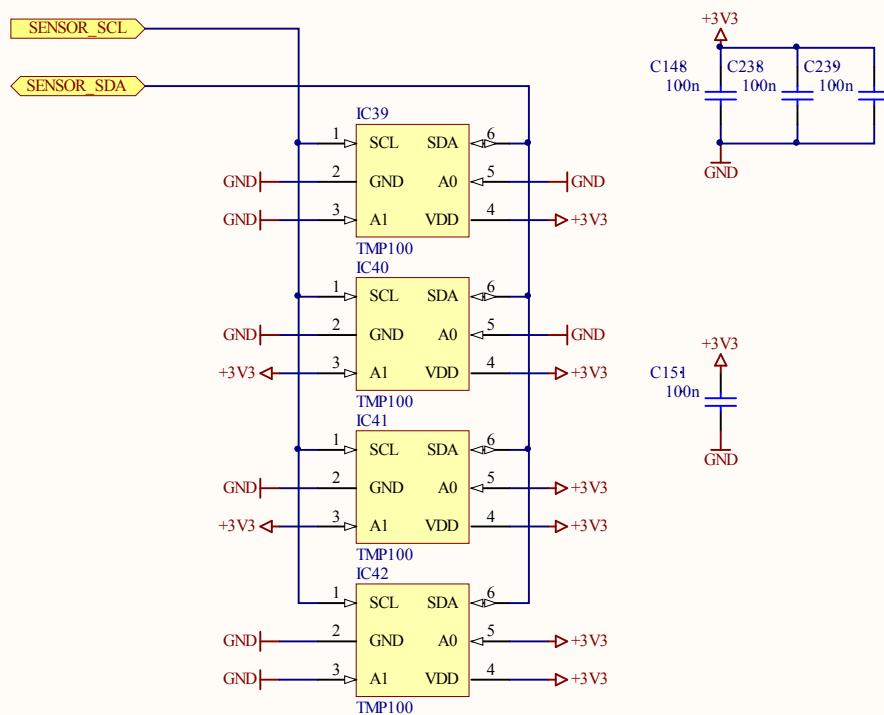
MCH Power supply module. Delivers:
- 3.3 V@4 A
- 2.5 V@4 A
- 1.2 V@3 A

Please note that supply is overrated a bit for optional extensions/modifications

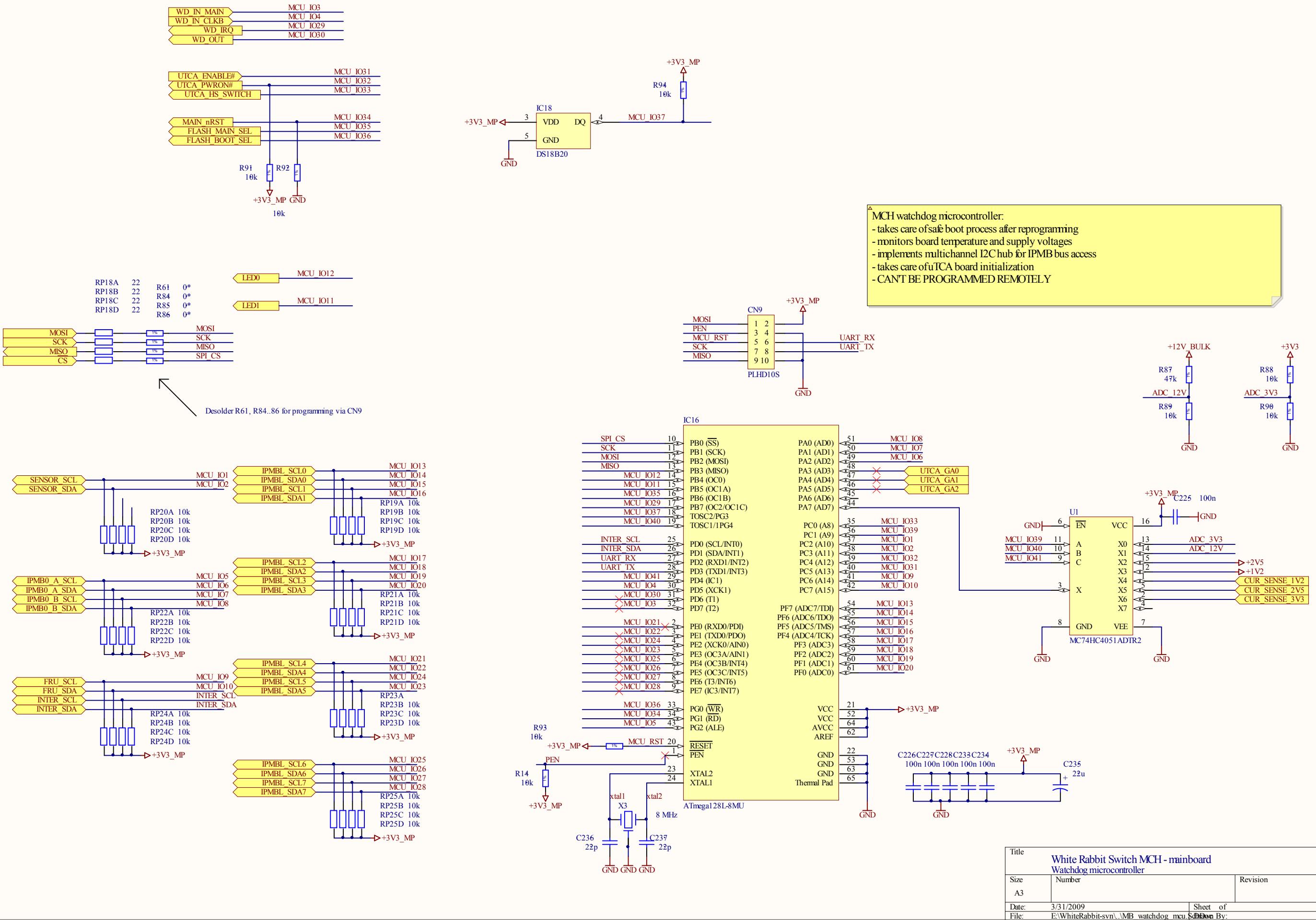


Title		
White Rabbit Switch MCH - mainboard Main power supply		
Size	Number	Revision
A3		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\.\MB_power_supply.SchDoc	8

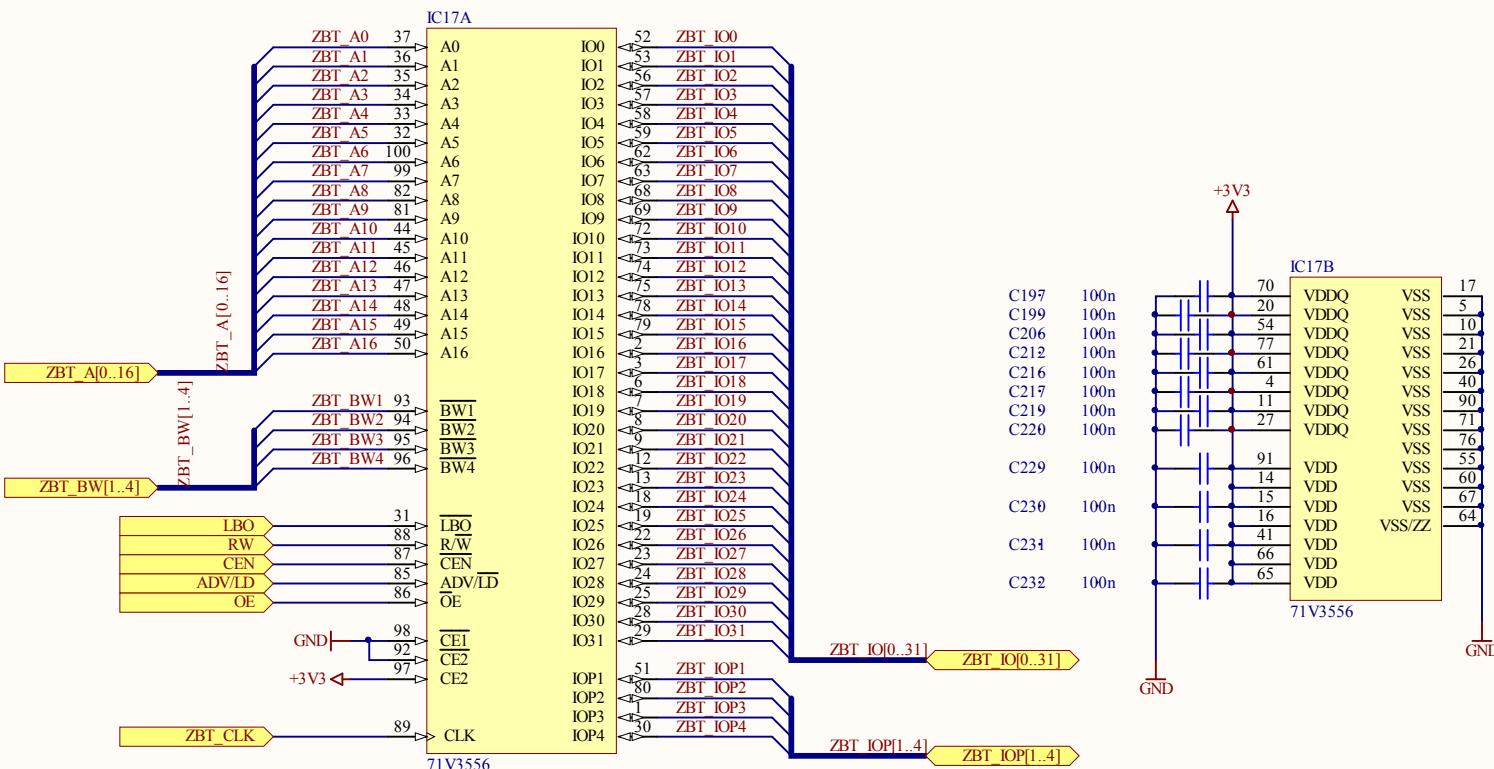
Four (4) I2C temperature sensors, placed in various spots on the MCH PCB!



Title		
White Rabbit Switch MCH - mainboard Temperature sensors		
Size	Number	Revision
A		
Date:	3/31/2009	Sheet of
File:	E:\WhiteRabbit-svn\MB_sensors.SchDoc	Drawn By:



A



Title

White Rabbit Switch MCH - mainboard
ZBT routing table memory

Size

A

Number

Revision

Date:

3/31/2009

Sheet of

File:

E:\WhiteRabbit-svn\MB_ZBT_memory.SchDrawn By: