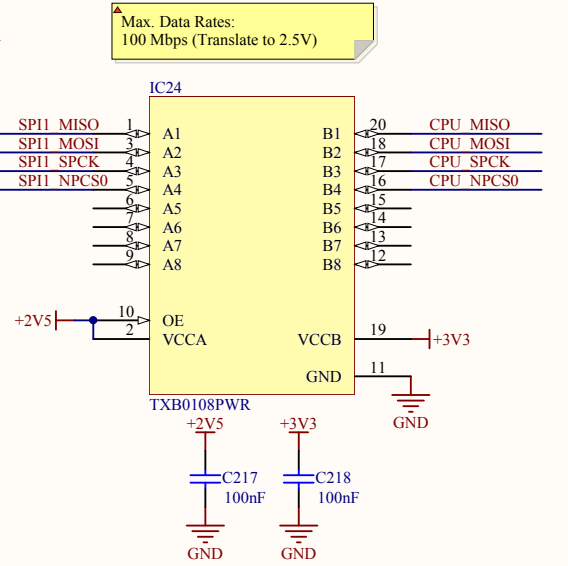
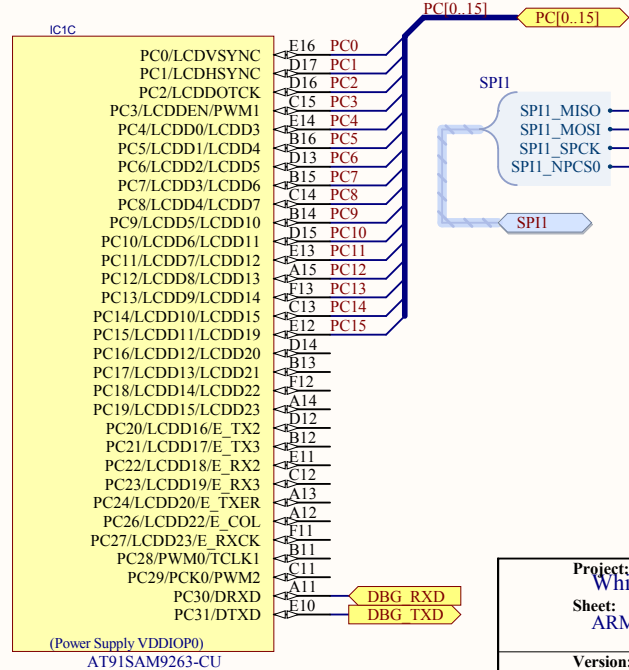
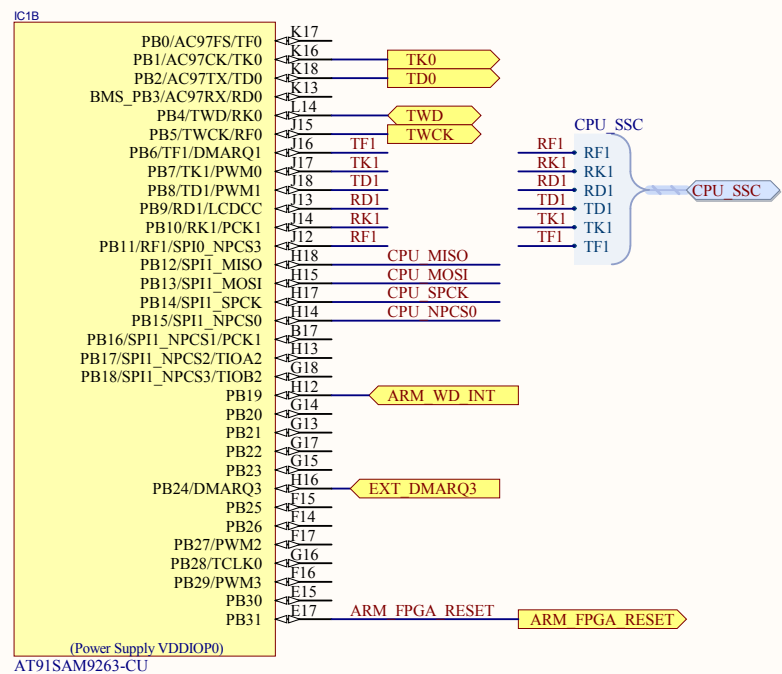
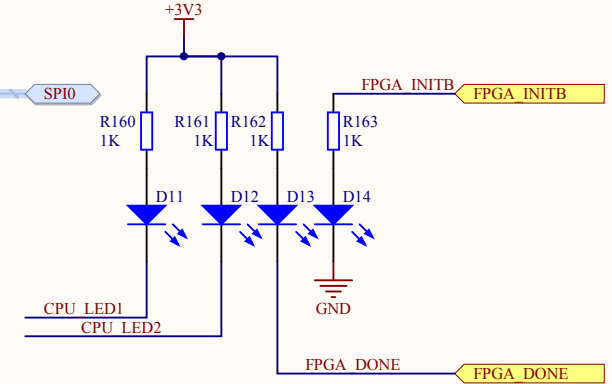
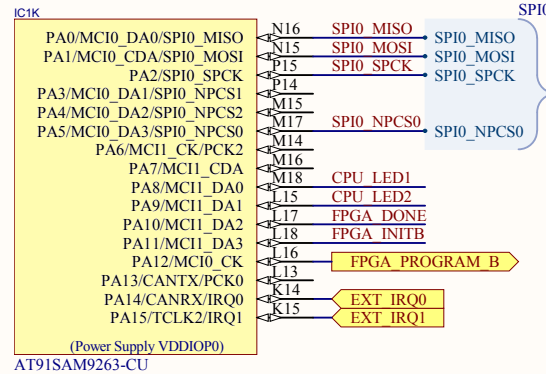
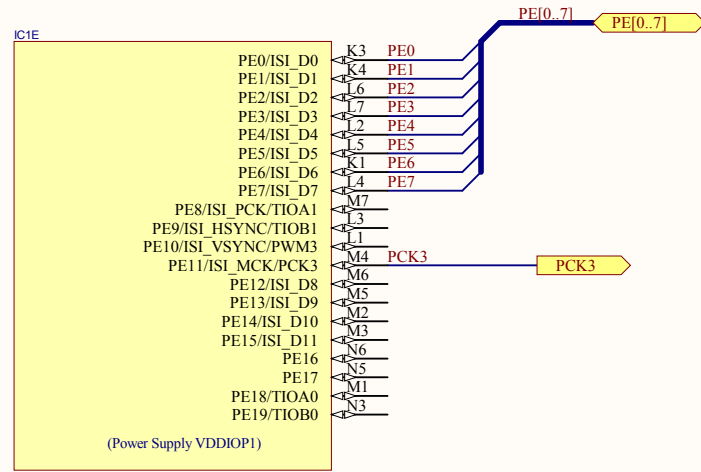
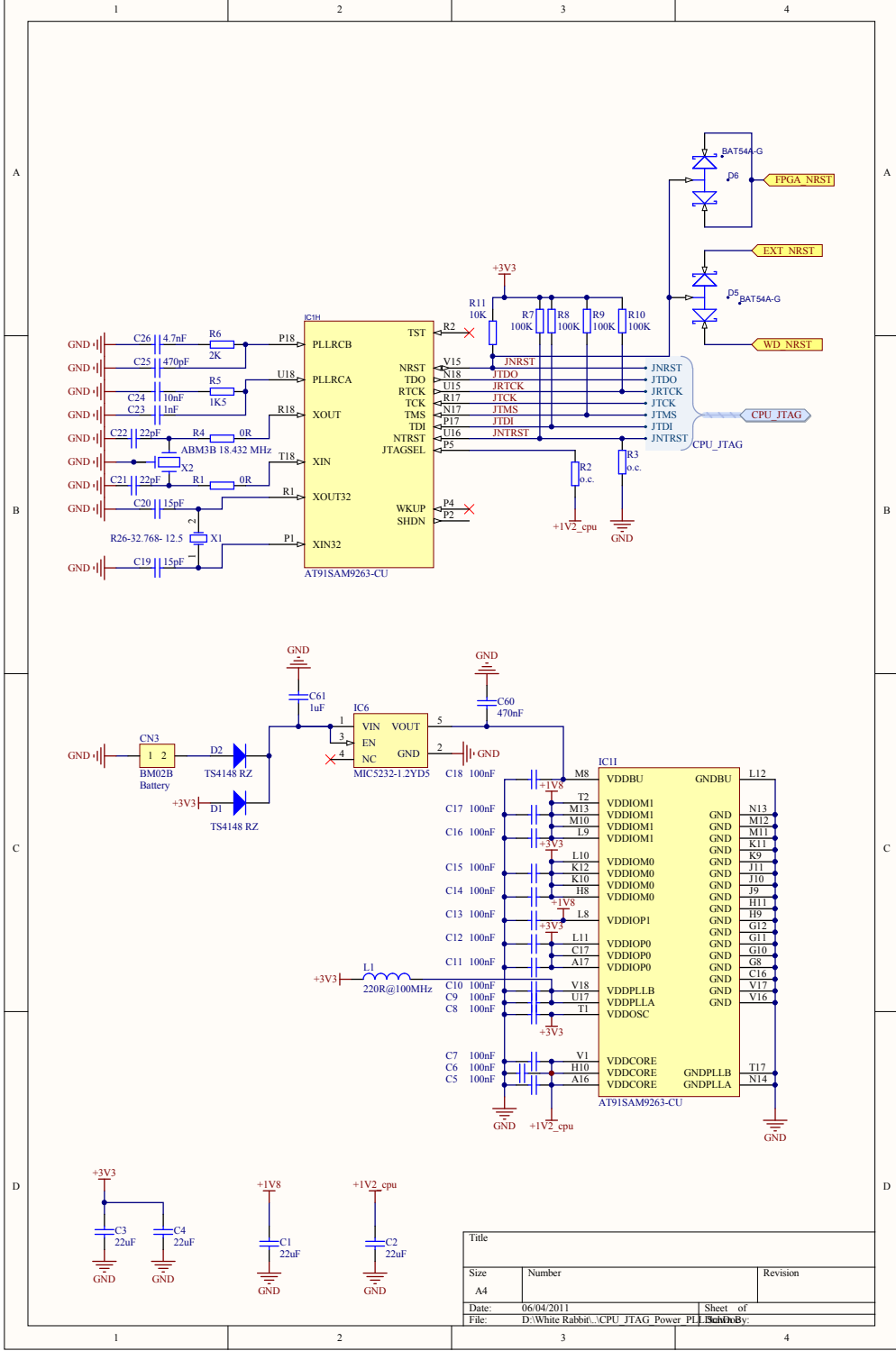


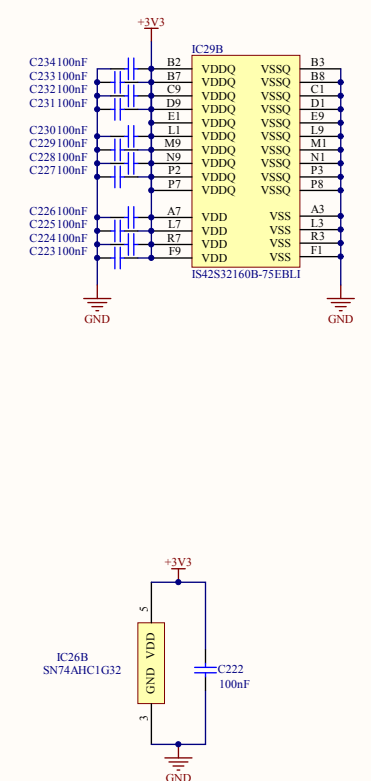
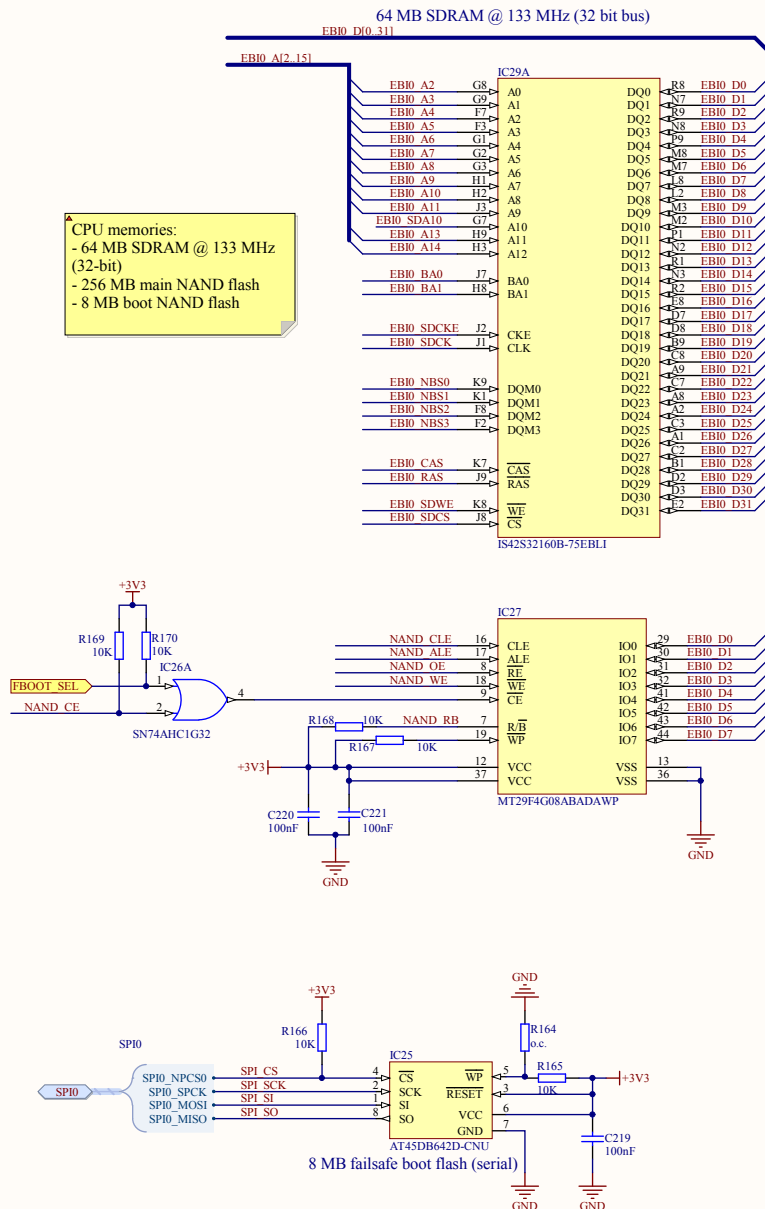
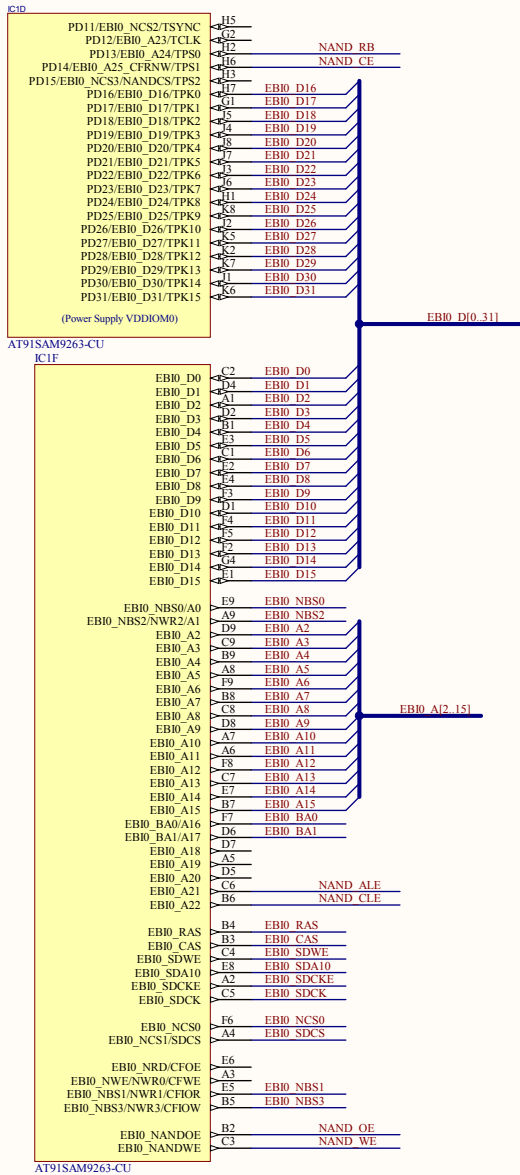
Project: White Rabbit Switch MCH - mainboard	
Sheet: External 100Mbps Ethernet PHY & magnetics	
Version: 1.1.0	Date: 2009/11/12
Author: Tomasz Wlostowski	License: Open Hardware License (OHL)
Company: CERN BE-CO-HT	



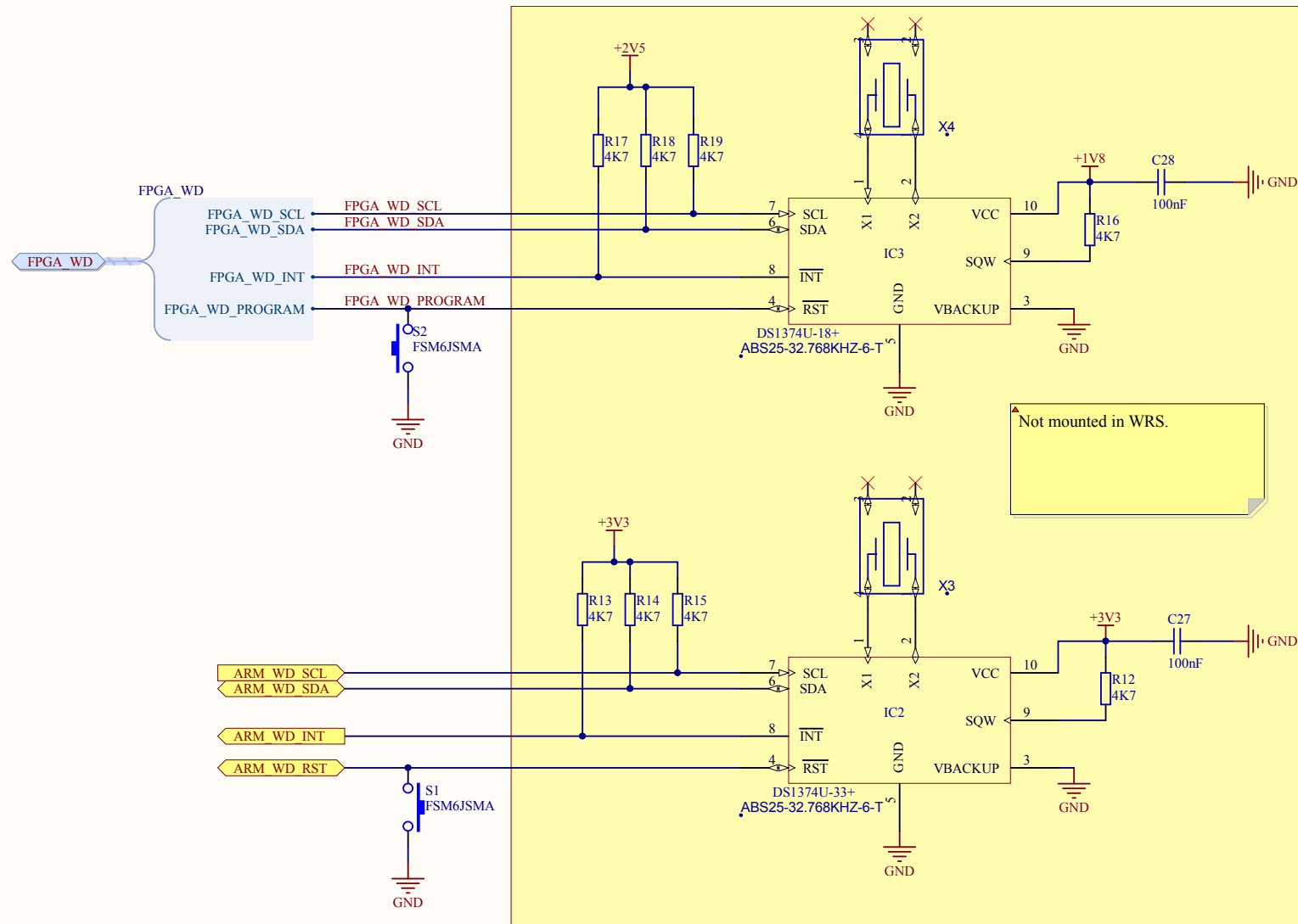
Project: White Rabbit Switch MCH - mainboard	
Sheet: ARM9 CPU I/O ports, busses and power	
Version: 1.1.0	Date: 2009/11/12
Author: Tomasz Wlostowski	License: Open Hardware License (OHL)
Company: CERN BE-CO-HT	



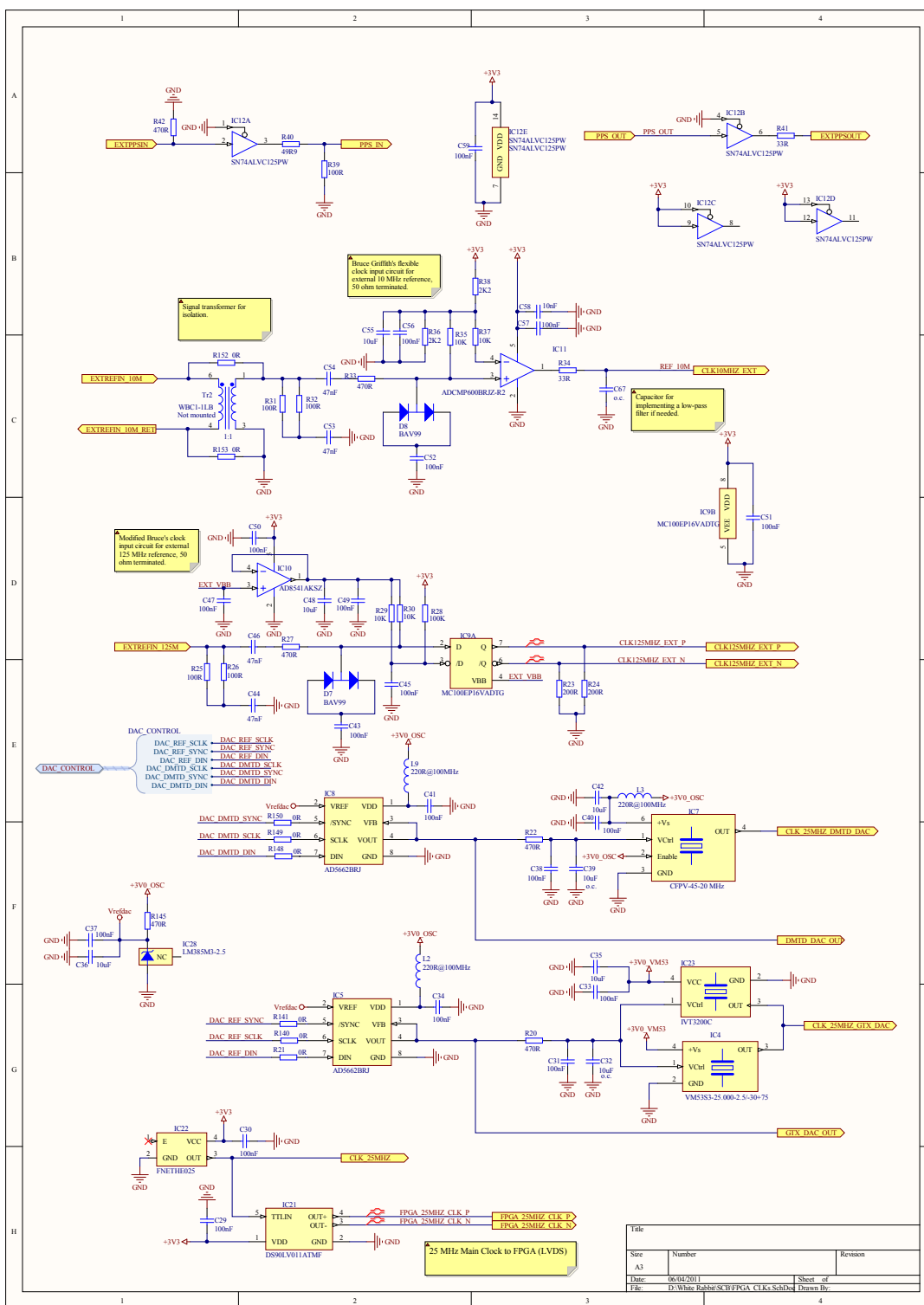
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Size	Number	Revision
A4		
Date:	06/04/2011	Sheet of
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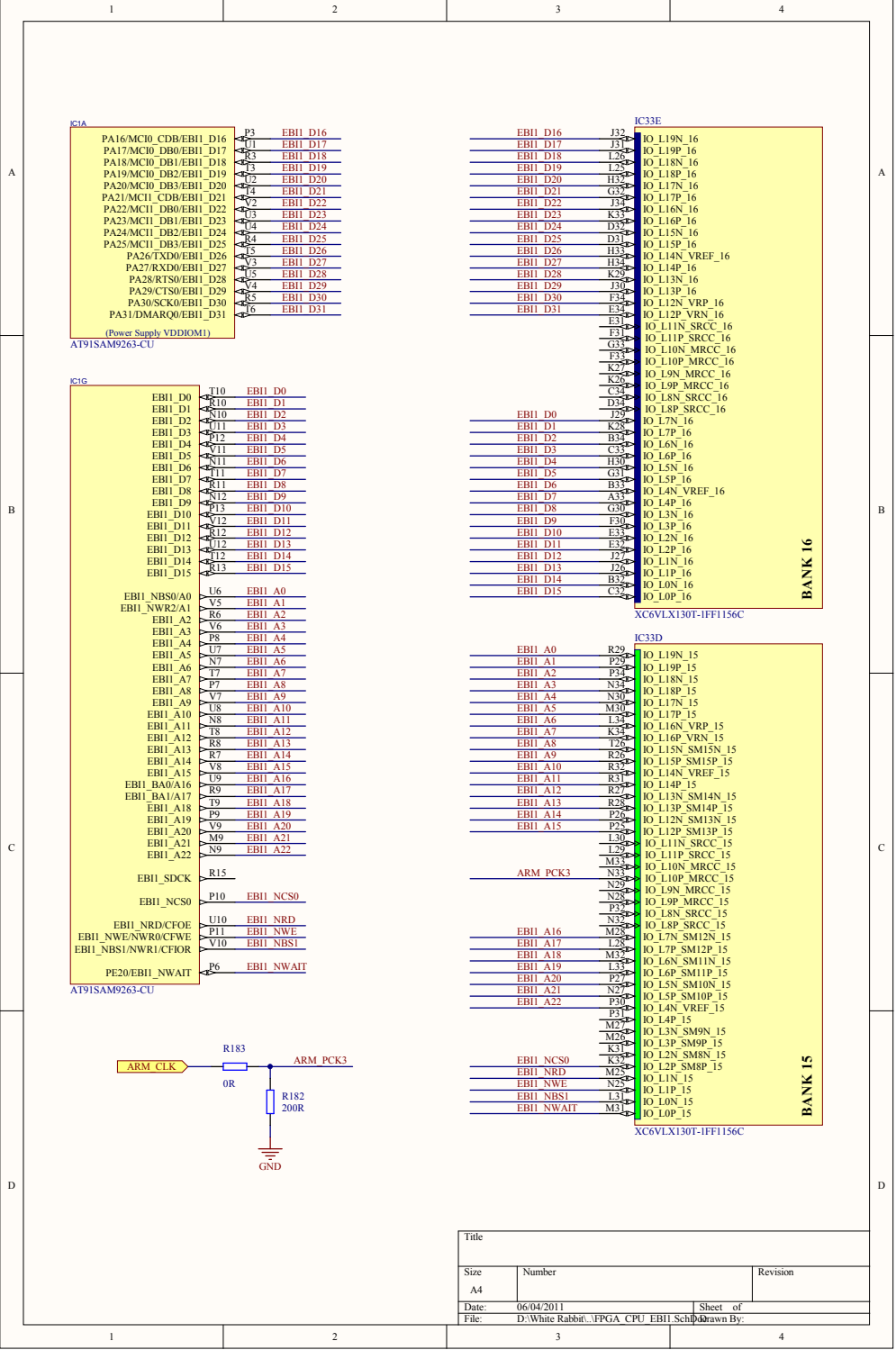


Project: White Rabbit Switch MCH - mainboard	
Sheet: ARM9 CPU SDRAM and flash memories	
Version: 1.1.0	Date: 2009/11/12
Author: Tomasz Wlostowski	License: Open Hardware License (OHL)
Company: CERN BE-CO-HT	



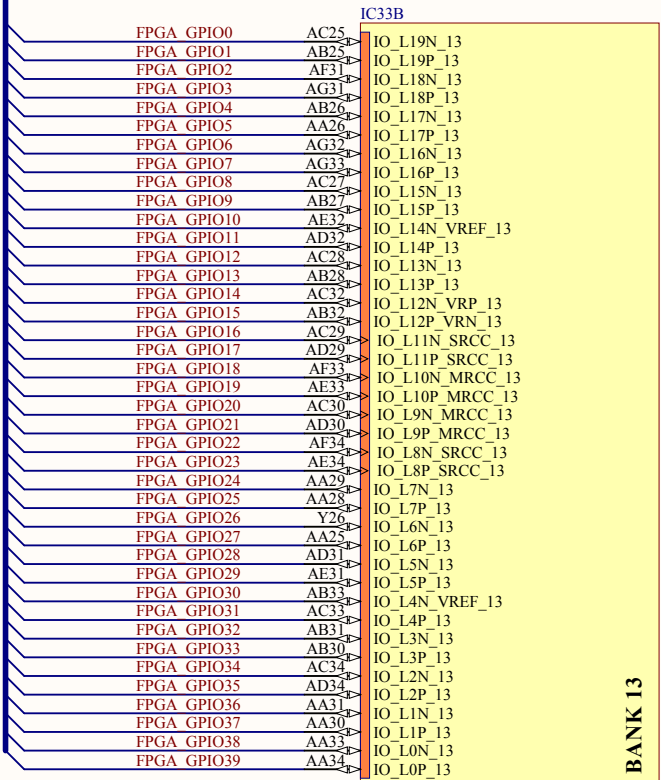
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A4		
Date:	06/04/2011	Sheet of
File:	D:\White Rabbit\External WatchDogs Sch Down By:	





FPGA_GPIO[0..39]

FPGA_GPIO[0..39]



IC33B

XC6VLX130T-1FF1156C

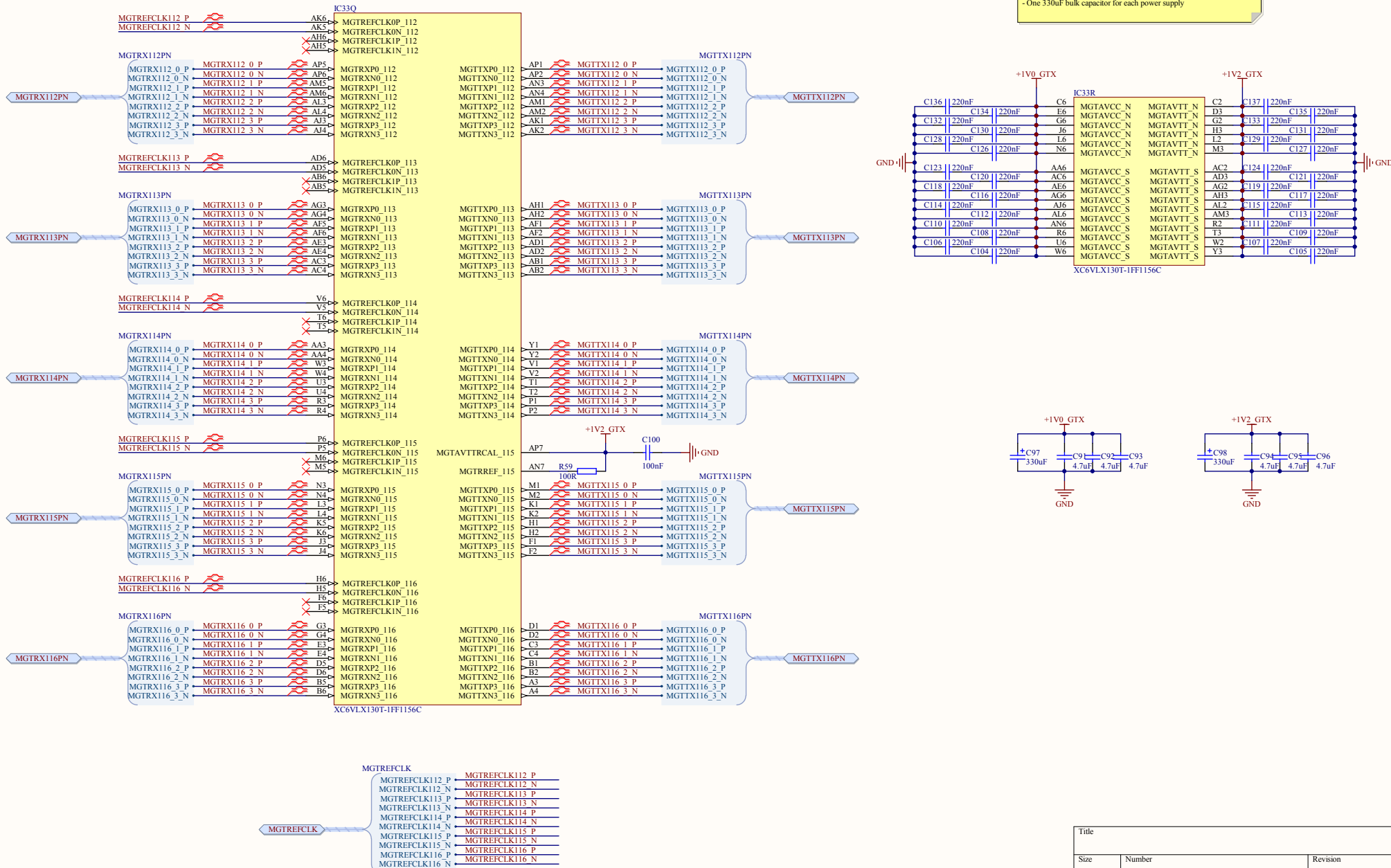
BANK 13

Title		
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Power Supply Decoupling Capacitors

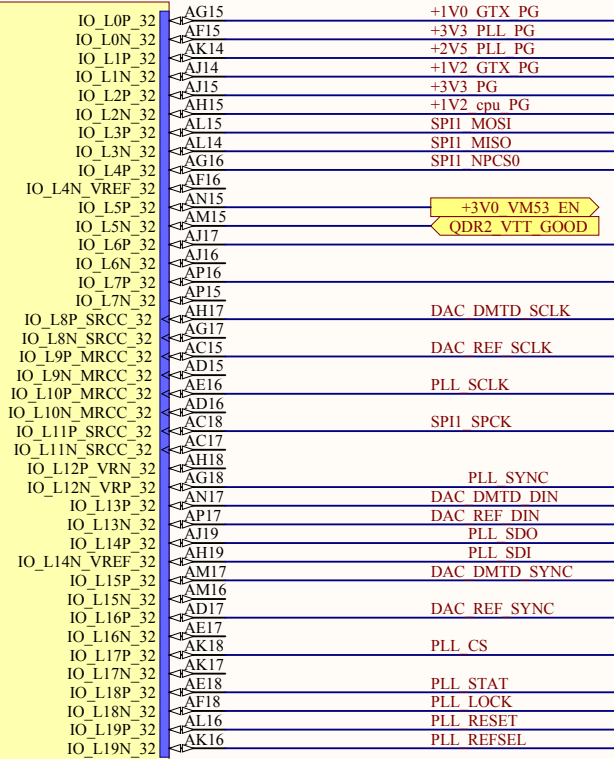
According to Xilinx UG366 (v2.3), page 230, the suggested filtering for the MGTAVCC and MGTAVTT power supplies is:

- One 0.22uF, size 0402, ceramic capacitor per power supply pin
- One 4.7uF, size 0402, ceramic capacitor per two Quads
- One 330uF bulk capacitor for each power supply

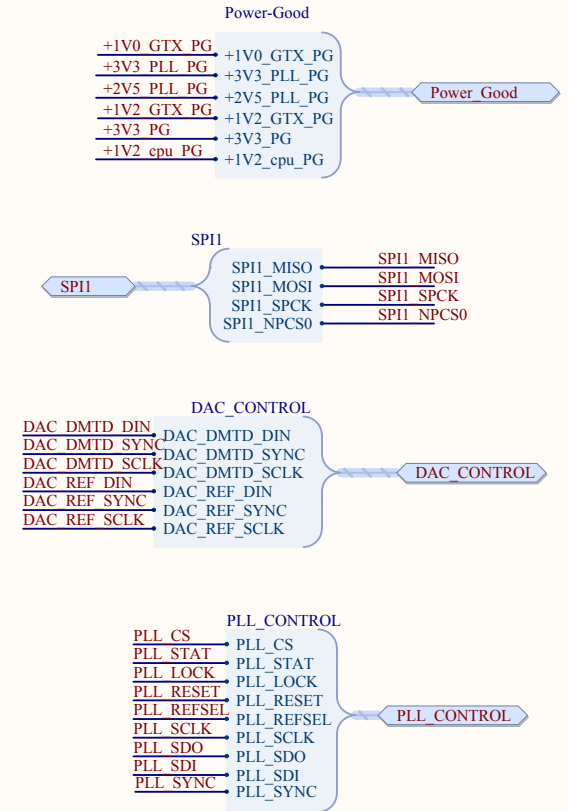
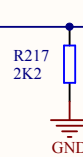


IC33K

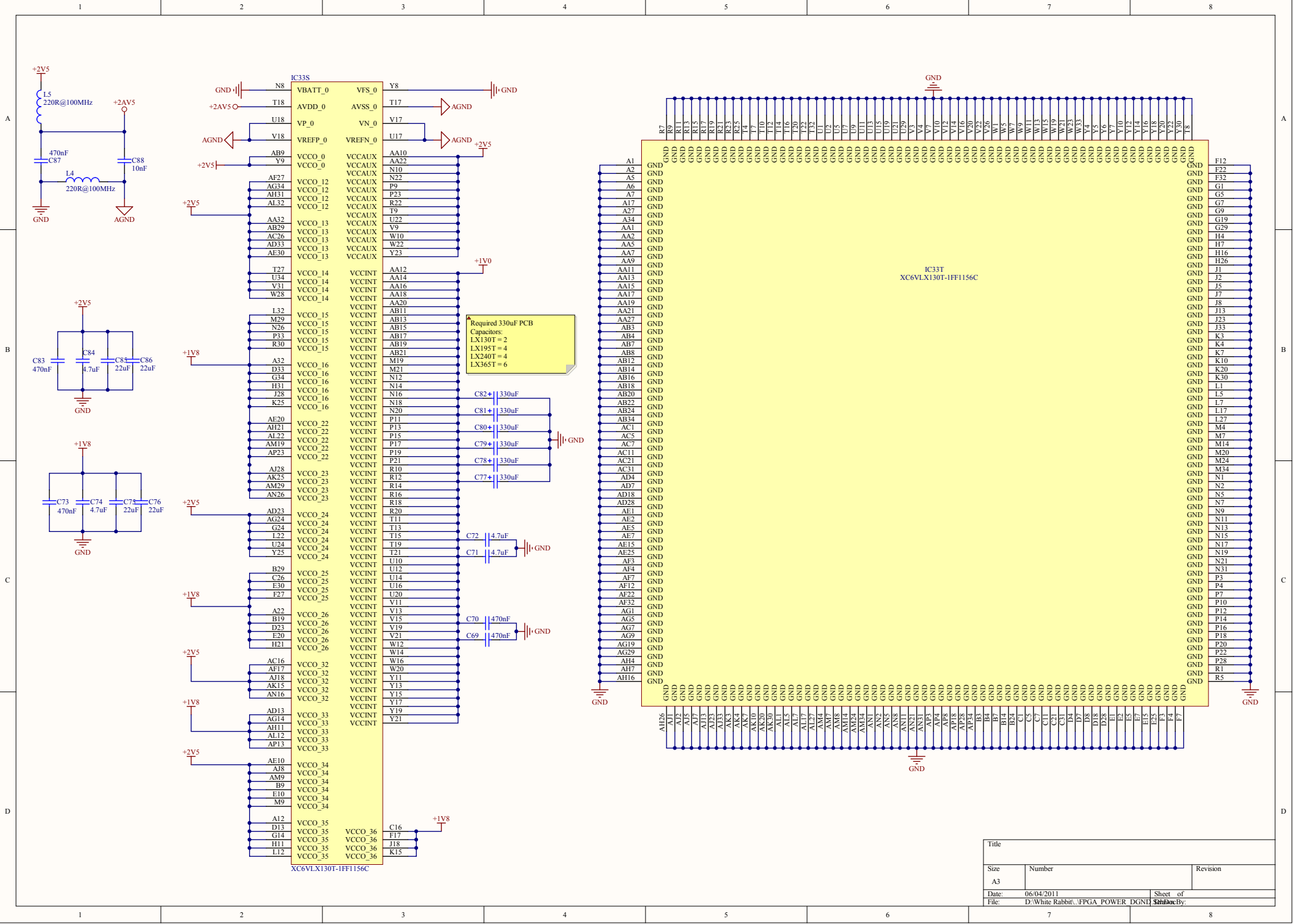
BANK 32



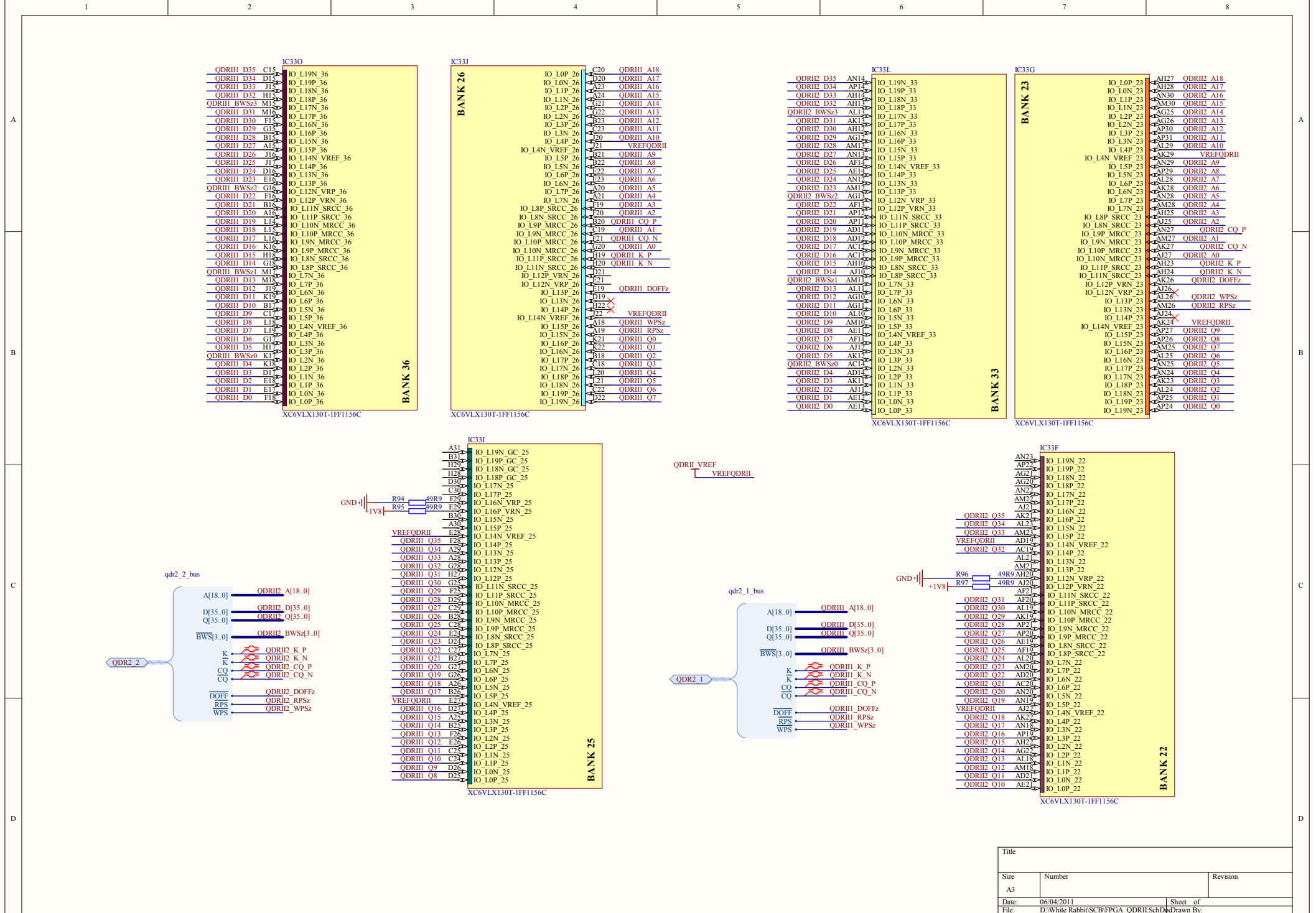
XC6VLX130T-1FF1156C



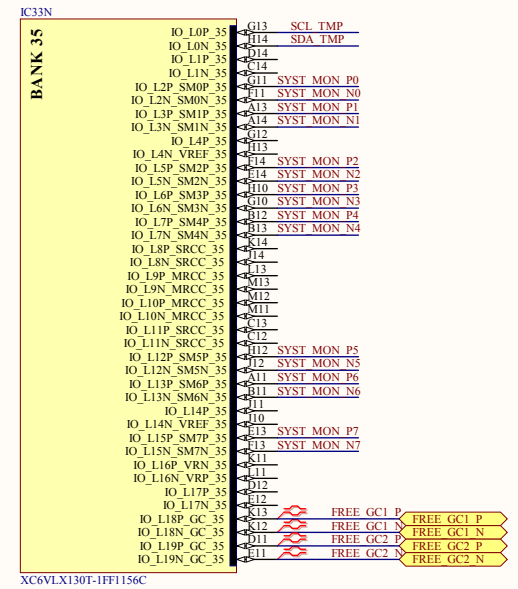
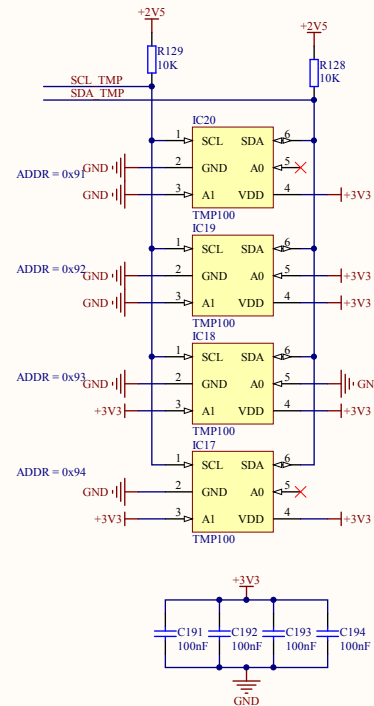
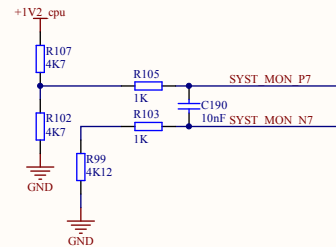
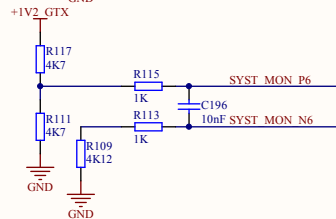
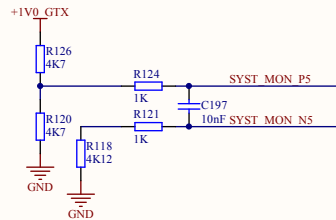
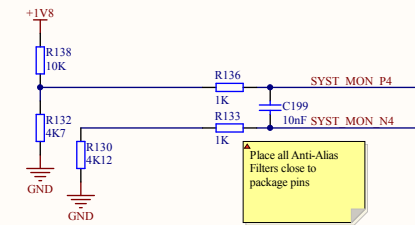
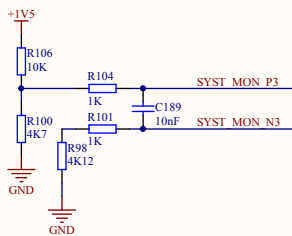
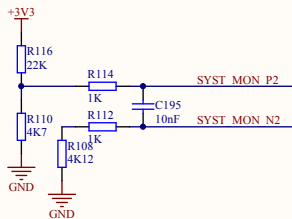
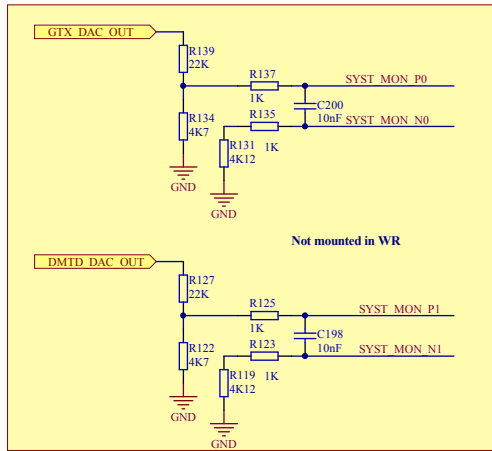
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Date:	06/04/2011	Sheet of
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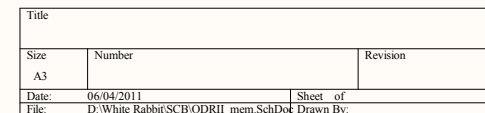
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Size	Number	Revision
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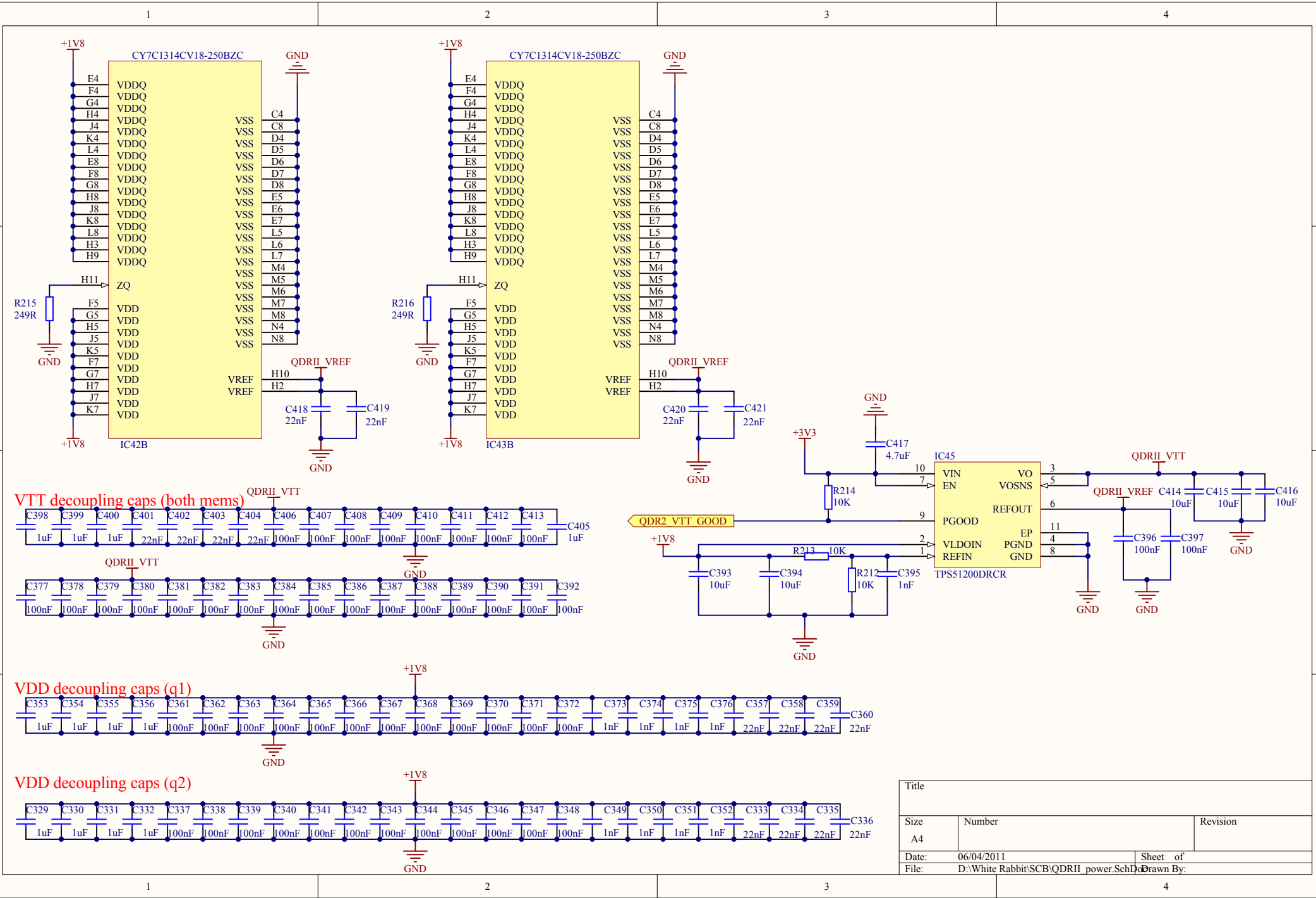


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Size	Number	Revision
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Date:	06/04/2011	Sheet of
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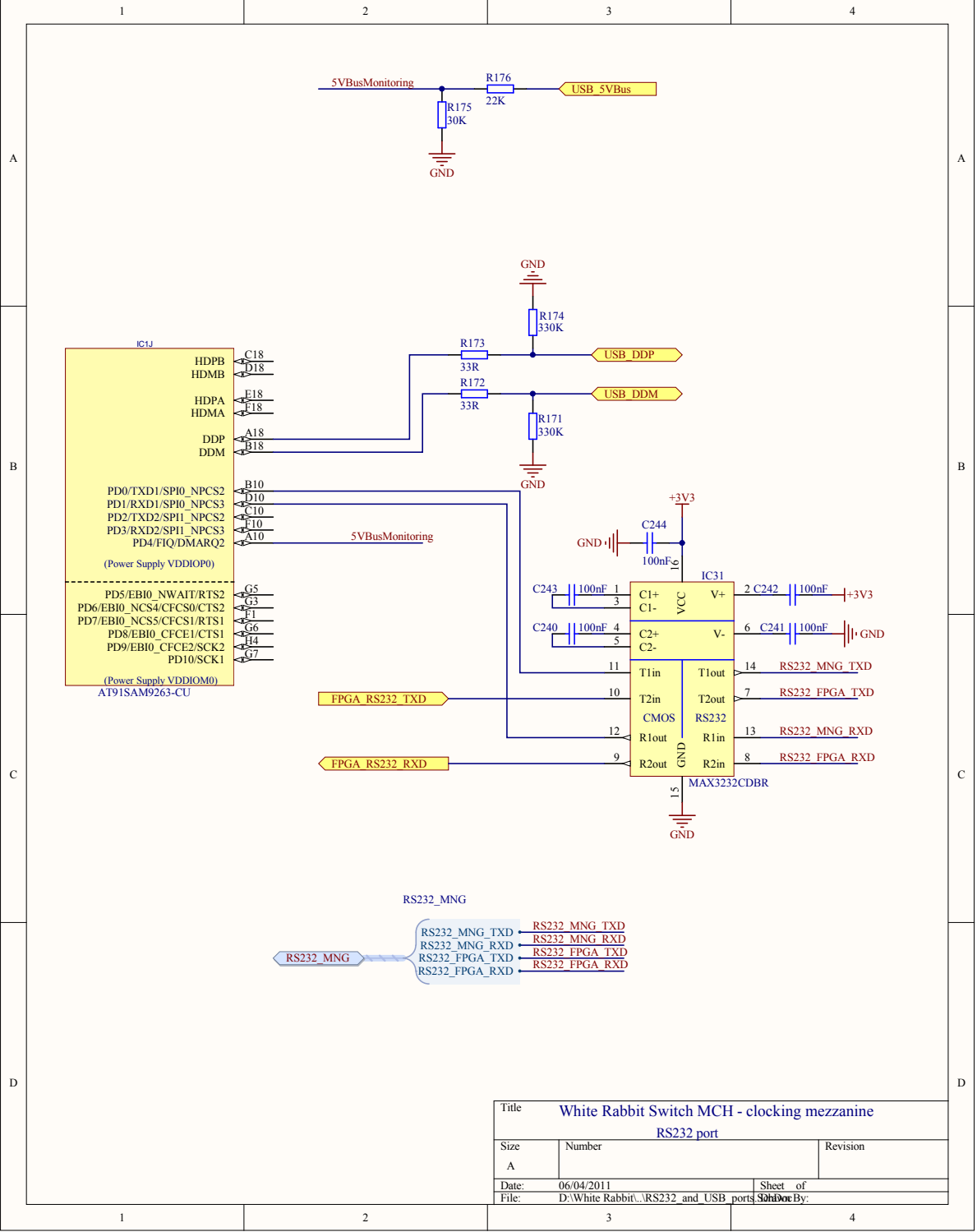


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Size	Number	Revision
A3		
Date:	06/04/2011	Sheet of
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Title		
Size	Number	Revision
A4		
Date:	06/04/2011	Sheet of
File:	D:\White Rabbit\SCB\QDRIL power.SchDoc	Drawn By:



Title		
White Rabbit Switch MCH - clocking mezzanine		
RS232 port		
Size	Number	Revision
A		
Date:	06/04/2011	Sheet of
File:	D:\White Rabbit_\RS232 and USB ports	Solved By:

IC33A

BANK 12

IO_L0P_12
IO_L0N_12
IO_L1P_12
IO_L1N_12
IO_L2P_12
IO_L2N_12
IO_L3P_12
IO_L3N_12
IO_L4P_12
IO_L4N_VREF_12
IO_L5P_12
IO_L5N_12
IO_L6P_12
IO_L6N_12
IO_L7P_12
IO_L7N_12
IO_L8P_SRCC_12
IO_L8N_SRCC_12
IO_L9P_MRCC_12
IO_L9N_MRCC_12
IO_L10P_MRCC_12
IO_L10N_MRCC_12
IO_L11P_SRCC_12
IO_L11N_SRCC_12
IO_L12P_VRN_12
IO_L12N_VRP_12
IO_L13P_12
IO_L13N_12
IO_L14P_12
IO_L14N_VREF_12
IO_L15P_12
IO_L15N_12
IO_L16P_12
IO_L16N_12
IO_L17P_12
IO_L17N_12
IO_L18P_12
IO_L18N_12
IO_L19P_12
IO_L19N_12

AD25
AD26
AE27
AD27
AH33
AH32
AE28
AE29
AJ34
AH34
AF28
AF29
AJ34
AK34
AH29
AH30
AN33
AN34
AG27
AG28
AF30
AG30
AF26
AE26
AJ31
AJ32
AJ29
AJ30
AK33
AK32
AL31
AK31
AM33
AL33
AN32
AM32
AP32
AP33
AL30
AM31

TxFD12 P
TxFD12 N
TxFD11 P
TxFD11 N
TxFD10 P
TxFD10 N
TxFD9 P
TxFD9 N
TxFD8 P
TxFD8 N
TxFD7 P
TxFD7 N

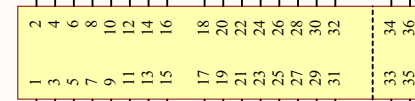
FREE SRCC P
FREE SRCC N

FREE MRCC P
FREE MRCC N

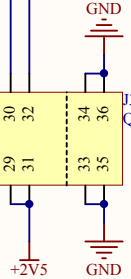
RxFD12 P
RxFD12 N
RxFD11 P
RxFD11 N
RxFD10 P
RxFD10 N
RxFD9 P
RxFD9 N
RxFD8 P
RxFD8 N
RxFD7 P
RxFD7 N

FREE SRCC P
FREE SRCC N

FREE MRCC P
FREE MRCC N



FREE GC P
FREE GC N
FREE GC P
FREE GC N



QSS-016-01-L-D-DP

XC6VLX130T-1FF1156C

Title		
Size A4	Number	Revision
Date: 06/04/2011	Sheet of	
File: D:\White Rabbit\SMI Link 7-12.SchDoc	Drawn By:	

