

Level: 3.3 V LVCMOS

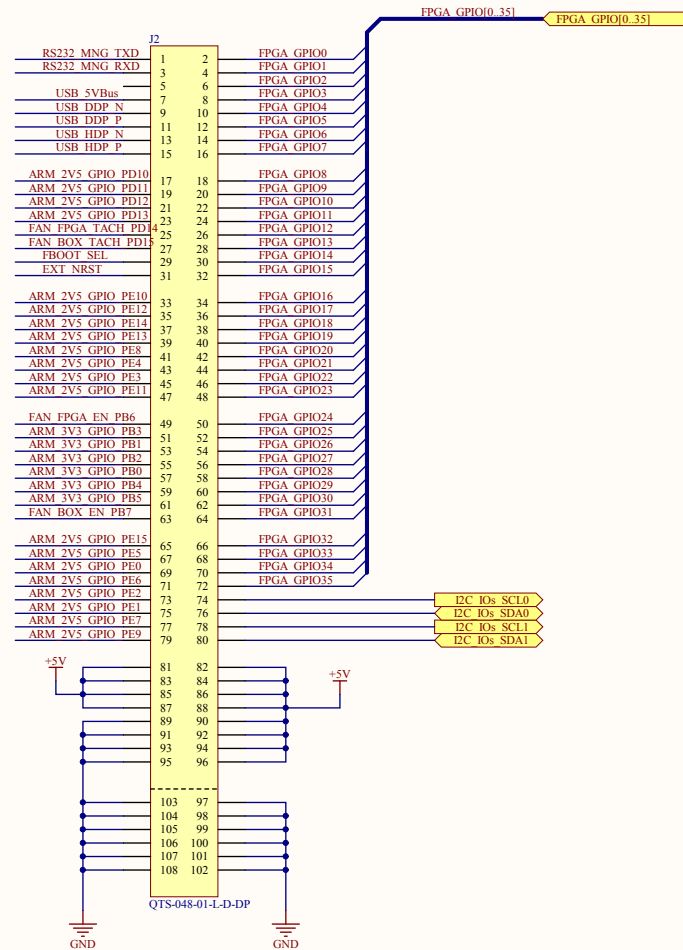
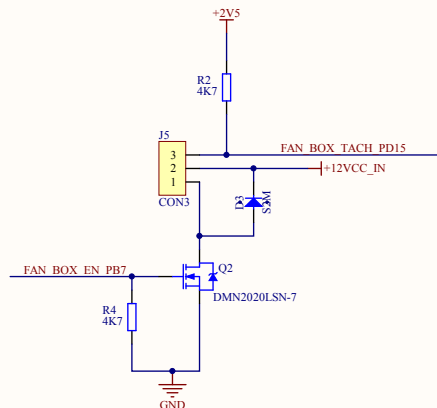
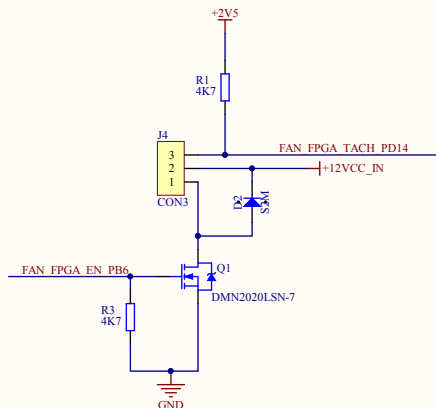
ARM 3V3 GPIO PB[0..5]

Level: 2.5 V LVCMOS

ARM 2V5 GPIO PD[10..13]

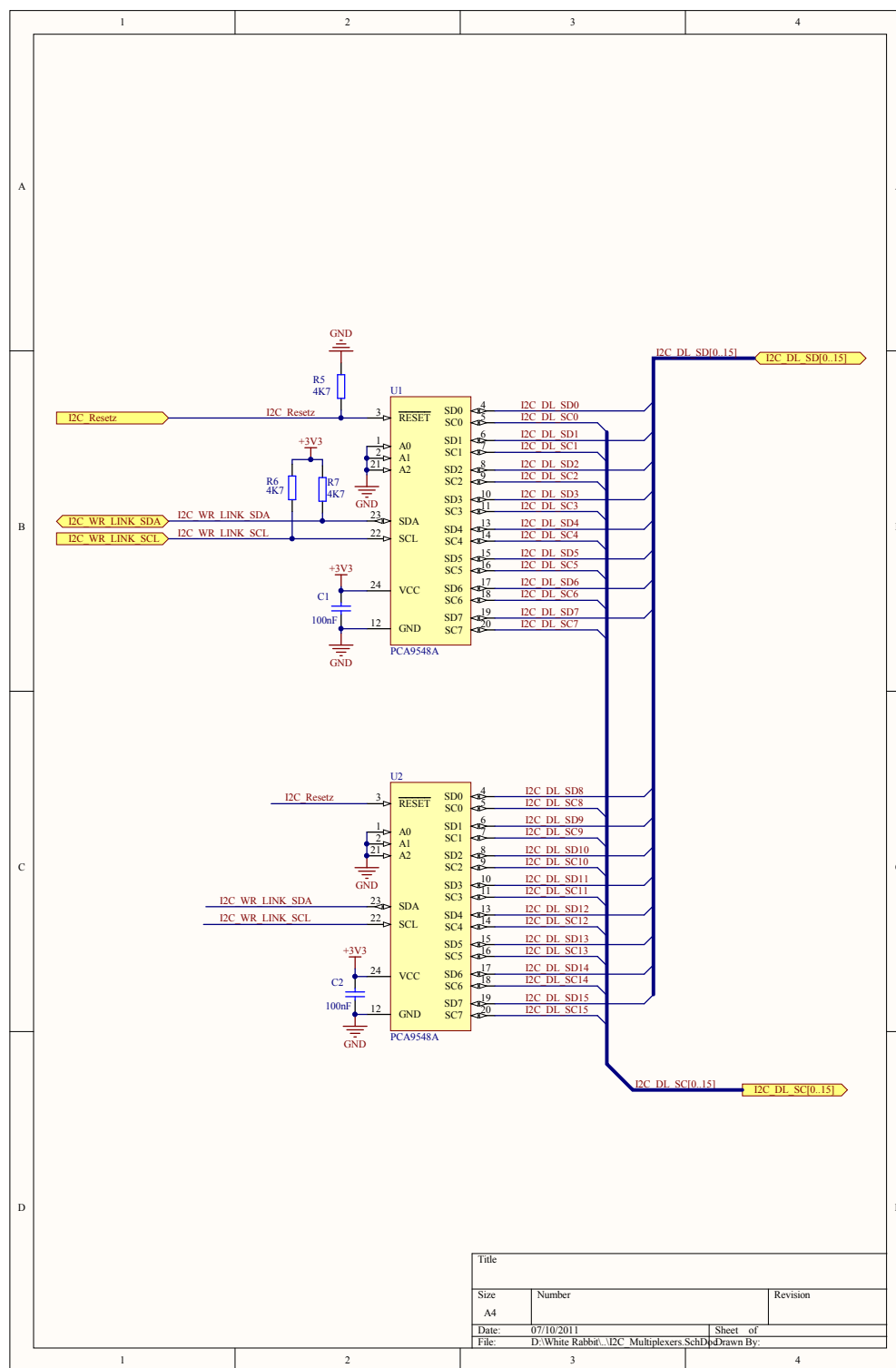
Level: 2.5 V LVCMOS

ARM 2V5 GPIO PE[0..15]

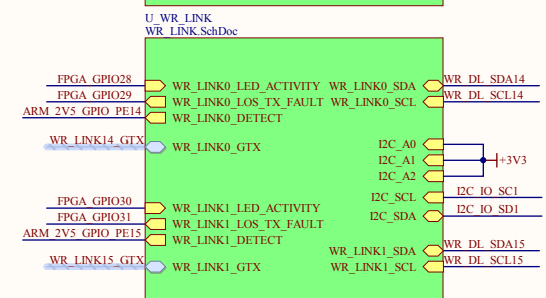
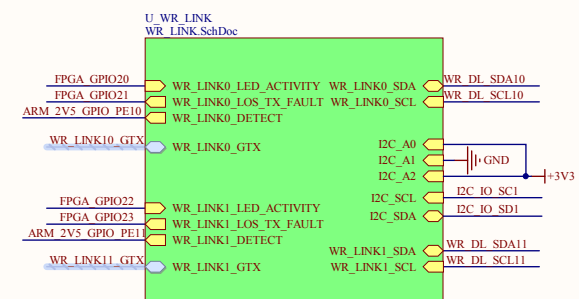
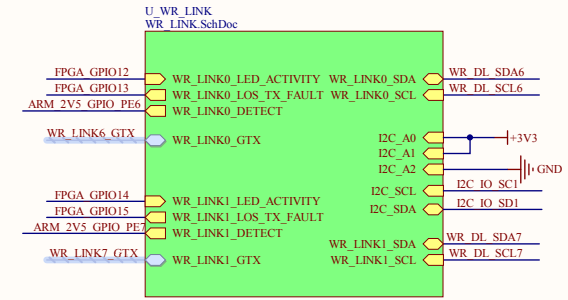
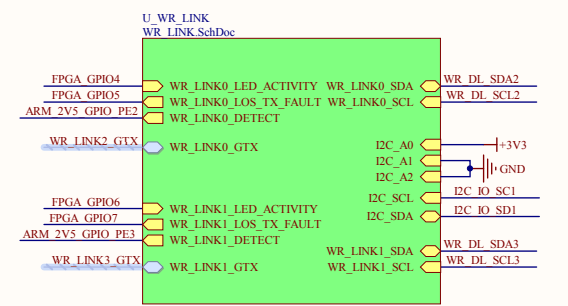
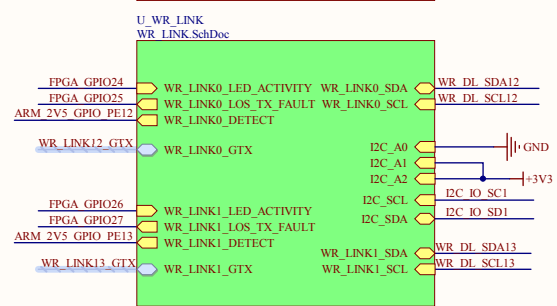
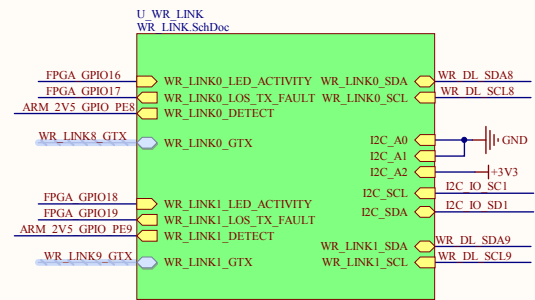
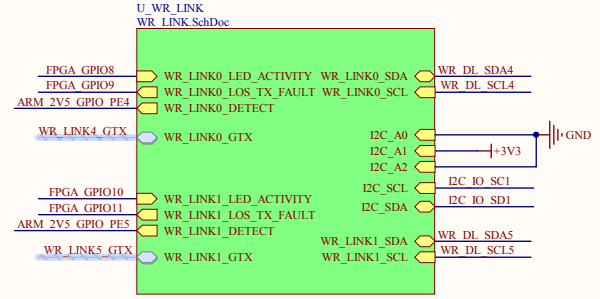
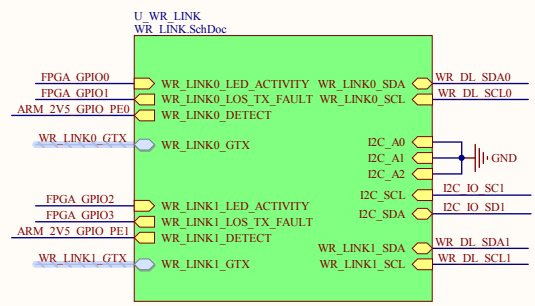
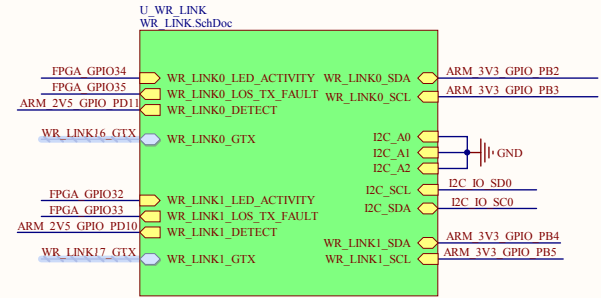
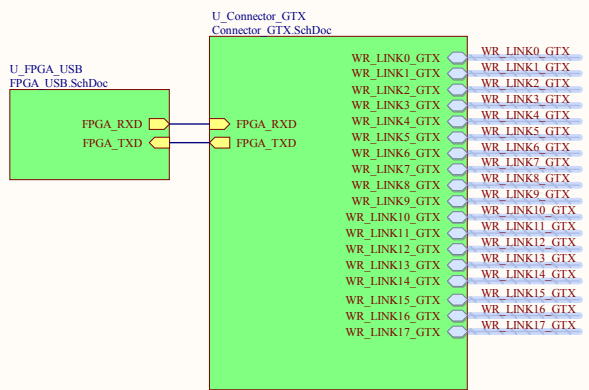
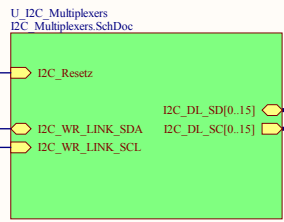
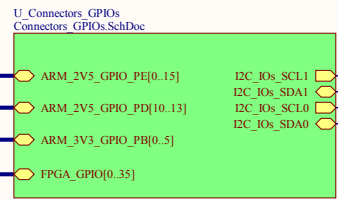
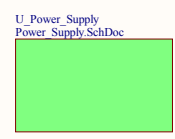


Title		
Size	Number	Revision
A3		
Date:	07/10/2011	Sheet of
File:	D:\White Rabbit\Connectors GPIOs Sch	Drawn By:

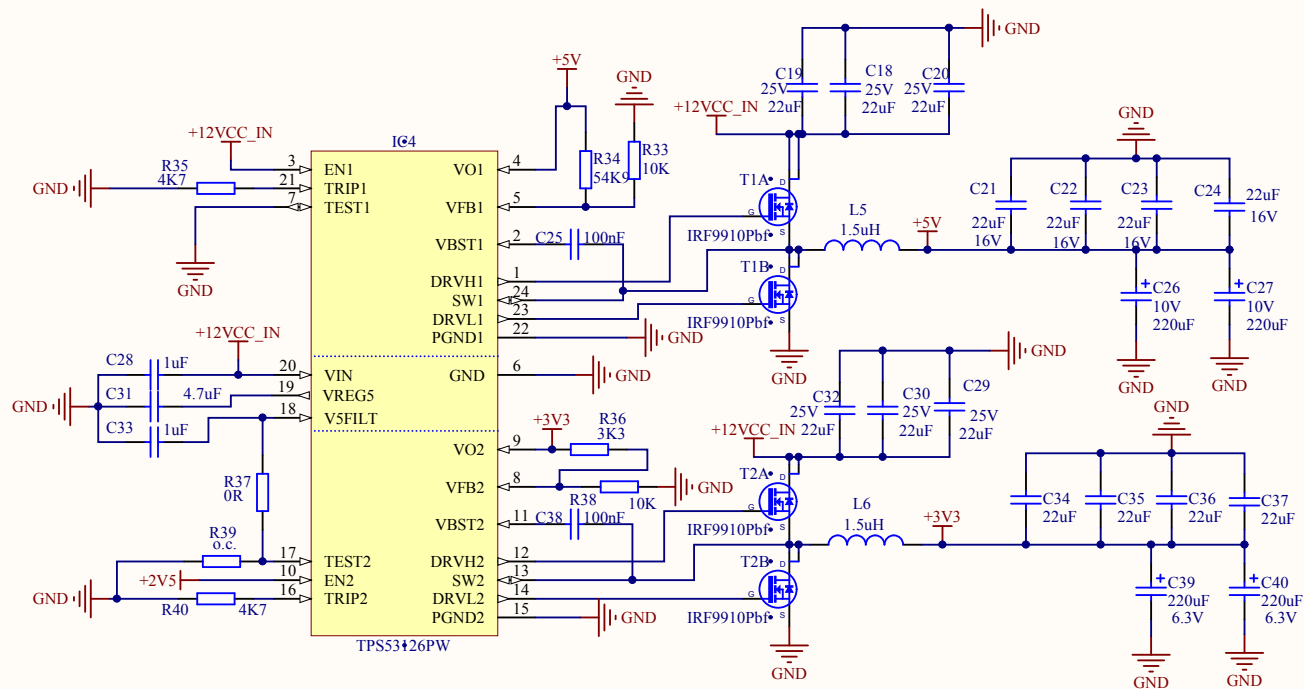




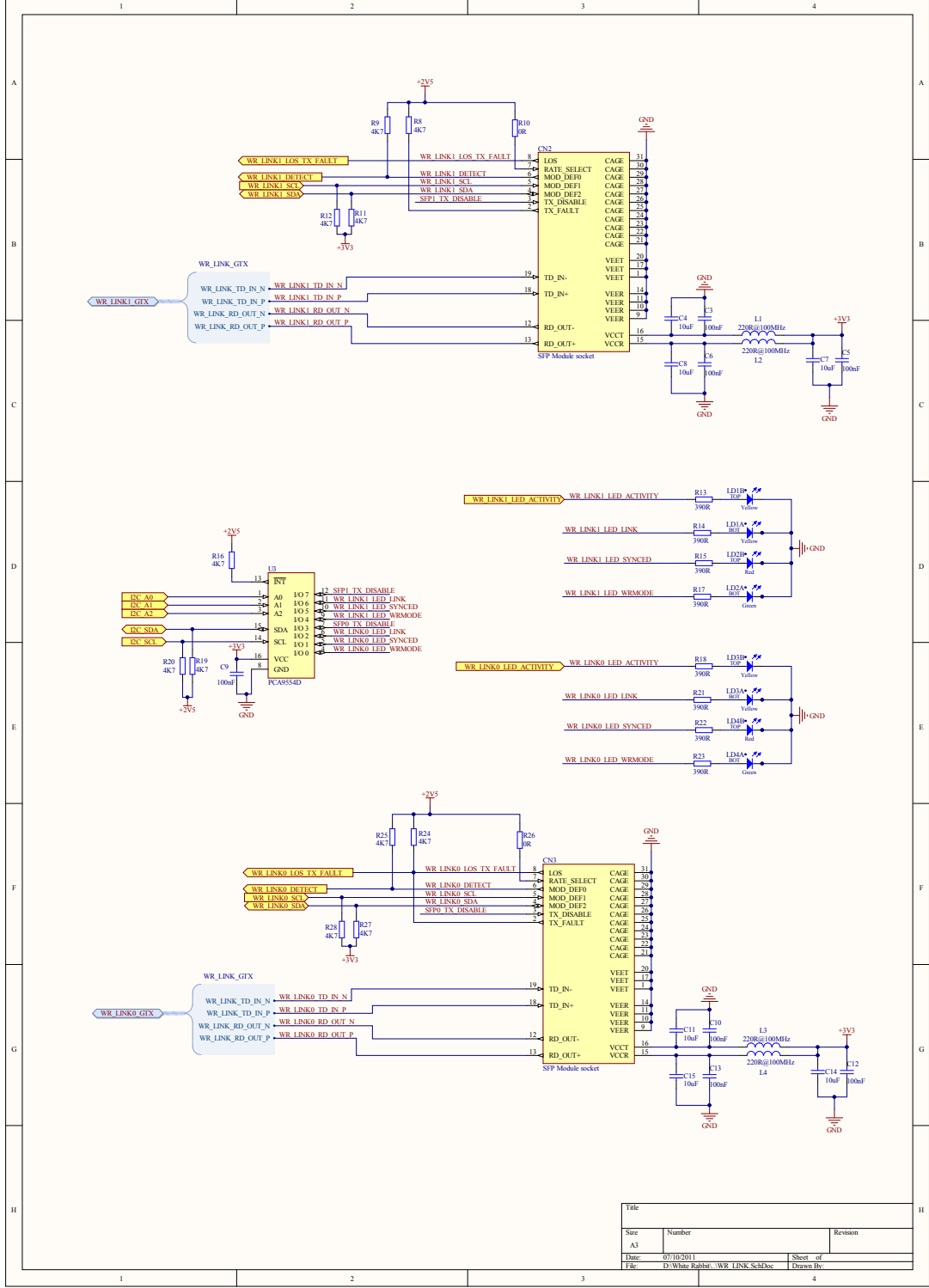
Title		
Size	Number	Revision
A4		
Date:	07/10/2011	Sheet of
File:	D:\White Rabbit\I2C Multiplexers SchD	Drawn By:



Title		
Size A3	Number	Revision
Date: 07/10/2011	Sheet of	
File: D:\White Rabbit\Main.SchDoc	Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	07/10/2011	Sheet of
File:	D:\White Rabbit\Power Supply.SchDoc	Drawn By:



Title		
Size	Number	Revision
A3		
Date:	07/10/2011	Sheet of
File:	D:\White Rabbit\WR LINK\SchDoc	Drawn By: