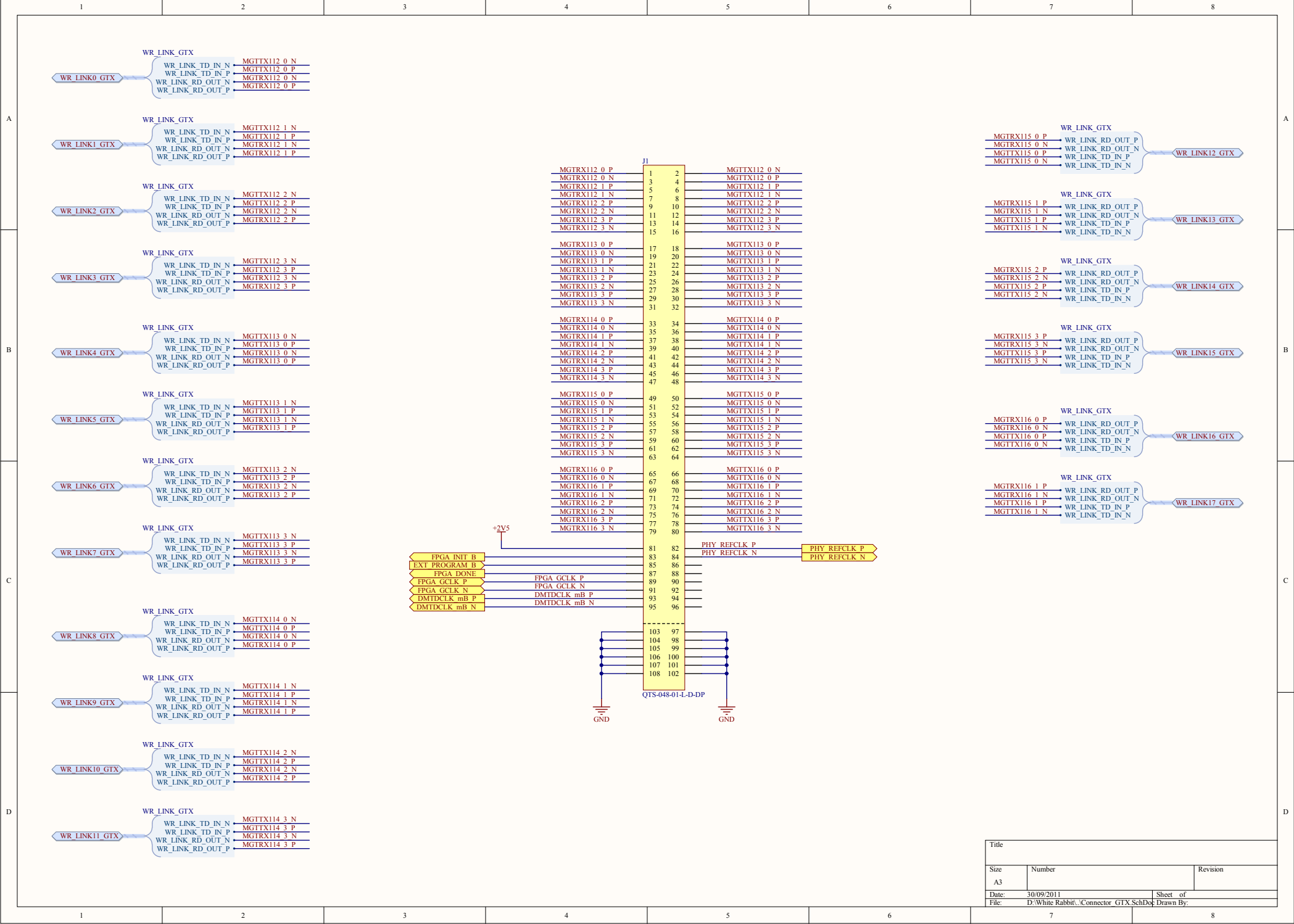
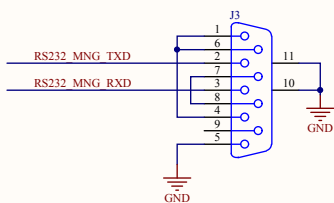
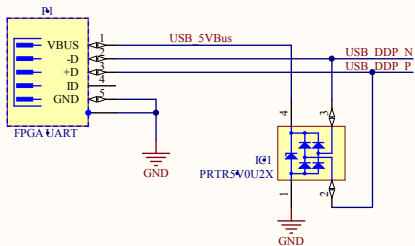


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Level: 3.3 V LVCMOS

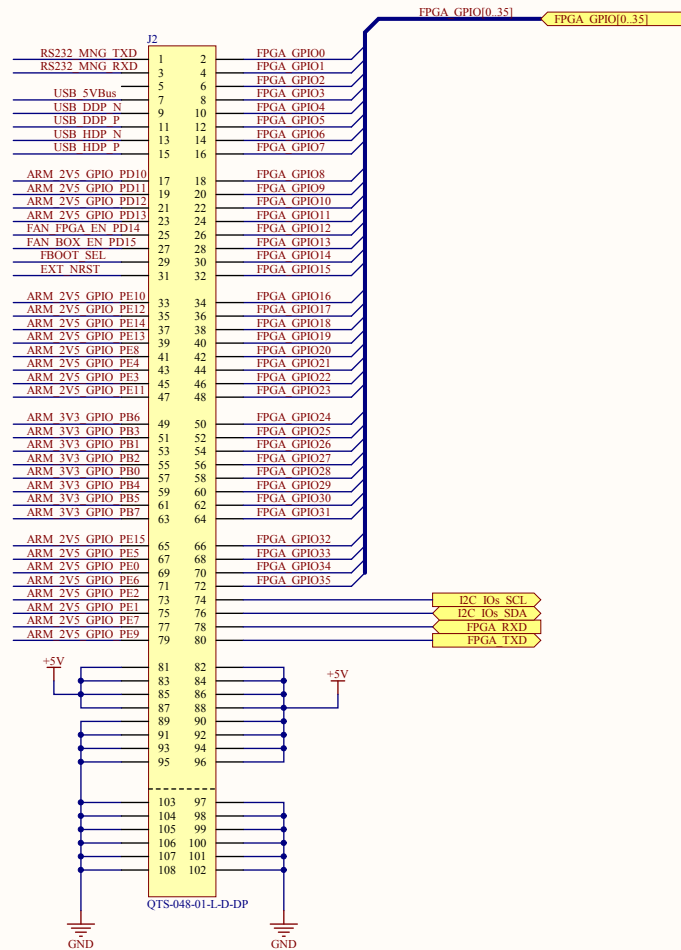
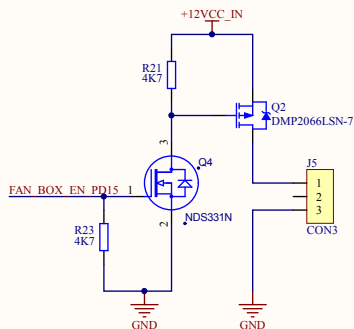
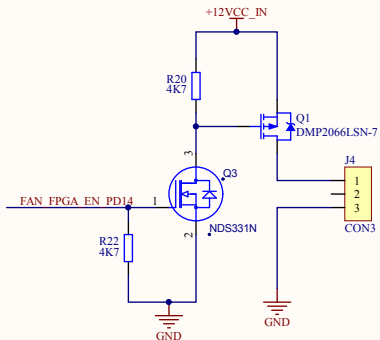
ARM 3V3 GPIO PB[0..7] ARM 3V3 GPIO PB[0..7]

Level: 2.5 V LVCMOS

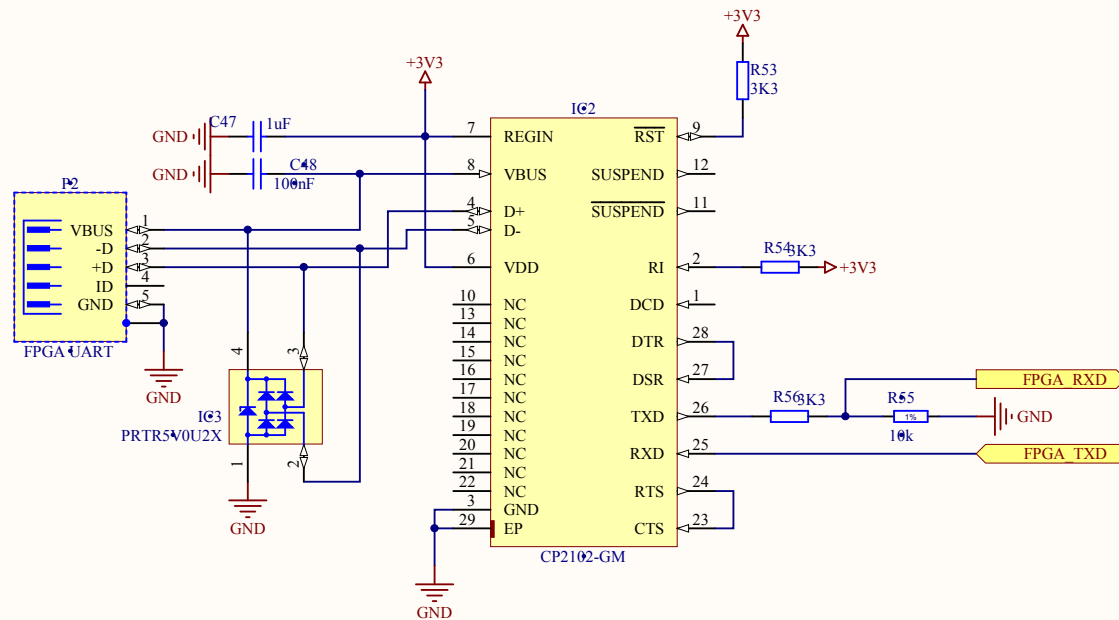
ARM 2V5 GPIO PD[10..13] ARM 2V5 GPIO PD[10..13]

Level: 2.5 V LVCMOS

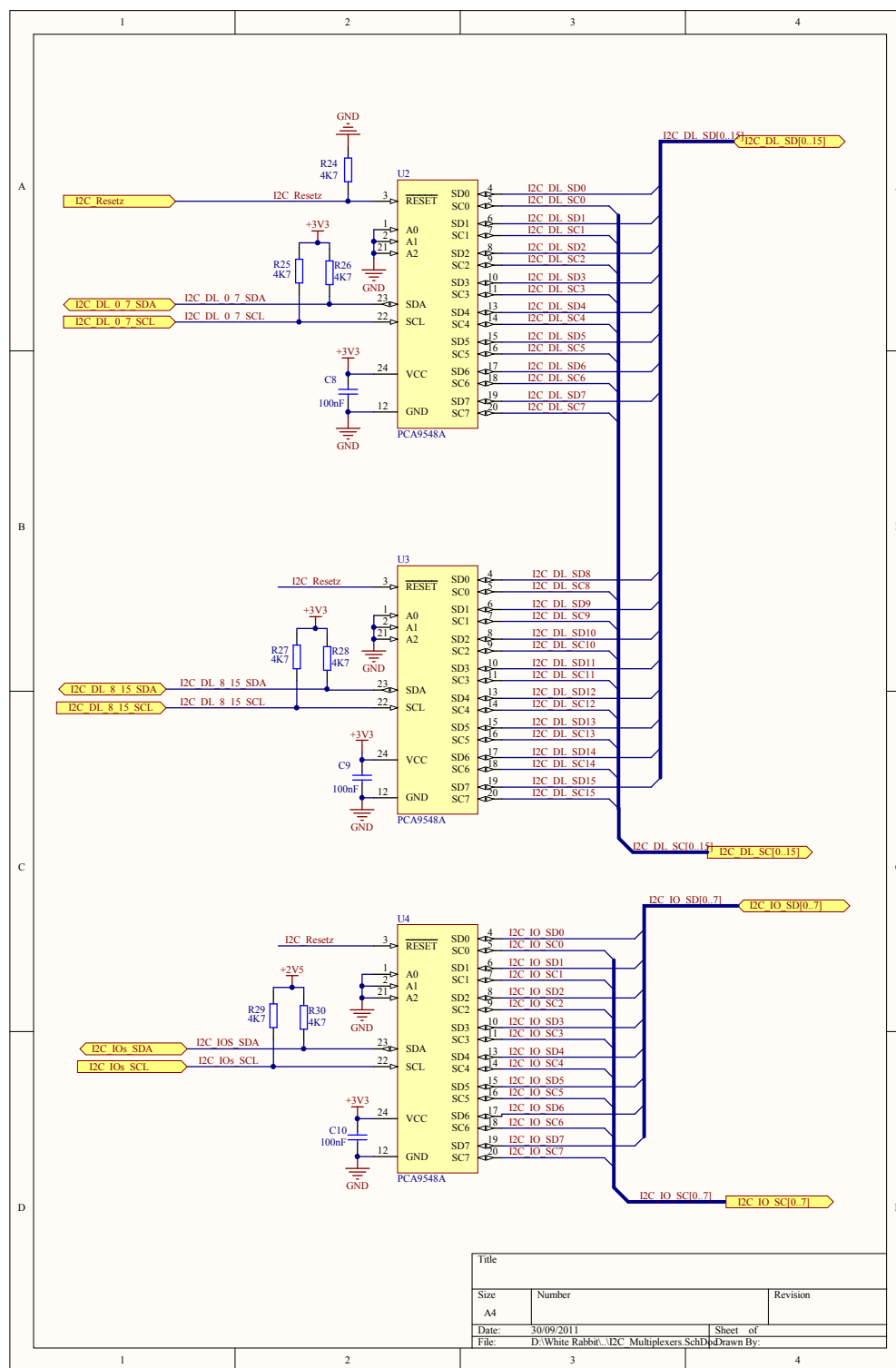
ARM 2V5 GPIO PE[0..15] ARM 2V5 GPIO PE[0..15]



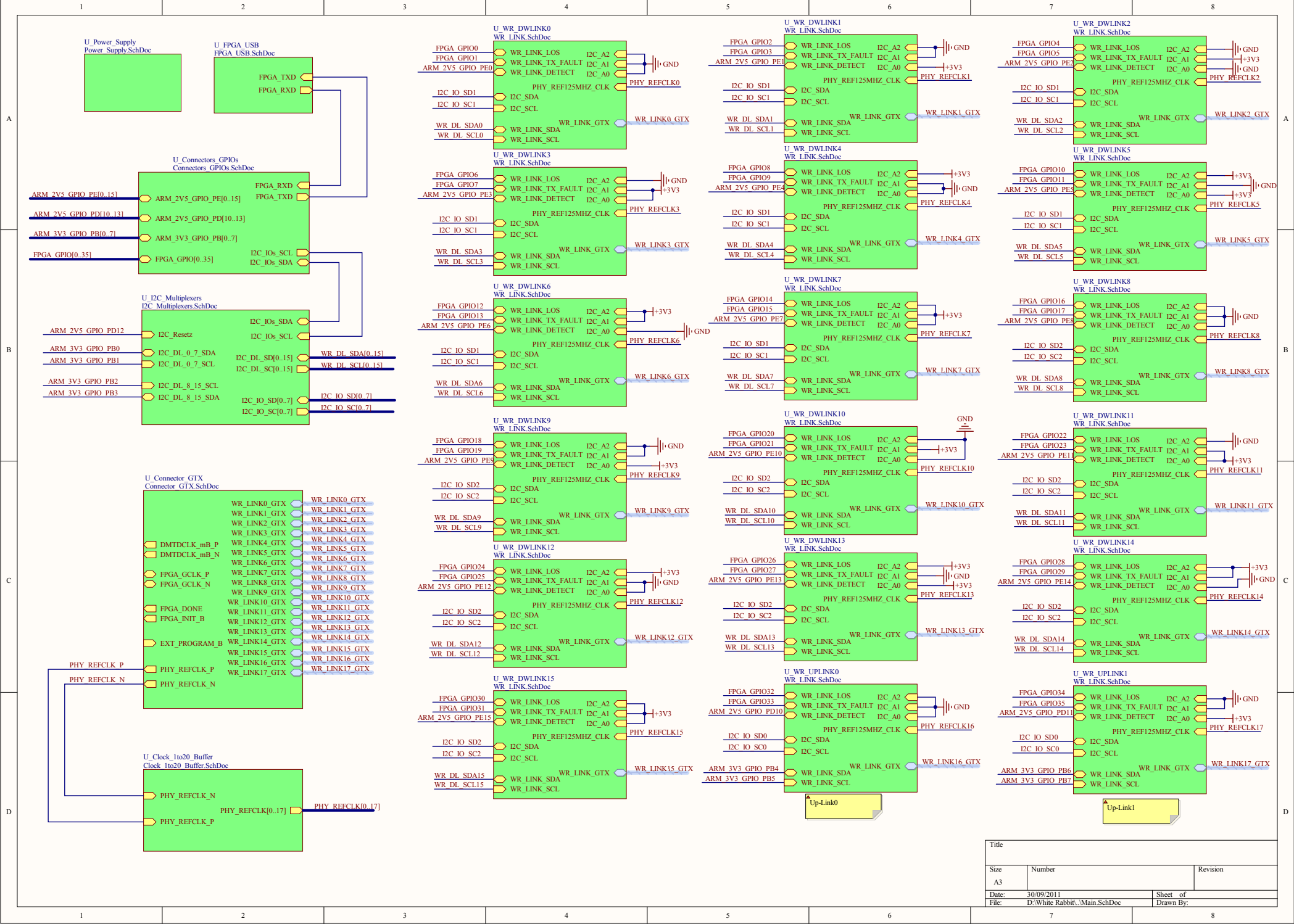
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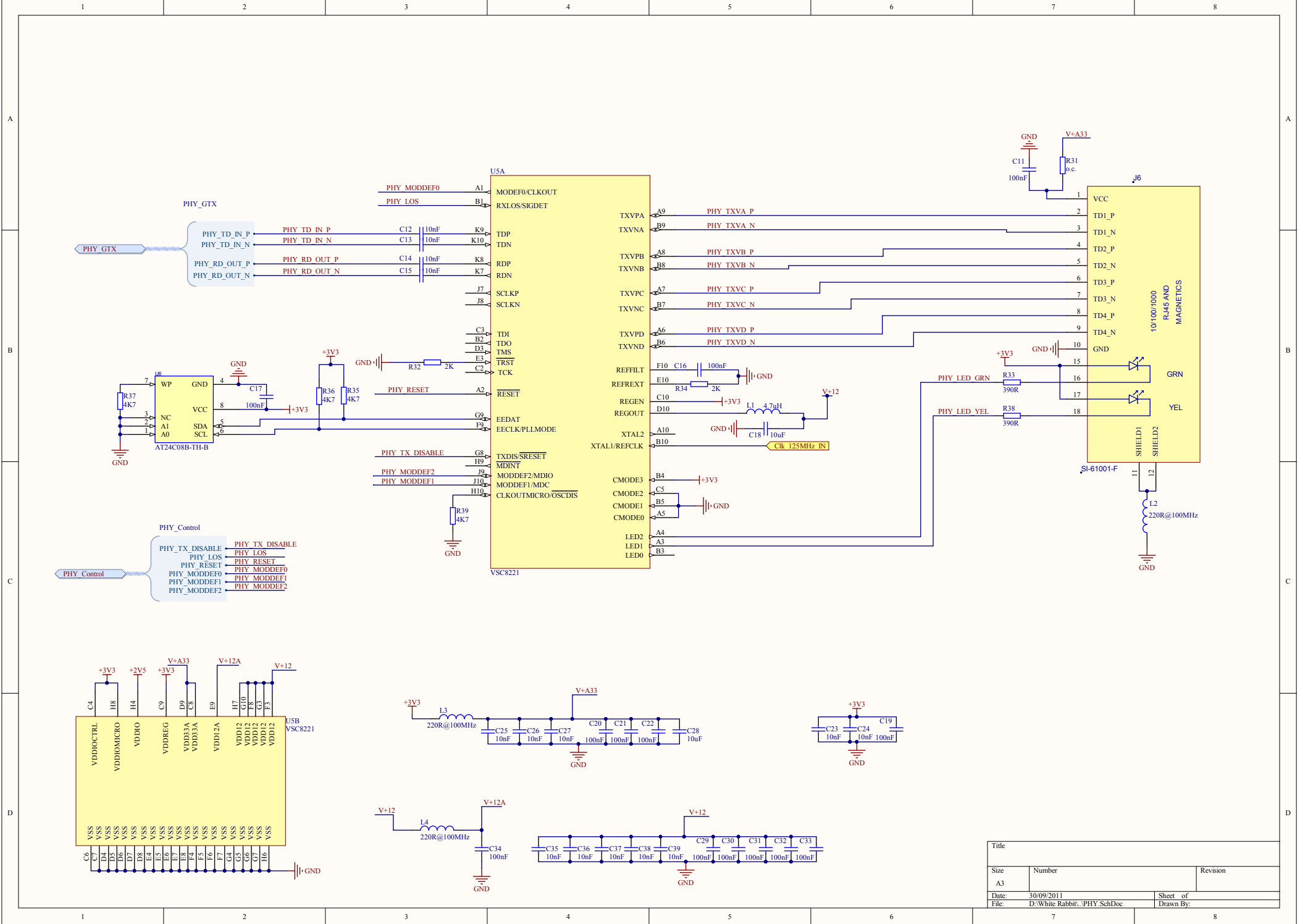


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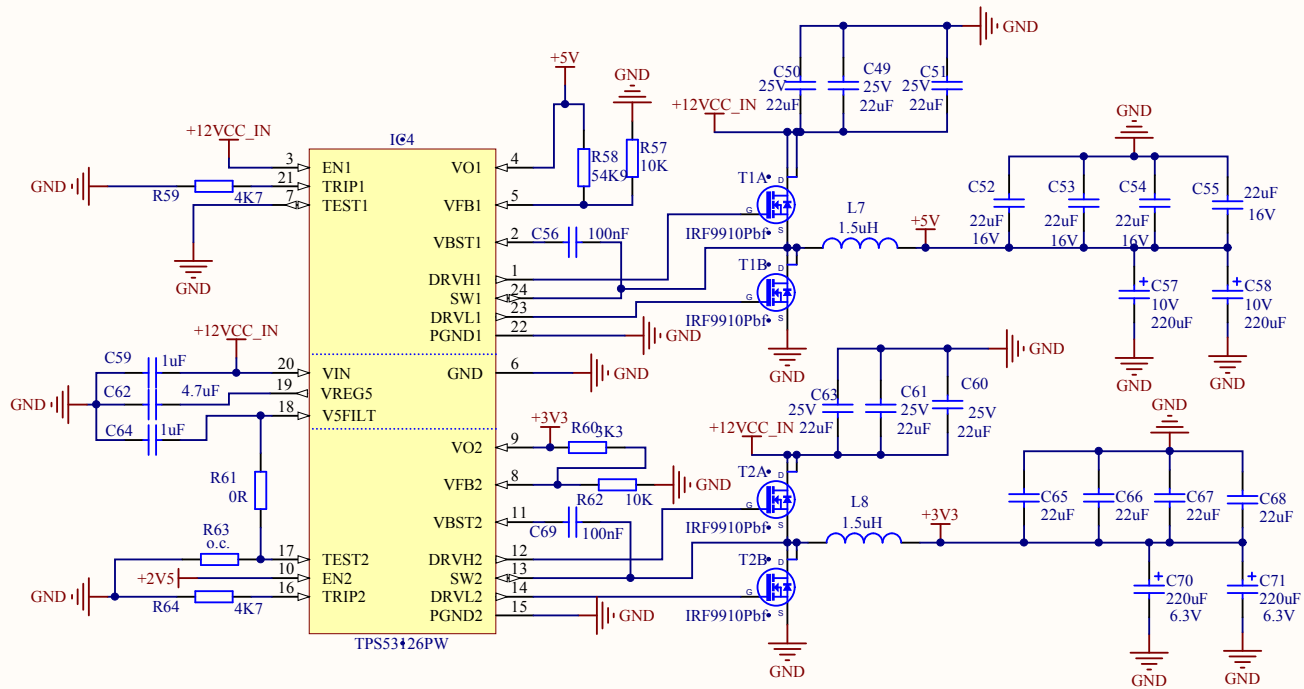


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Size	Number	Revision
A4		
Date:	30/09/2011	Sheet of
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SFP\_Control

SFP\_Control

SFP\_TX\_DISABLE  
SFP\_LOS  
SFP\_DETECT  
SFP\_SCL  
SFP\_SDA  
SFP\_TX\_FAULT

SFP\_TX\_DISABLE  
SFP\_LOS  
SFP\_DETECT  
SFP\_SCL  
SFP\_SDA  
SFP\_TX\_FAULT

+2V5

R40  
0R

SFP\_LOS  
SFP\_DETECT  
SFP\_SCL  
SFP\_SDA  
SFP\_TX\_DISABLE  
SFP\_TX\_FAULT

LOS  
RATE\_SELECT  
MOD\_DEF0  
MOD\_DEF1  
MOD\_DEF2  
TX\_DISABLE  
TX\_FAULT

GND

SFP\_TD\_IN\_N

19

TD\_IN-

SFP\_TD\_IN\_P

18

TD\_IN+

SFP\_RD\_OUT\_N

12

RD\_OUT-

SFP\_RD\_OUT\_P

13

RD\_OUT+

SFP Module socket

SFP\_GTX

SFP\_GTX

SFP\_TD\_IN\_N  
SFP\_TD\_IN\_P  
SFP\_RD\_OUT\_N  
SFP\_RD\_OUT\_P

SFP\_TD\_IN\_N  
SFP\_TD\_IN\_P  
SFP\_RD\_OUT\_N  
SFP\_RD\_OUT\_P

Title

Size

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Number

Revision

Date: 30/09/2011

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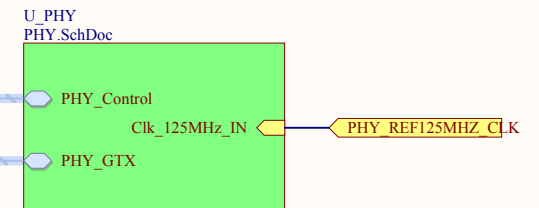
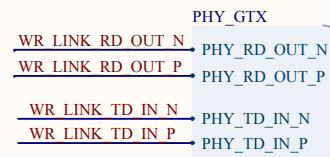
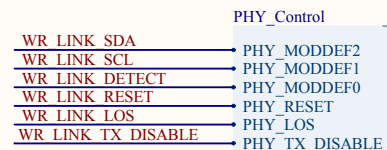
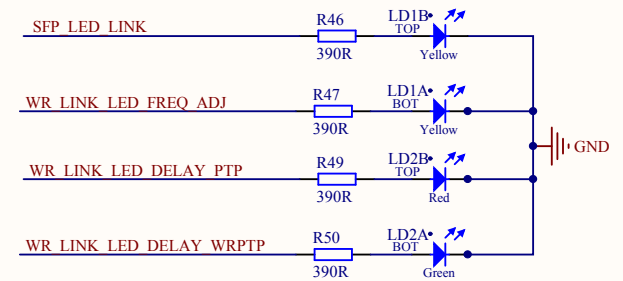
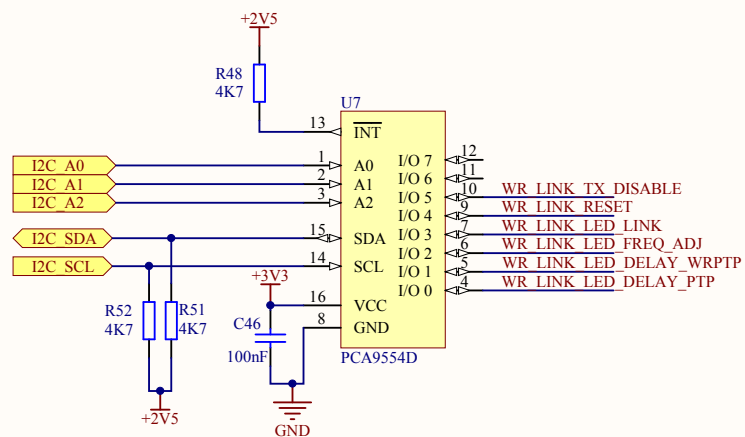
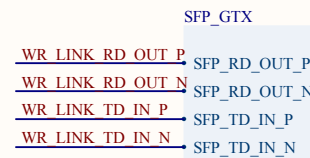
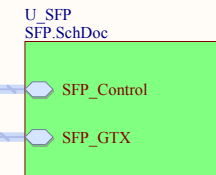
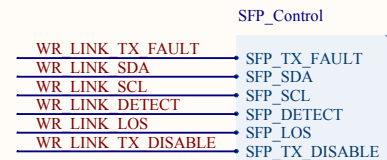
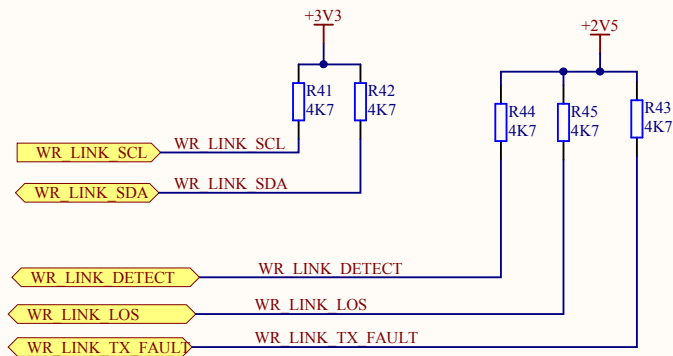
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