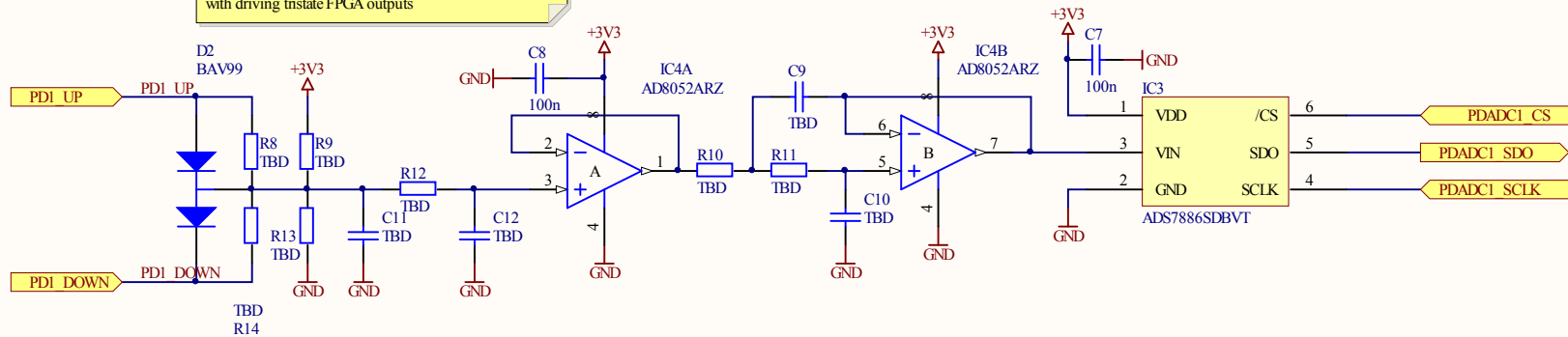
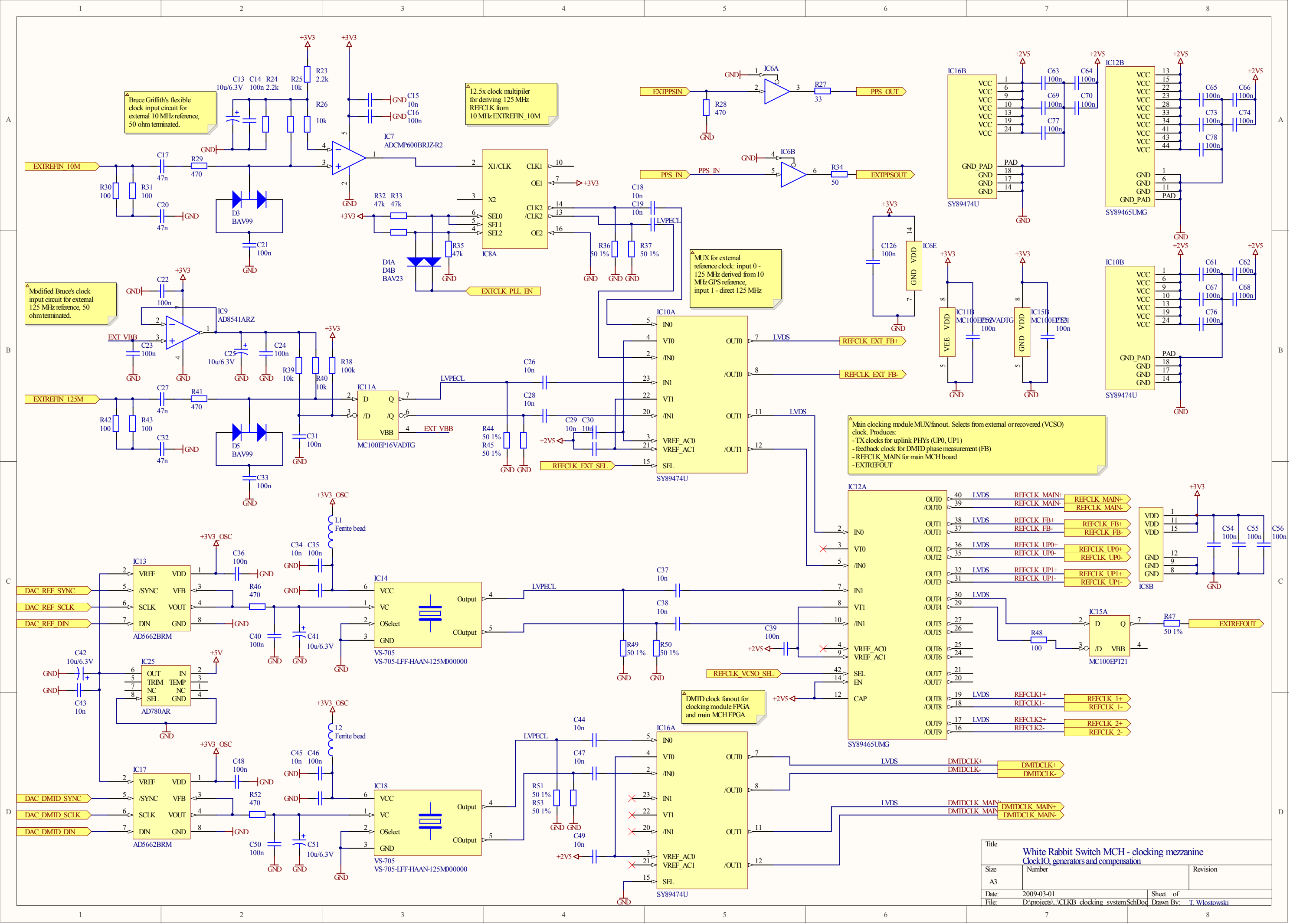


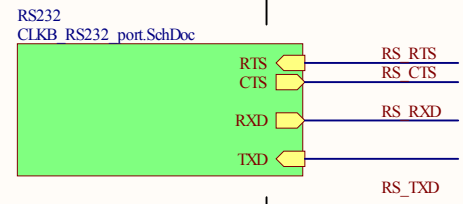
Optional external charge pump, filter and ADC for PLL. Diodes are optional in case we have problems with driving tristate FPGA outputs



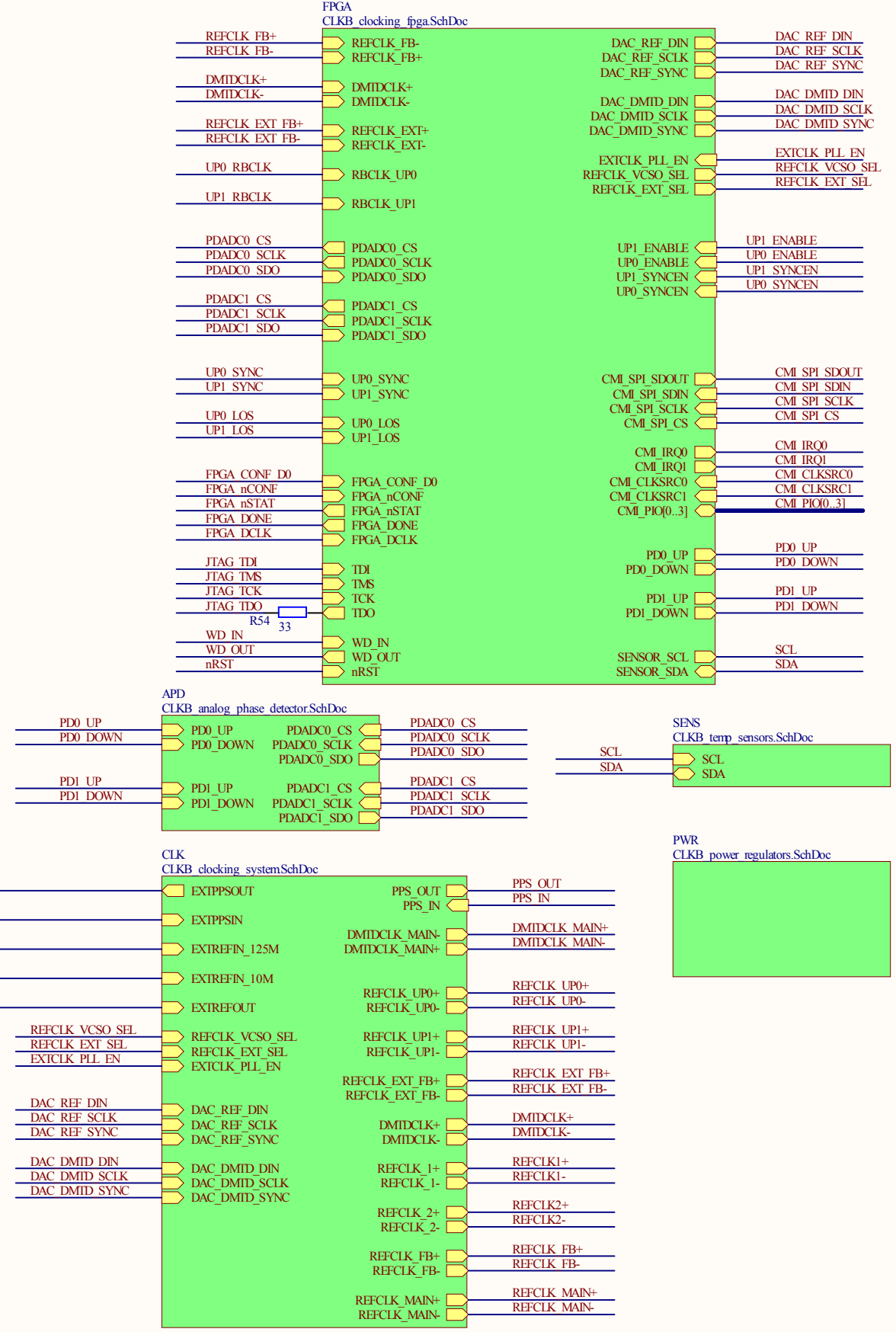
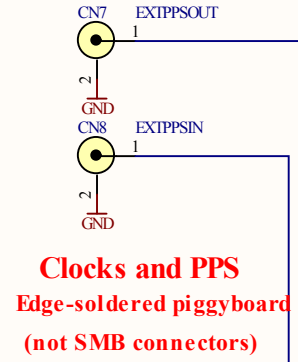
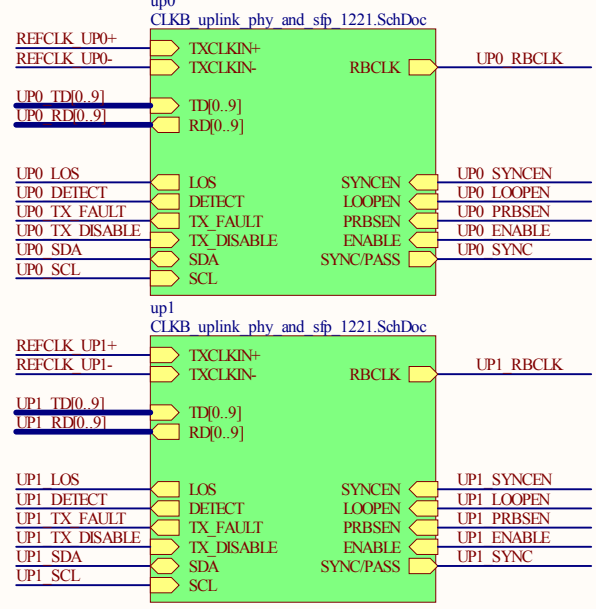
Title		
White Rabbit Switch MCH - clocking mezzanine Analog phase detector & ADC		
Size	Number	Revision
A		0.0.5
Date:	2009-03-01	Sheet of
File:	D:\projects\CLKB analog_phase_detector.SchDoc	Drawn By:



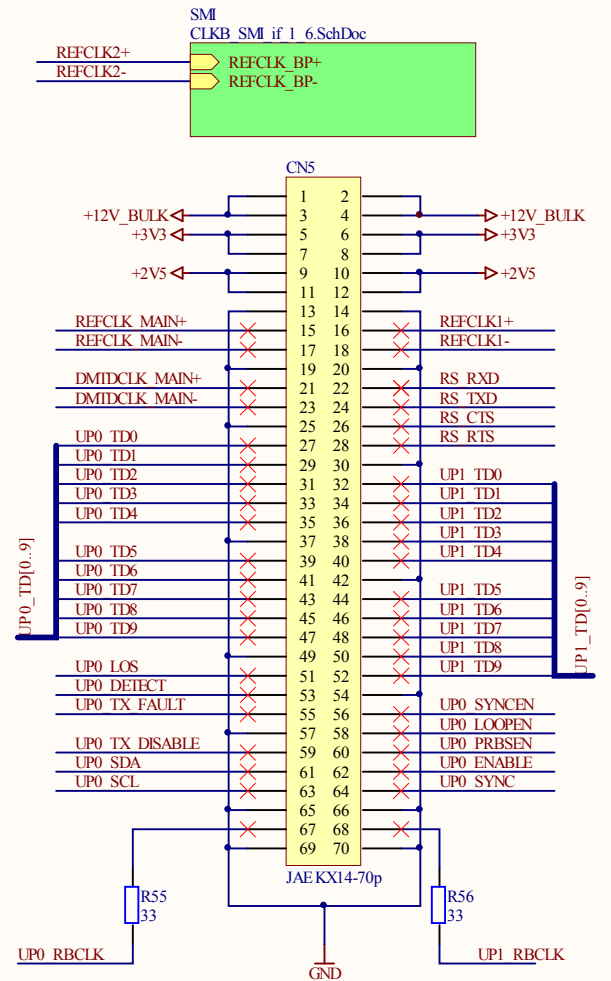
Front panel
RS232 port



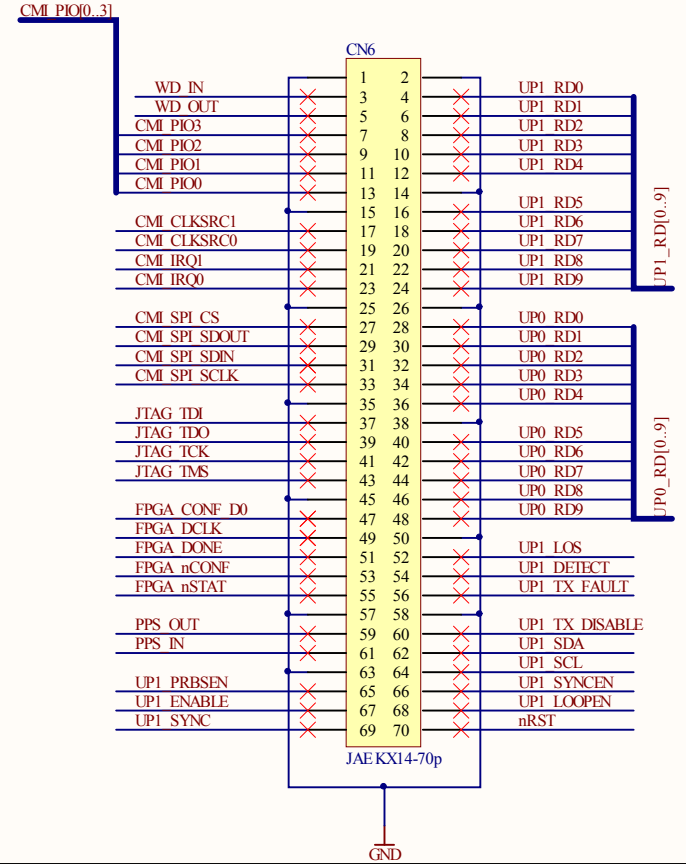
Uplink ports (PHY + SFP)



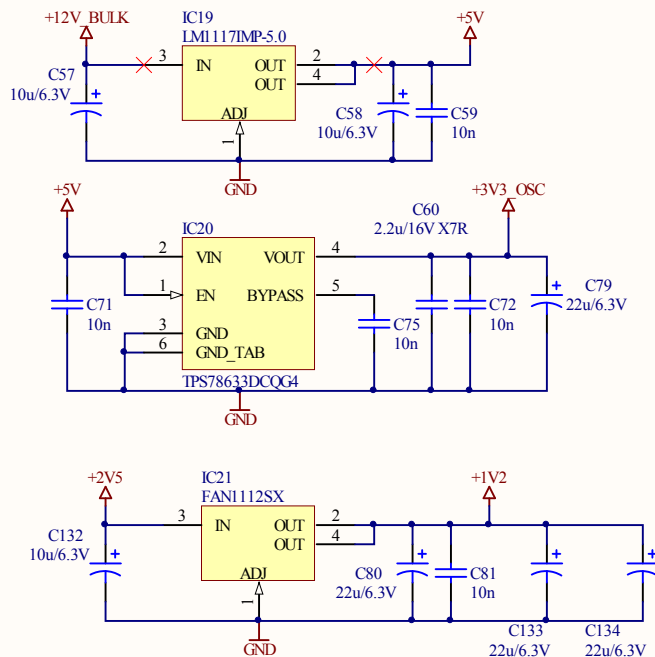
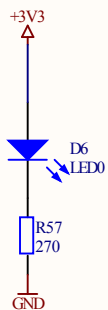
SMI backplane I/F



Mainboard interface



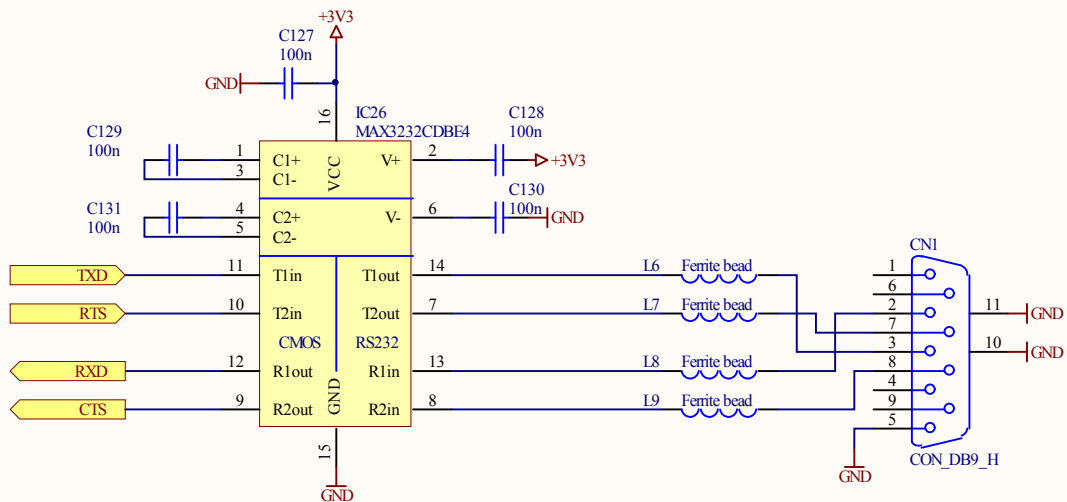
Title White Rabbit Switch MCH - clocking mezzanine System diagram & mainboard connection			
Size A3	Number	Revision	
Date:	2009-03-01	Sheet of	
File:	D:\projects\...\CLKB_main.SchDoc	Drawn By:	



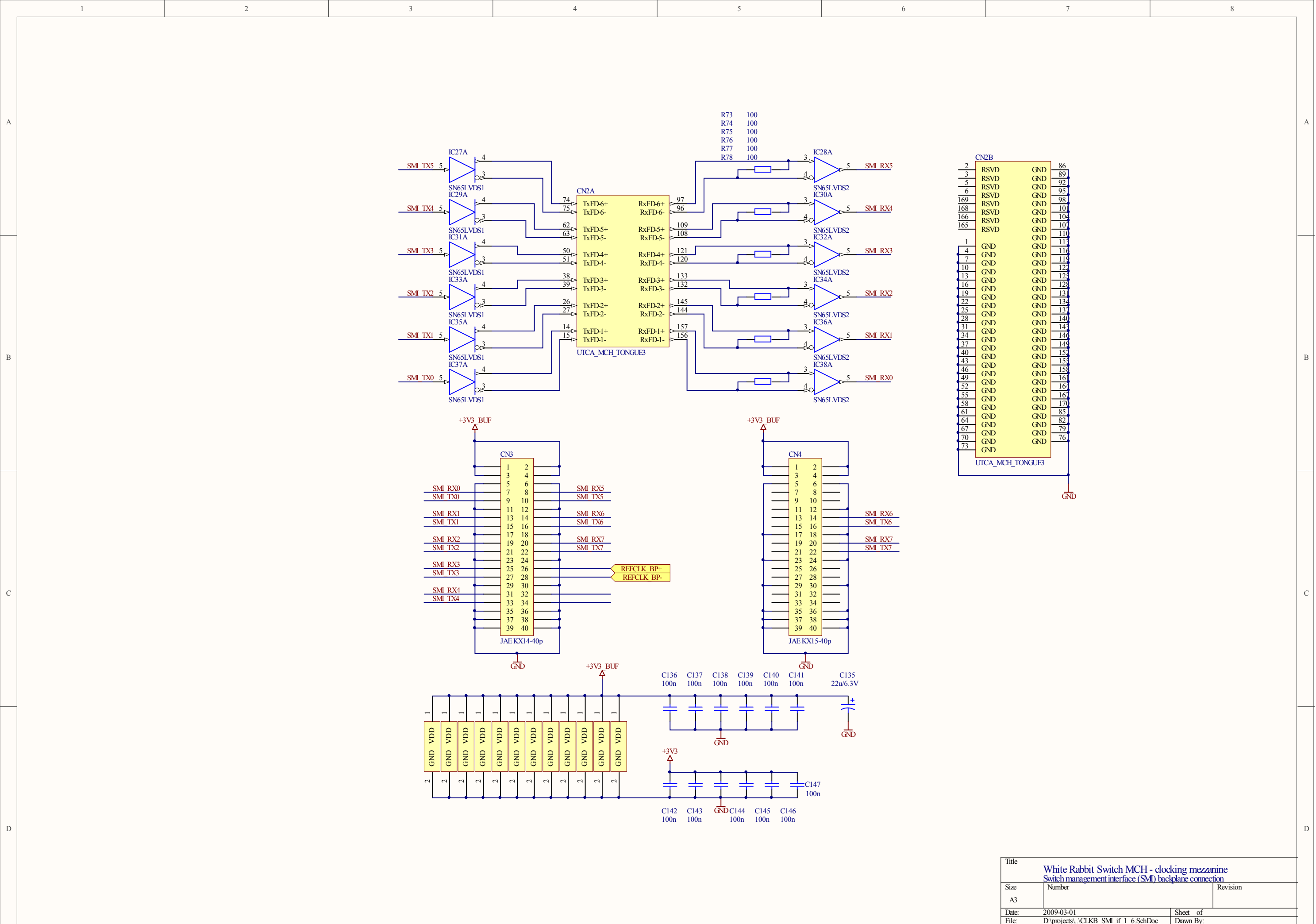
Input voltages (from main MCH board):
- +12VBULK backplane bulk power (+12 V)
- +2V5 from main regulator (+2.5 V, several amps)
- +3V3 from main regulator (+3.3 V, several amps)

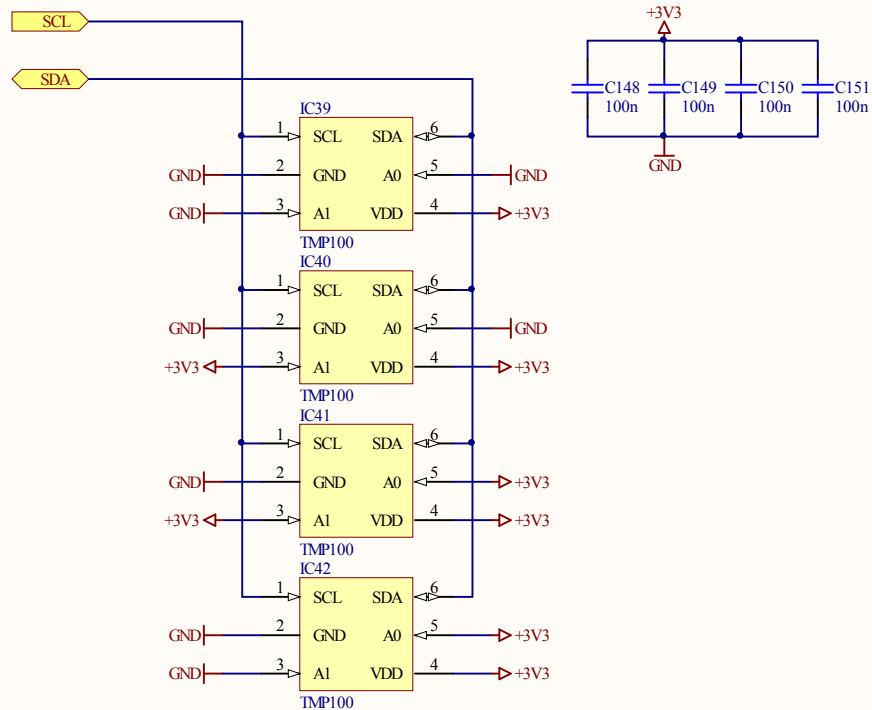
Output voltages:
- +3V3OSC - +3.3 V @200 mA ultralow-noise supply for VCOSs and DACs
- +5V - +5 V for DAC reference and +3V3OSC LDO supply
- +1V2 - +1.2V @ 0.5 A for FPGA Vcore

Title		
White Rabbit Switch MCH - clocking mezzanine Power supply regulators		
Size	Number	Revision
A4		
Date:	2009-03-01	Sheet of
File:	D:\projects\...\CLKB_power_regulators.SchDoc	Drawn By:



Title		
White Rabbit Switch MCH - clocking mezzanine RS232 port		
Size	Number	Revision
A		
Date:	2009-03-01	Sheet of
File:	D:\projects\CLKB_RS232_port.SchDoc	Dawn By:





Title		
White Rabbit Switch MCH - clocking mezzanine Temperature sensors		
Size	Number	Revision
A		
Date:	2009-03-01	Sheet of
File:	D:\projects\...\CLKB_temp_sensors.SchDoc	Dawn By:

