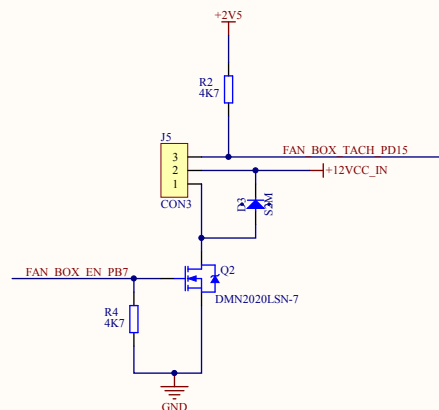
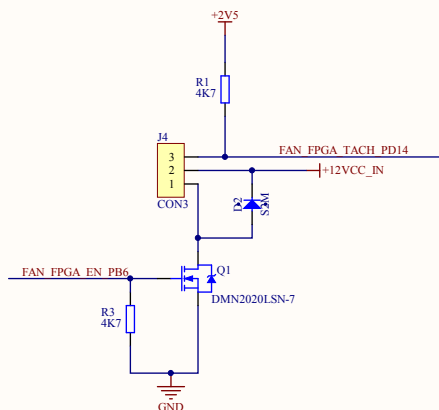
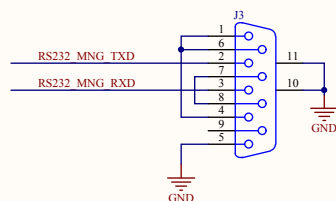


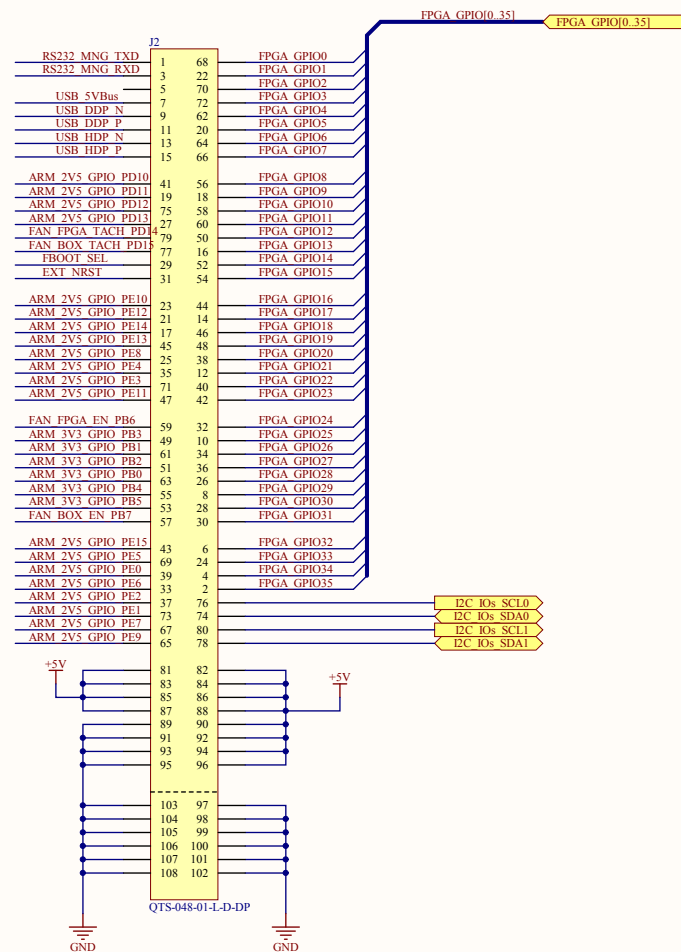
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ARM 3V3 GPIO PB[0..5] ARM 3V3 GPIO PB[0..5]

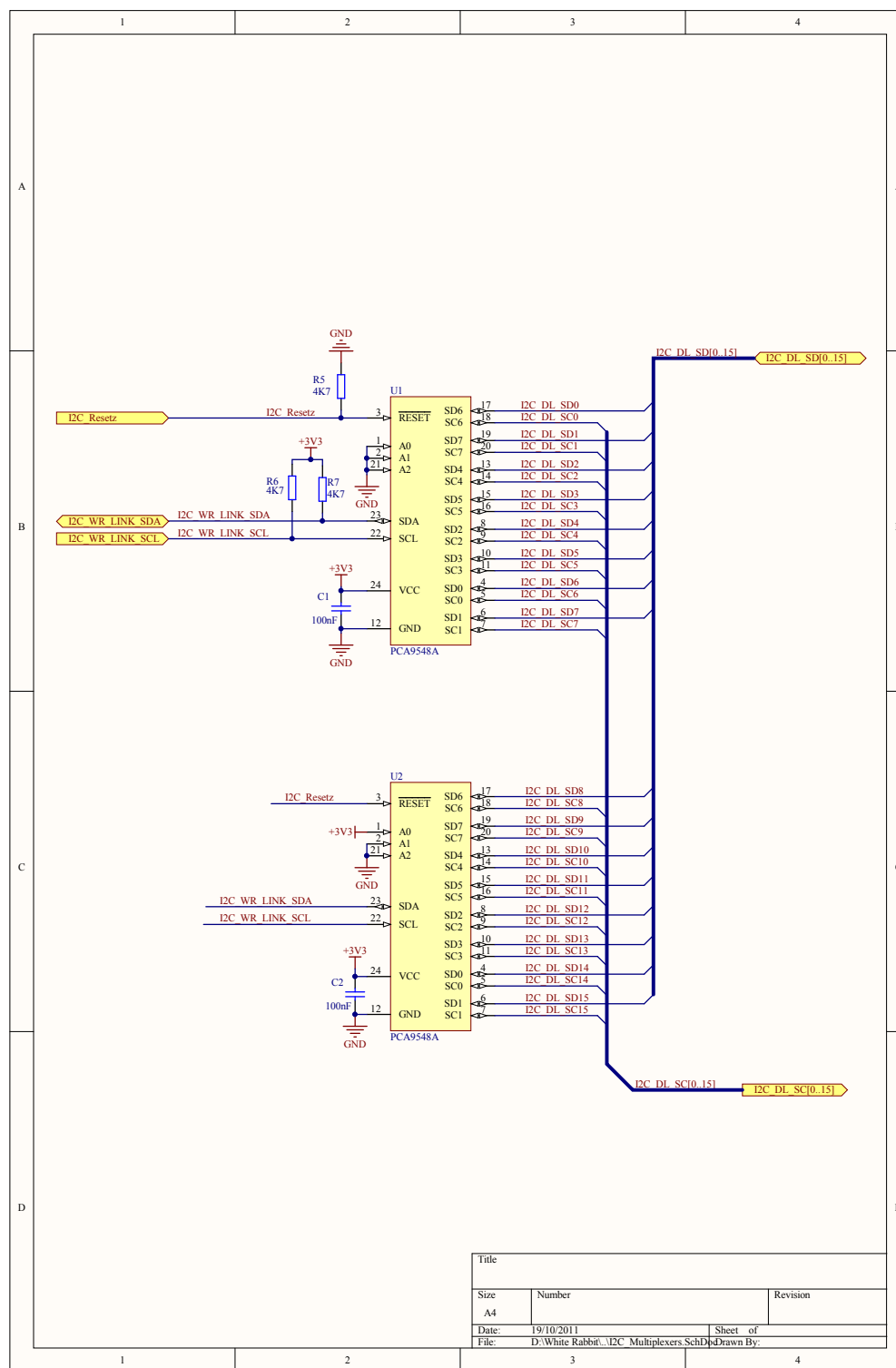
ARM 2V5 GPIO PD[10..13]

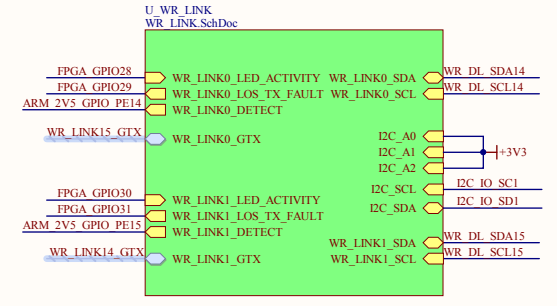
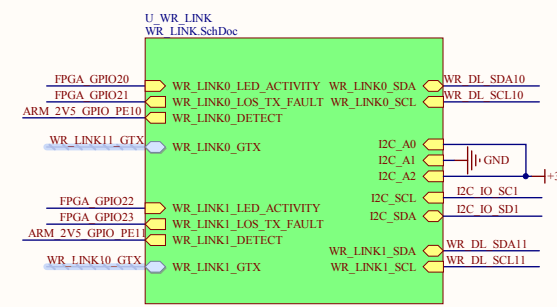
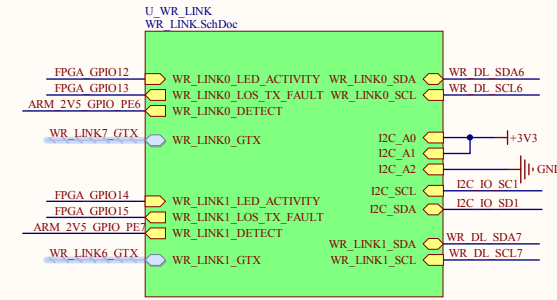
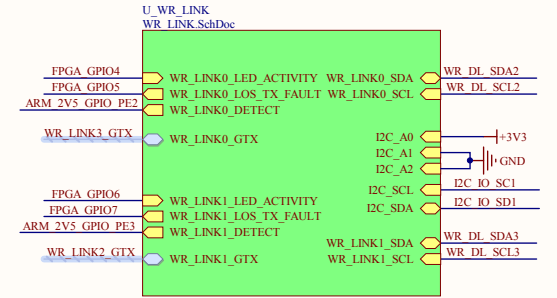
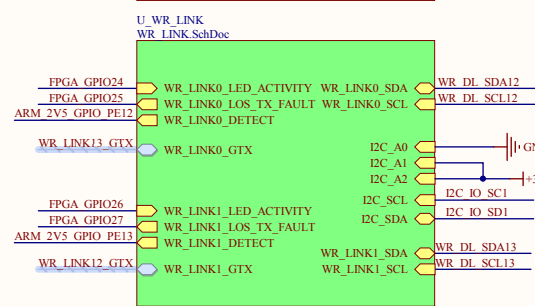
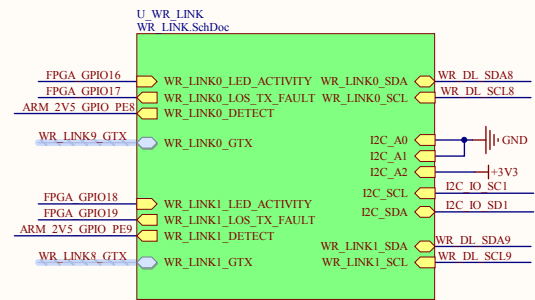
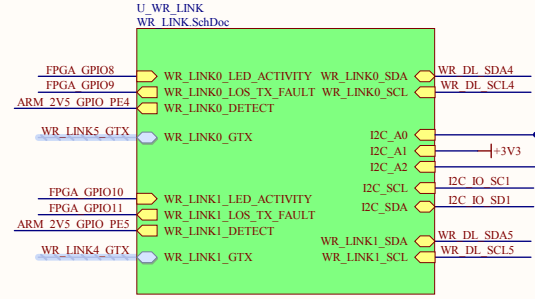
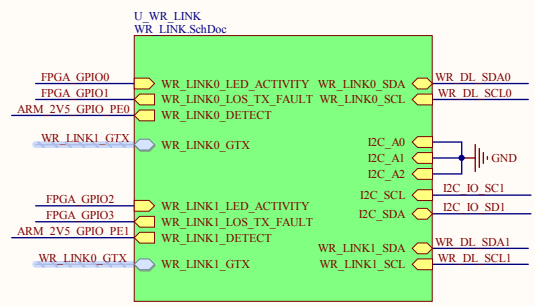
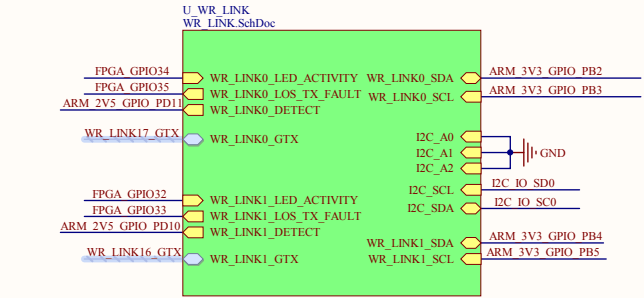
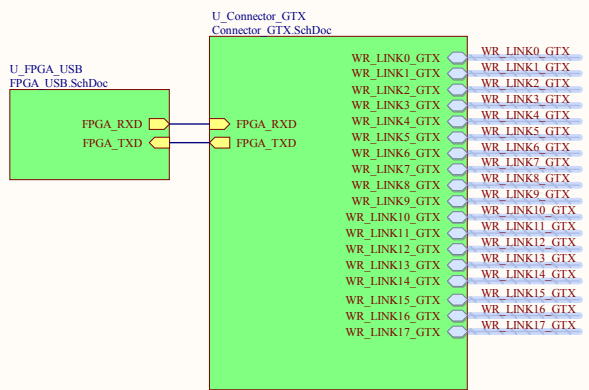
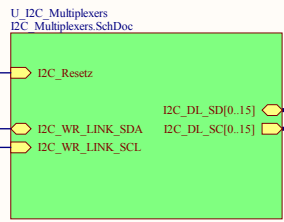
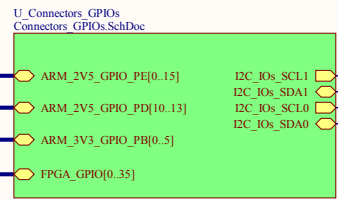
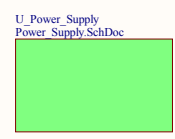
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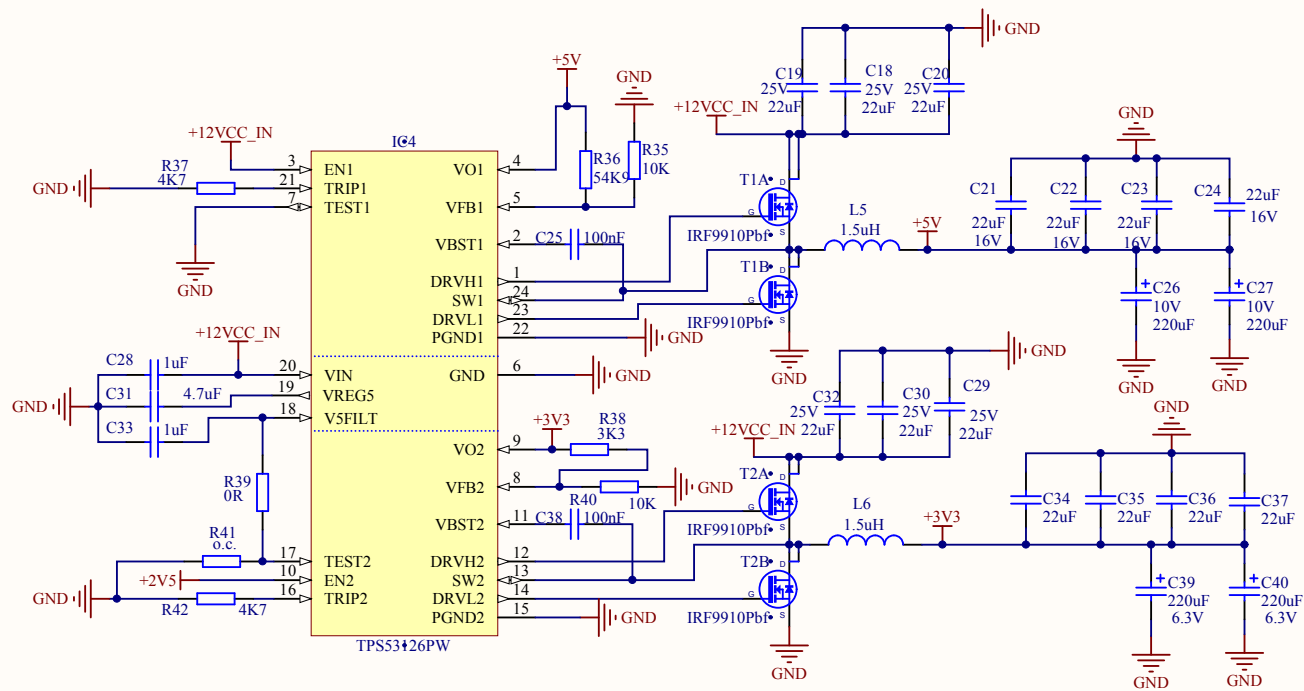
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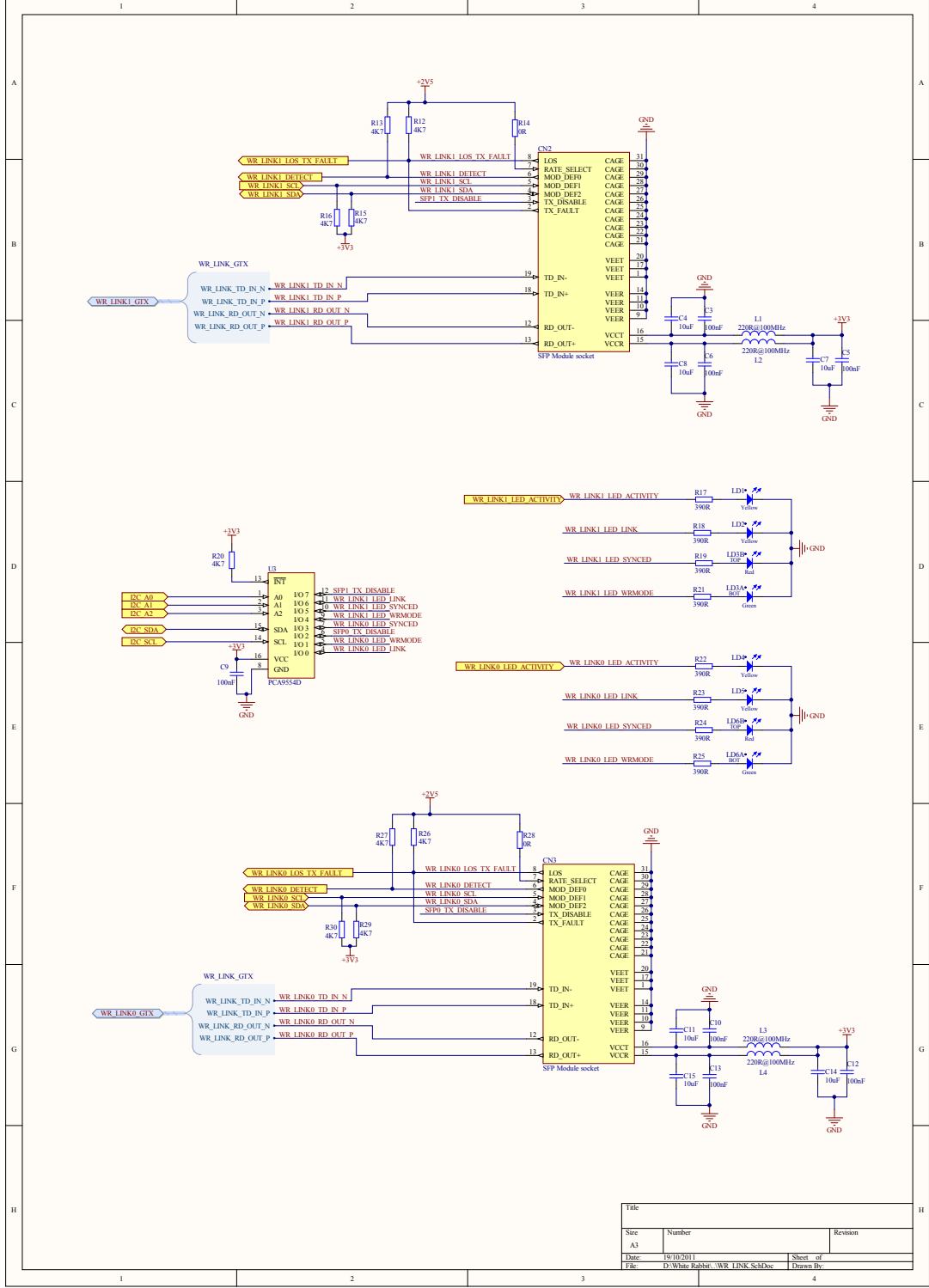




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