## miniBackplane Manufacturing PCB Specification

## 1 PCB General data.

• **Dimension:** 439,06 mm x 174,40mm

• **Thickness:** 1,49 mm  $\pm$  10%

• Number of layers: 4

2 routing layers ("Top Layer" and "Bottom Layer")

o 2 plane layers ("GND" and "PWR")

• Components total: 480 (top: 156, bottom: 324)

• **SMD components:** Yes, both sides

• Silkscreen: Yes, both sides

Nets: 434Vias: 1182

Hole Size: 0,3 mmVia Diameter: 0,7 mm

• Pads: 2253

• Plating Finish: Nickel-Gold

• Controlled Impedance: Yes, on "Top Layer" and "Bottom Layer"

## 2 Stack-Up Diagram

	Thickness Total:	1,49 mm ± 10%
Bottom Layer		17 + 25 μm
PrePreg		178 μm
PWR		35 μm
Core		980 µm
GND		35 μm
PrePreg		178 μm
Top Layer		17 + 25 μm

## 3 Layer Stack-Up Detail

Layer Name	Gerber Document	Copper Thickness	Dielectric Height	Dielectric Material	Dielectric Constant	50Ω Impedance Width	100Ω Differential Impedance Width/Gap
Top Solder Mask	(.GTS)		0,010mm	Solder Resist	3,5		
Top Layer	(.GTL)	0,042mm				0,3mm	0,2mm/0,2mm
PrePreg			0,178mm	FR-4	4,8		
GND	(.GP1)	0,035mm					
Core			0,98mm	FR-4	4,8		
PWR	(.GP2)	0,035mm					
PrePreg			0,178mm	FR-4	4,8		
Bottom Layer	(.GBL)	0,042mm				0,3mm	0,2mm/0,2mm
Bottom Solder Mask	(.GBS)		0,010mm	Solder Resist	3,5		