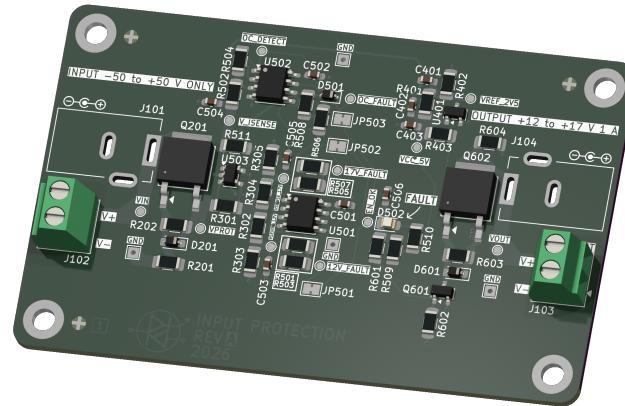
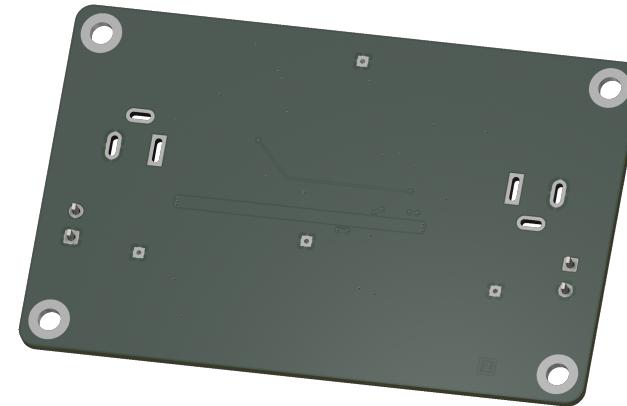


INPUT PROTECTION Assembly DOCUMENT

Top View



Bottom View



PROJECT
ASSIGNMENT 1
DRN
2024-04-13 R. HICKS
CHK

ENG APP

MFR APP

SHEET PATH

FILENAME Input-Protection.kicad_pcb

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INPUT PROTECTION

3eaf265

DRAWING No

Assembly
SHEET

1 OF 3

A4

INPUT PROTECTION Assembly DOCUMENT

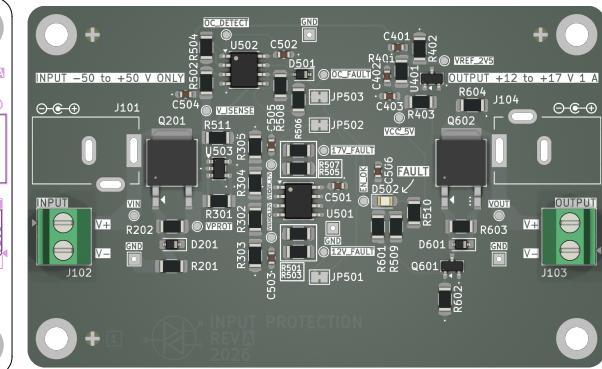
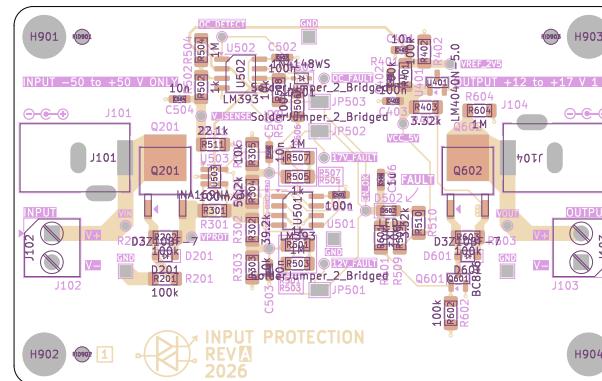
Components

| Type | Front Side | Back Side | Total |
|-------|------------|-----------|-------|
| THT | 5 | 0 | 5 |
| SMD | 45 | 0 | 45 |
| Total | 50 | 0 | 50 |

ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
- DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
- IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
- CONFORMAL COATING IS NOT REQUIRED.
- DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

Top Assembly (Scale 1:1)



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INPUT PROTECTION

SHEET PATH

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Assembly

A4

FILENAME Input-Protection.kicad_pcb

VARIANT

CHECKED

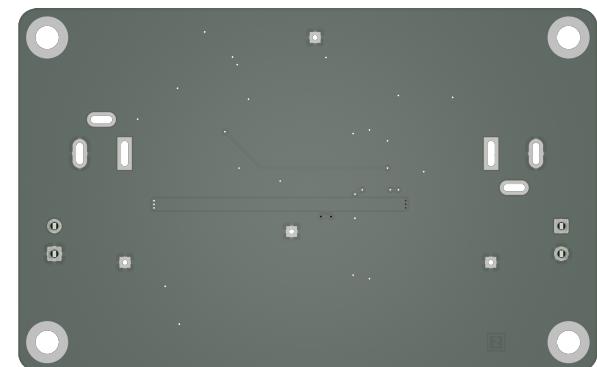
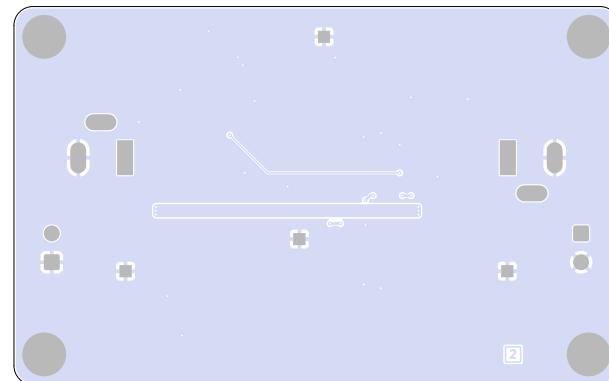
REVISION + (Unreleased)

SHEET

2 OF 3

INPUT PROTECTION Assembly DOCUMENT

Bottom Assembly (Scale 1:1)



PROJECT
ASSIGNMENT 1

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INPUT PROTECTION

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3eaf265

DRAWING No

REVISION + (Unreleased)

SHEET

3 OF 3

Assembly

A4