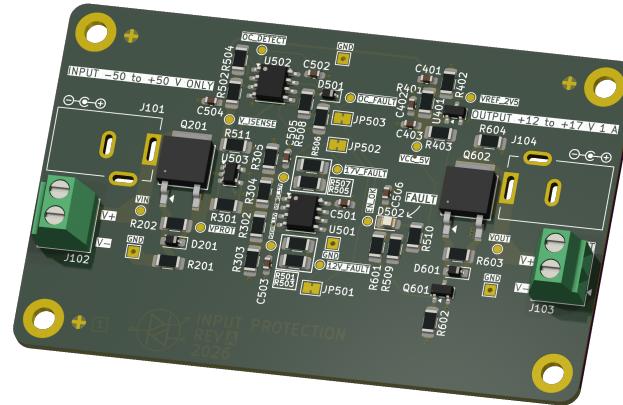
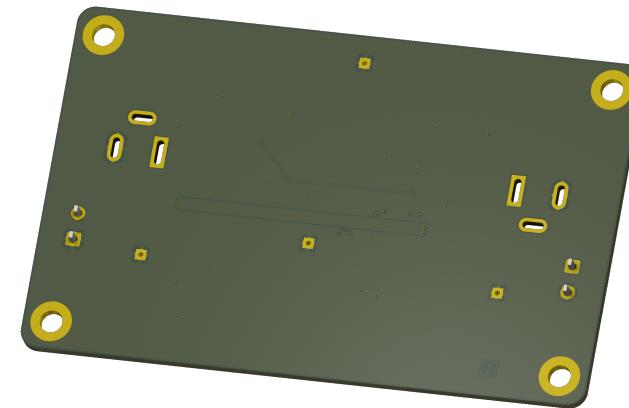


# INPUT PROTECTION ASSEMBLY DOCUMENT

Top View



Bottom View



PROJECT  
ASSIGNMENT 1  
DRN  
2024-04-13 R. HICKS  
CHK

ENG APP

MFR APP

SHEET PATH

FILENAME Input-Protection.kicad\_pcb

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Ryan Dynamics

INPUT PROTECTION

GIT HASH

555e892

DRAWING No

ASSEMBLY

A4

1.0.0 SHEET 1 OF 3

# **INPUT PROTECTION ASSEMBLY DOCUMENT**

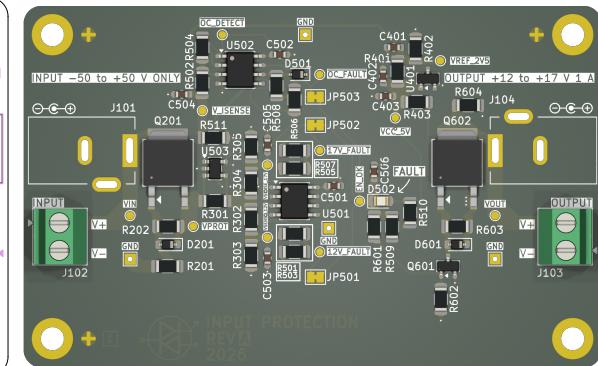
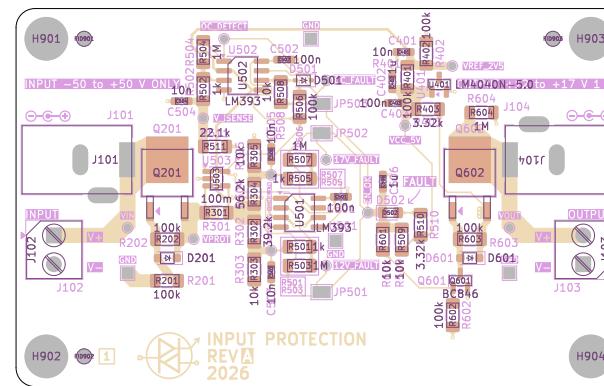
## Components

Type	Front Side	Back Side	Total
THT	5	0	5
SMD	45	0	45
<b>Total</b>	<b>50</b>	<b>0</b>	<b>50</b>

#### ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
  - 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
  - 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
  - 4) CONFORMAL COATING IS NOT REQUIRED.
  - 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

TOP ASSEMBLY (SCALE 1:1)



# PROJECT ASSIGNMENT 1

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DRN  
2024-04-13 R. HICKS

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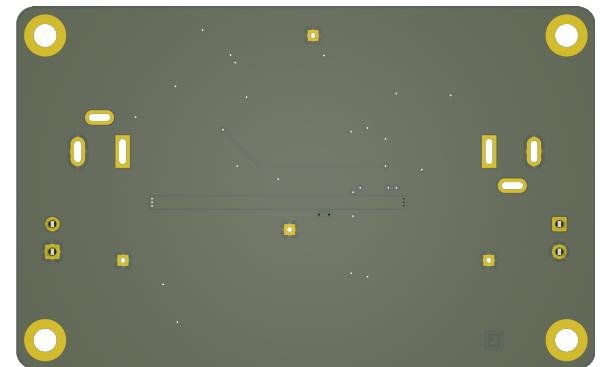
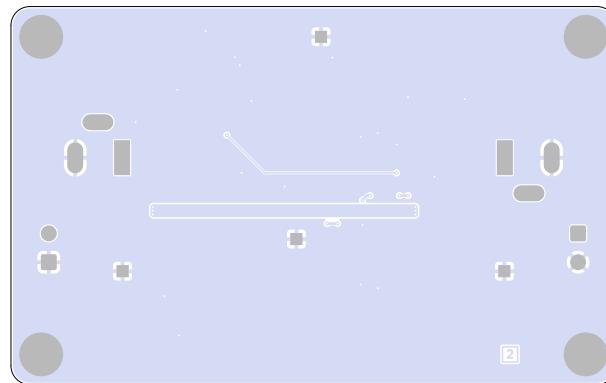
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## INPUT PROTECTION

SHEET PATH	GIT HASH	DRAWING No		
	555e892		ASSEMBLY	A4
FILENAME	Input-Protection.kicad_pcb	VARIANT	RELEASED	REVISION 1.0.0 SHEET 2 OF 3

# INPUT PROTECTION ASSEMBLY DOCUMENT

BOTTOM ASSEMBLY (SCALE 1:1)



PROJECT  
ASSIGNMENT 1

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2024-04-13 R. HICKS

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ASSEMBLY

A4

VARIANT

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REVISION

1.0.0

SHEET

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