

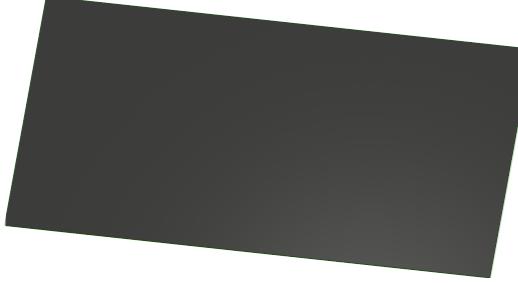
Input Protection

Variant: DRAFT

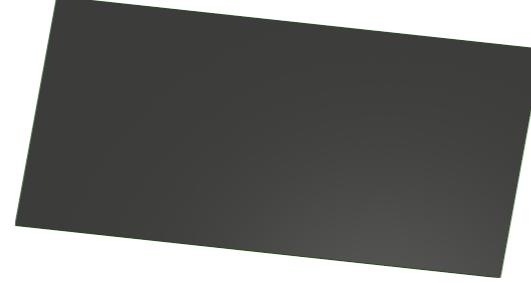
2026-02-07
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT – Very early stage of schematic, ignore details.

PRELIMINARY – Close to final schematic.

CHECKED – There shouldn't be any mistakes. Contact the engineer if found.

RELEASED – A board with this schematic has been sent to production.

PROJECT Assignment 1	Ryan Dynamics		
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CHK			
ENG APP			
MFR APP			
SHEET PATH /	GIT HASH 9521b57	DRAWING No	A3
FILENAME Input-Protection.kicad_sch	VARIANT DRAFT	REVISION + (Unreleased)	
SHEET 1 OF 4			

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

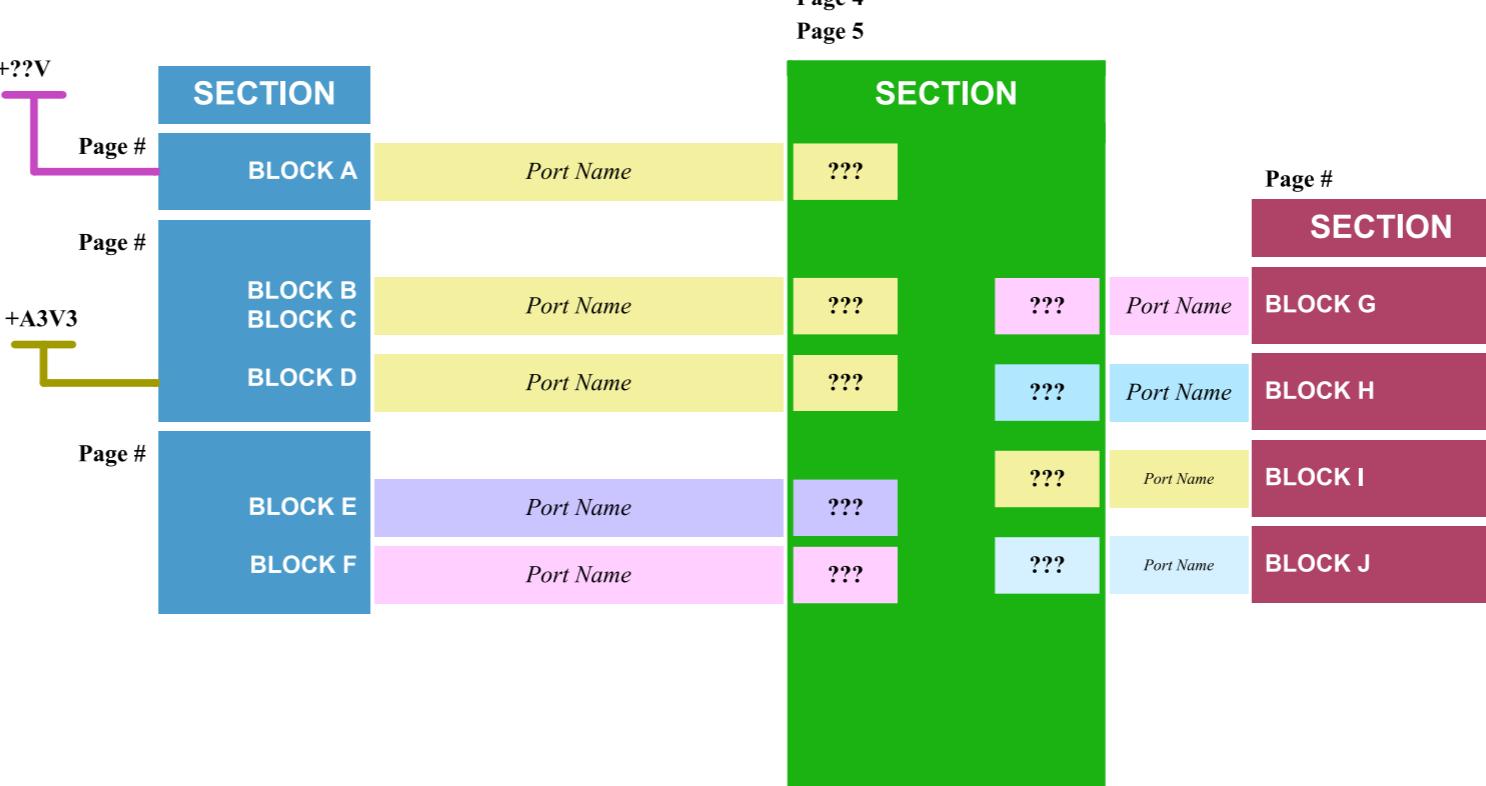
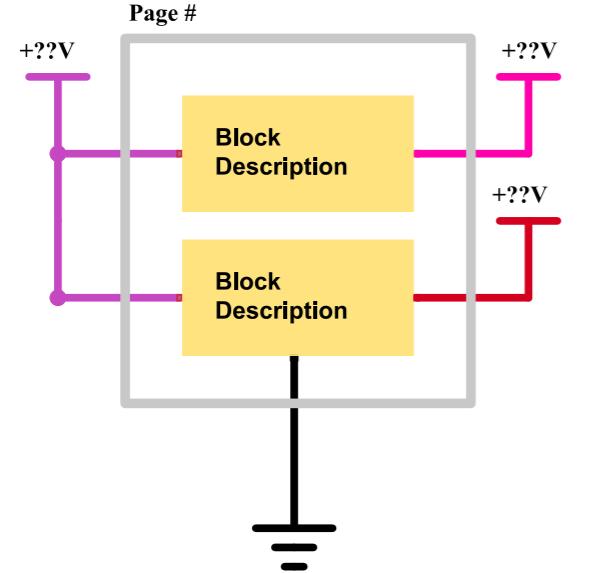
DESIGN NOTE:
Example text for debug notes.

DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

[2] BLOCK DIAGRAM



TARGET SPECIFICATIONS:

INPUT VOLTAGE: -50 to +50 V

OUTPUT VOLTAGE: +12 to +17 V

Spec 3 ?

Spec 4 ?

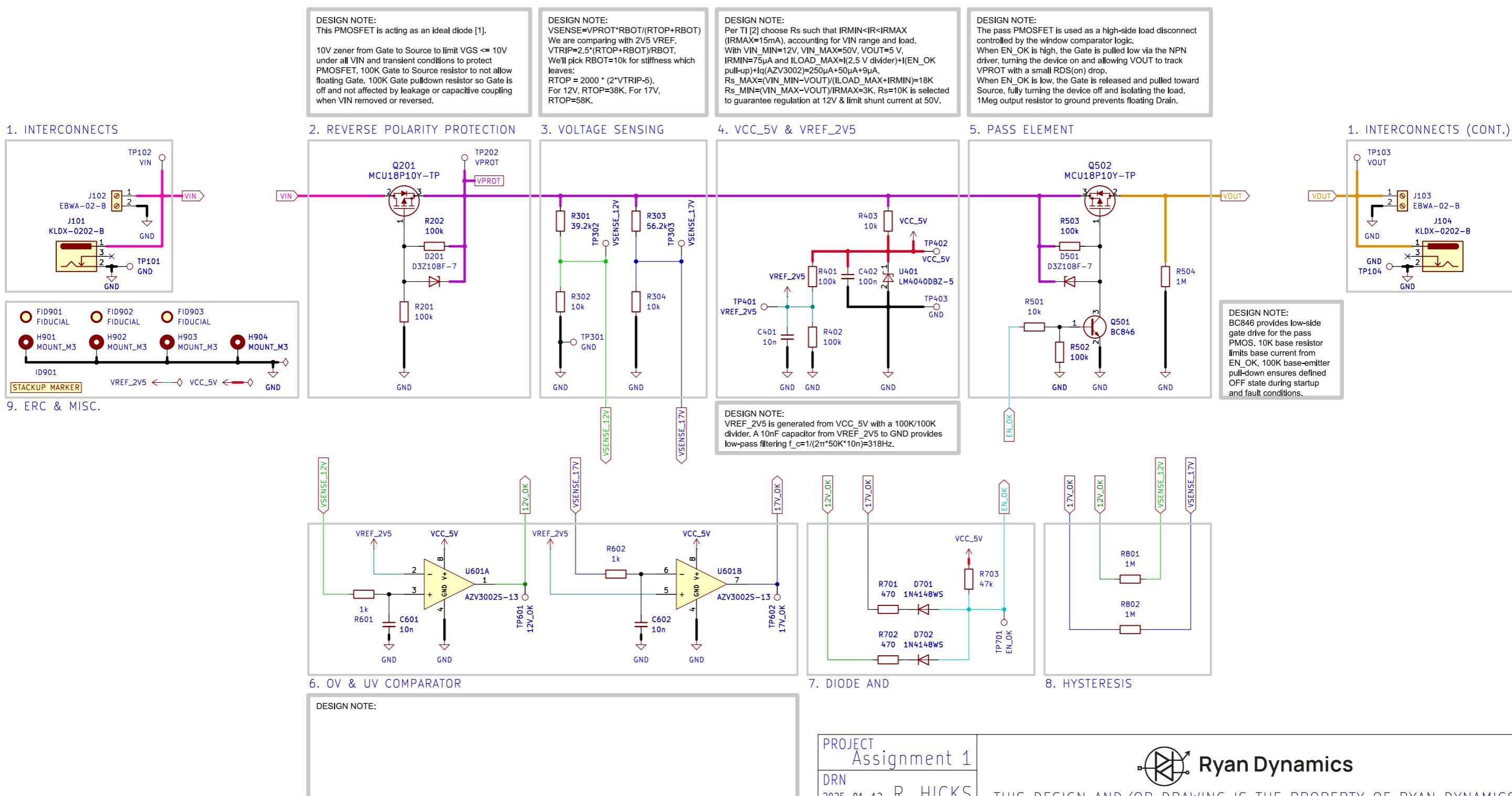
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2025-01-12	
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ENG APP	
MFR APP	
SHEET PATH	GIT HASH
/Block Diagram/	9521b57
FILENAME	DRAWING No
Block Diagram.kicad_sch	BLOCK DIAGRAM
VARIANT	A3
DRAFT	REVISION + (Unreleased)
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[3] SCHEMATIC



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3 OF 4



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SCHEMATIC

A3

[4] REVISION HISTORY

A

A

B

B

C

C

D

D

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