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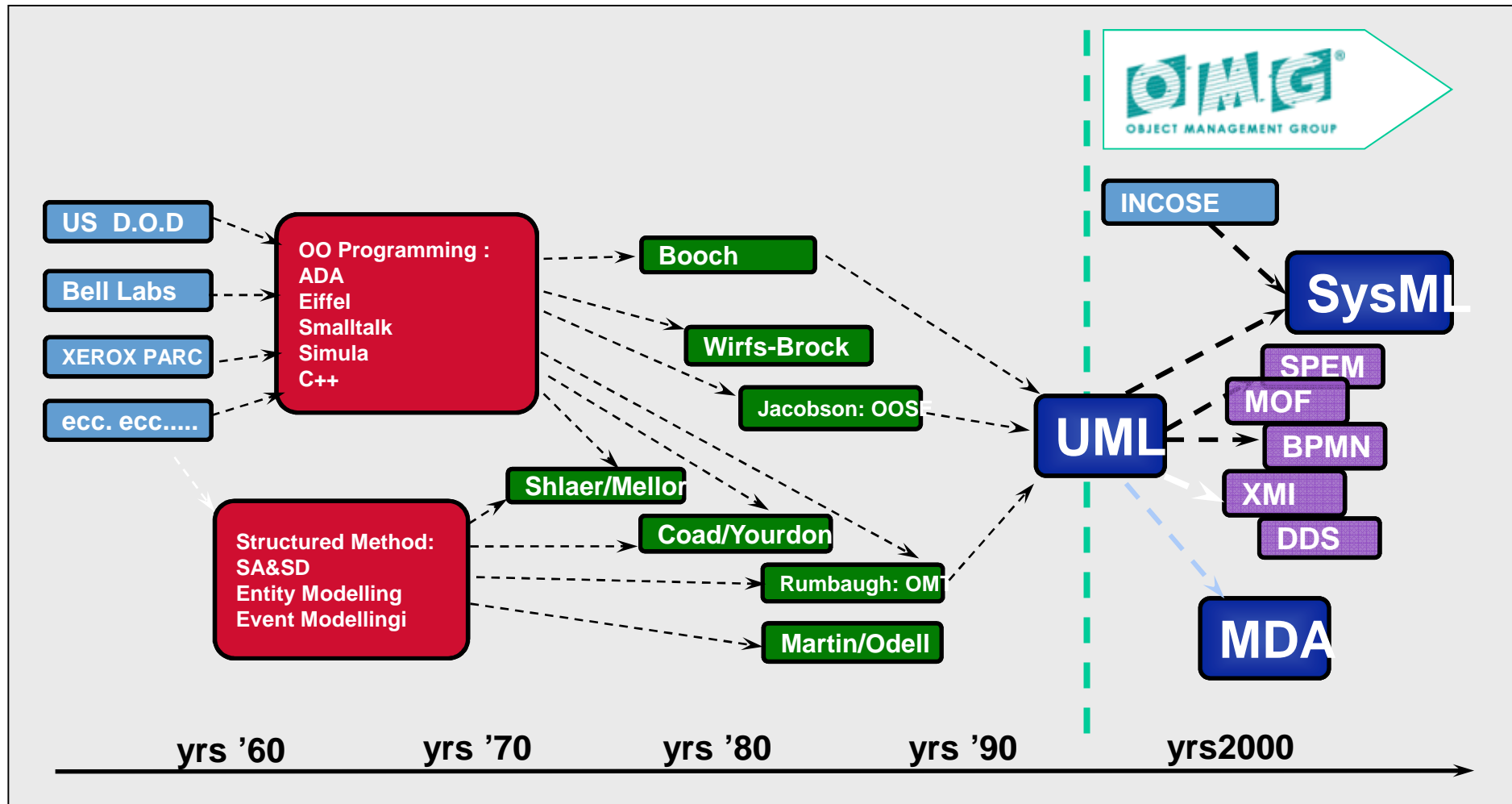
**SysML**

**THALES**

**AIRBUS**



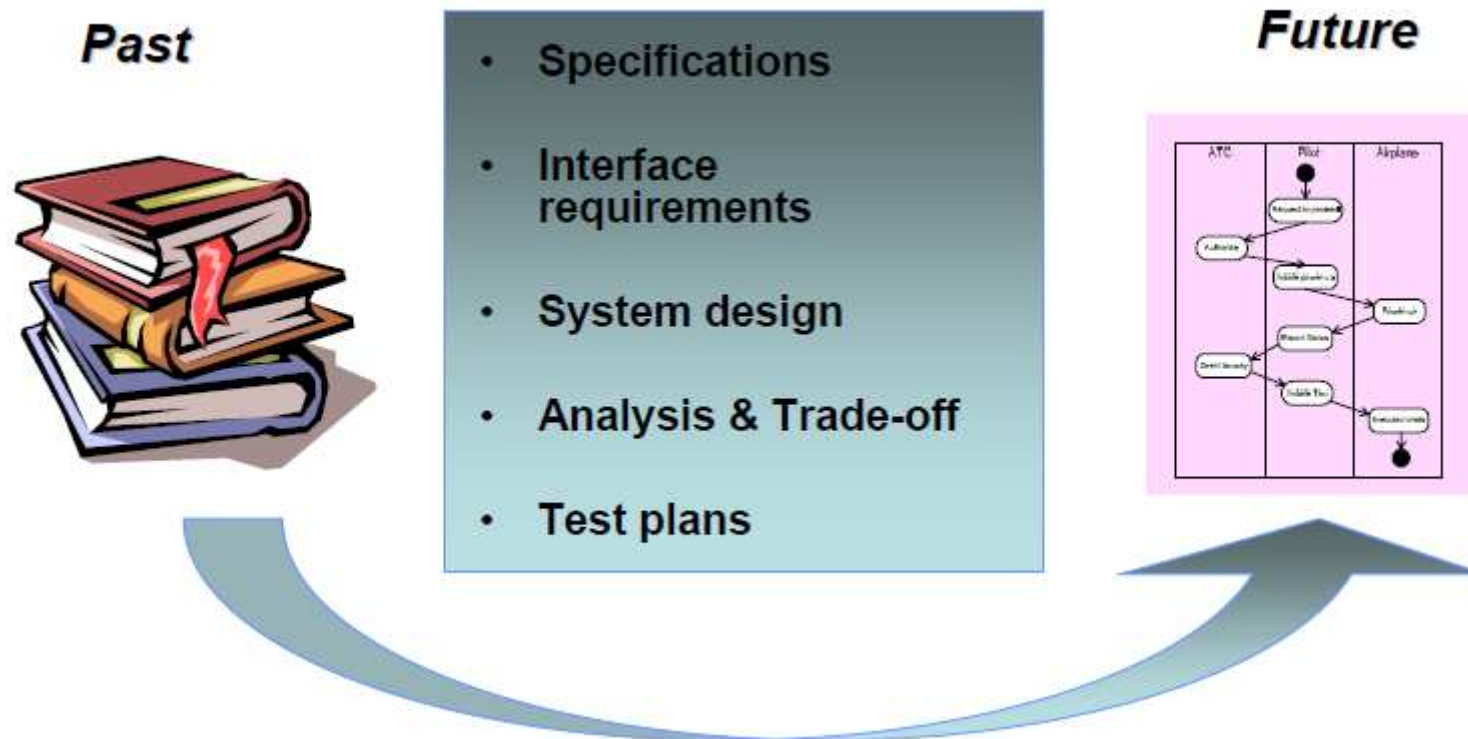
# Historical Perspective



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# From document centric to Model centric



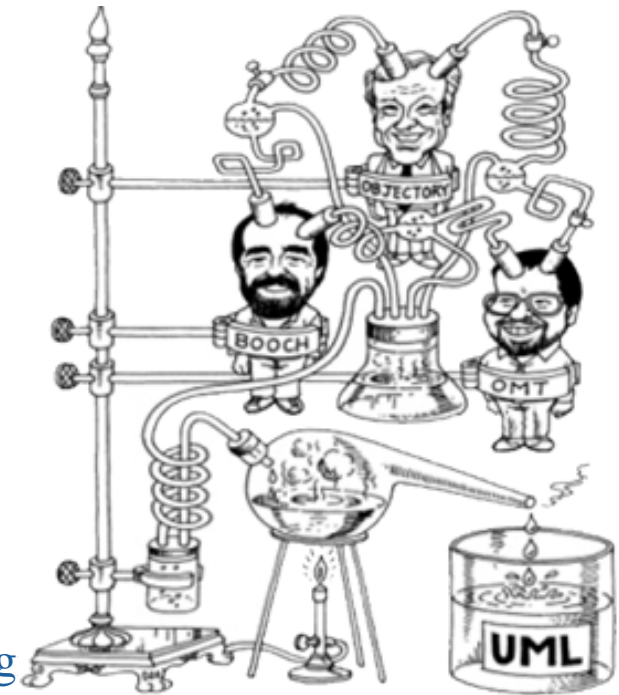
# What is UML

## ➤ UML (Unified Modeling Language)

- ❑ A consensus on best practices in software eng

- Now a de facto standard

- ❑ A graphical modeling language supported by tools



## From UML to SysML (3/3)

- Why not UML for systems ?
  - ❑ Objects are for computer-literates, not systems engineers
  - ❑ Requirements are described outside the model using, e.g., DOORS
  - ❑ Allocation relations are not explicitly supported
- Nevertheless SysML is a UML 2 profile
  - ❑ In response to the UML for Systems Engineering RFP developed by the OMG and INCOSE



- SysML standard
  - ❑ <http://www.omgsysml.org/>

# Who are the main contributors

## ➤ Industry & Government

- ❑ American Systems, BAE SYSTEMS, Boeing, Deere & Company, EADS-Astrium, Eurostep, Lockheed Martin, Motorola, NIST, Northrop Grumman, oose.de, Raytheon, THALES

## ➤ Vendors

- ❑ Artisan, EmbeddedPlus, Gentleware, IBM, I-Logix, Mentor Graphics, PivotPoint Technology, Sparx Systems, Telelogic, Vitech Corp

## ➤ Academia

- ❑ Georgia Institute of Technology

## ➤ Liaison Organizations

- ❑ INCOSE, ISO AP233 Working Group



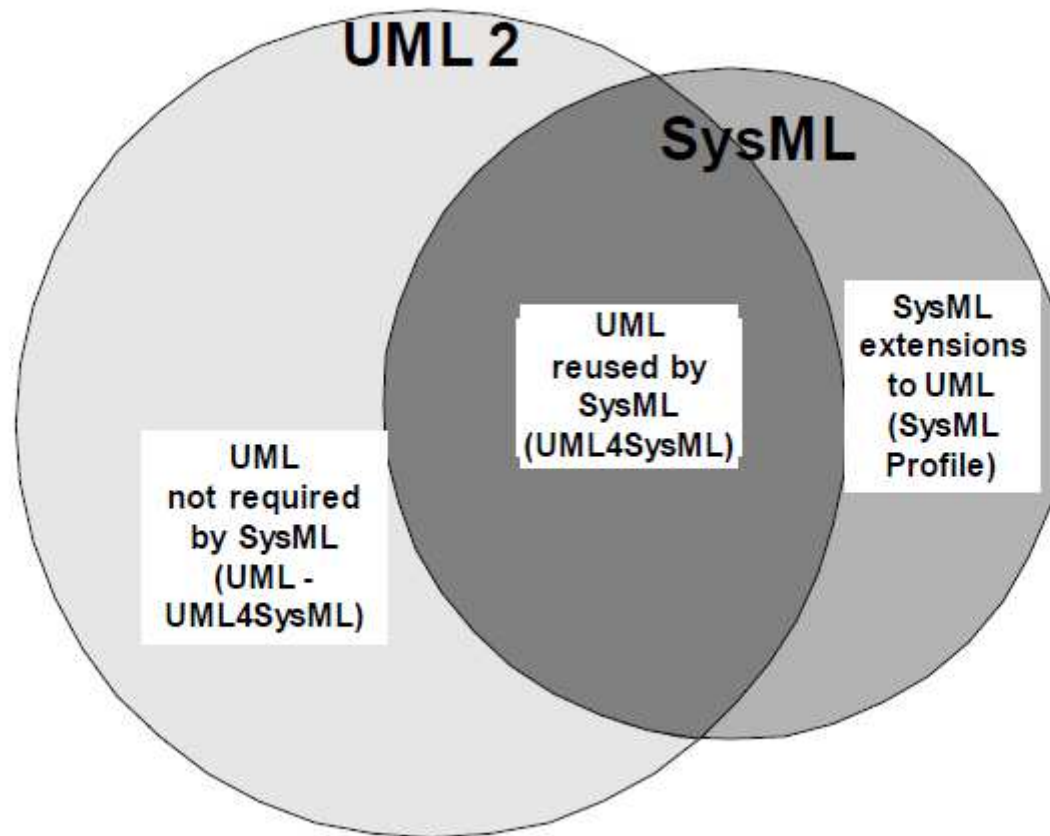
# What is SysML?



- SysML has been designed with systems in mind
  - ❑ A graphical modelling language that supports the specification, analysis, design, verification, and validation of systems
- SysML is a notation, not a method
- **The SysML standard is a reference language for system engineering**
- SysML is supported by tools
  - ❑ SysML plugins for UML tools
  - ❑ Proprietary tools: Enterprise Architect, Rhapsody, Modelio, ...
  - ❑ Free software tools: TOPCASED, TTool, ...

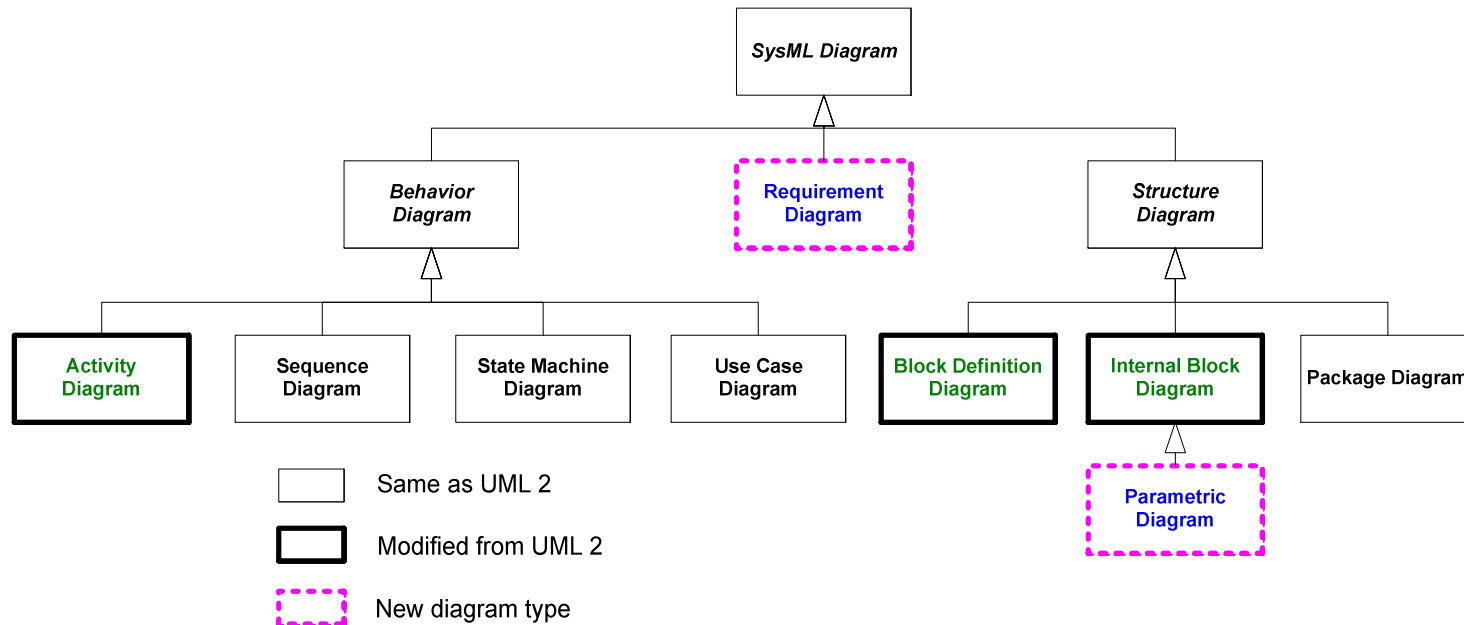


# Relationship between UML and SysML





# SysML Diagrams Taxonomy



## SysML diagrams (1/3)

Diagram name	Description
Requirement Diagram	Shows system requirements and their relationships with other elements.
Use-case Diagram	Shows system services and functions with respect to its external environment.
Sequence Diagram	Show interactions between blocks inside the system, as well as interactions between the system and actors.
Activity Diagram	Shows system behavior as function flows and data flows

## SysML diagrams (2/3)

Diagram name	Description
Package Diagram	Show how a model is organized into packages, views and viewpoints.
Block Diagram	Show system structure as components along with their properties, operations and relationships.
Internal Block Diagram	Show the internal structures of components, including their parts and connectors.

## SysML diagrams (3/3)

Diagram name	Description
State Machine Diagram	Shows system internal behavior as states and transitions between states.
Parametric Diagram	Shows parametric constraints between structural elements.



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## Use Case diagram

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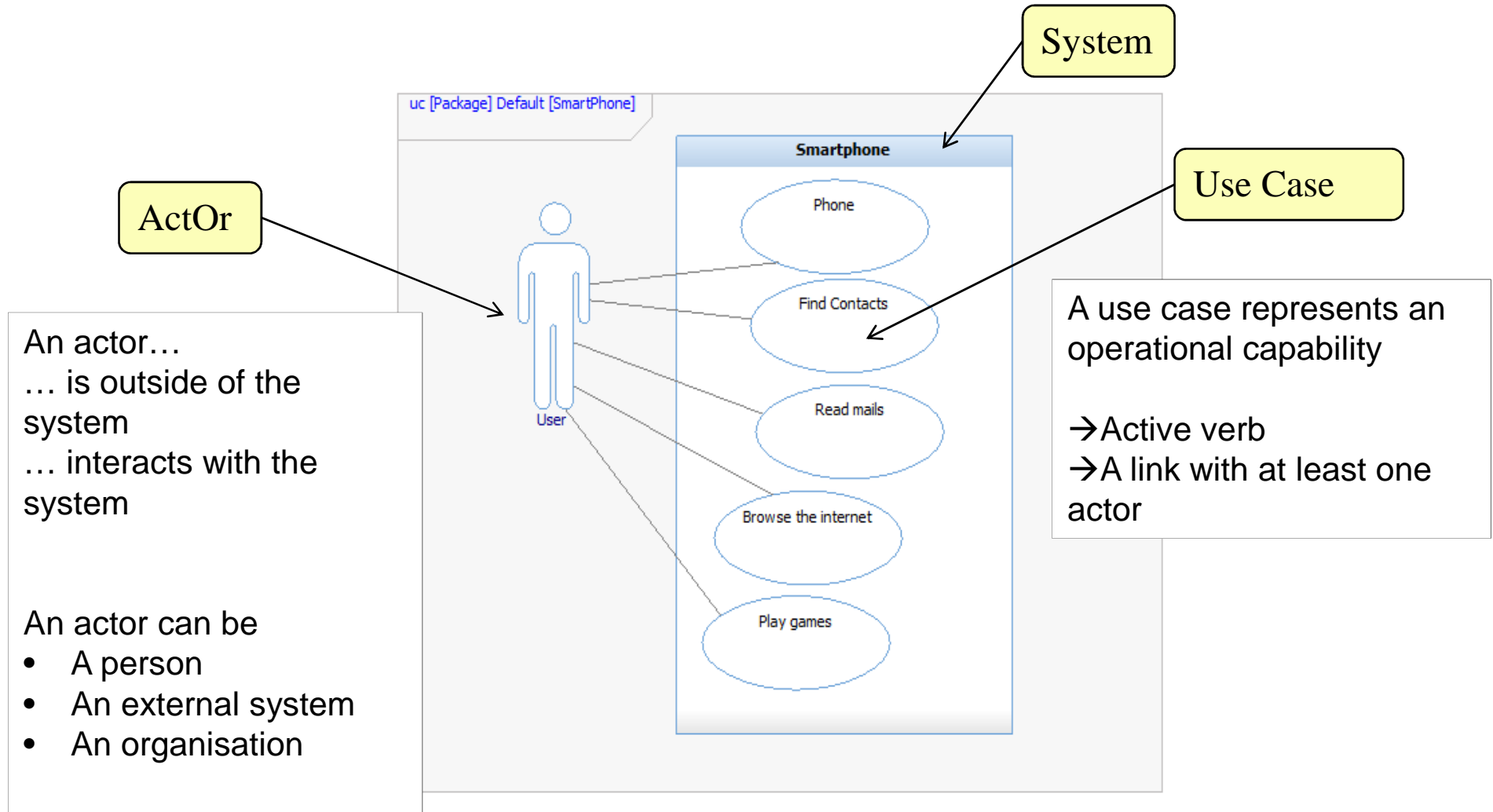


# Why use case modeling?

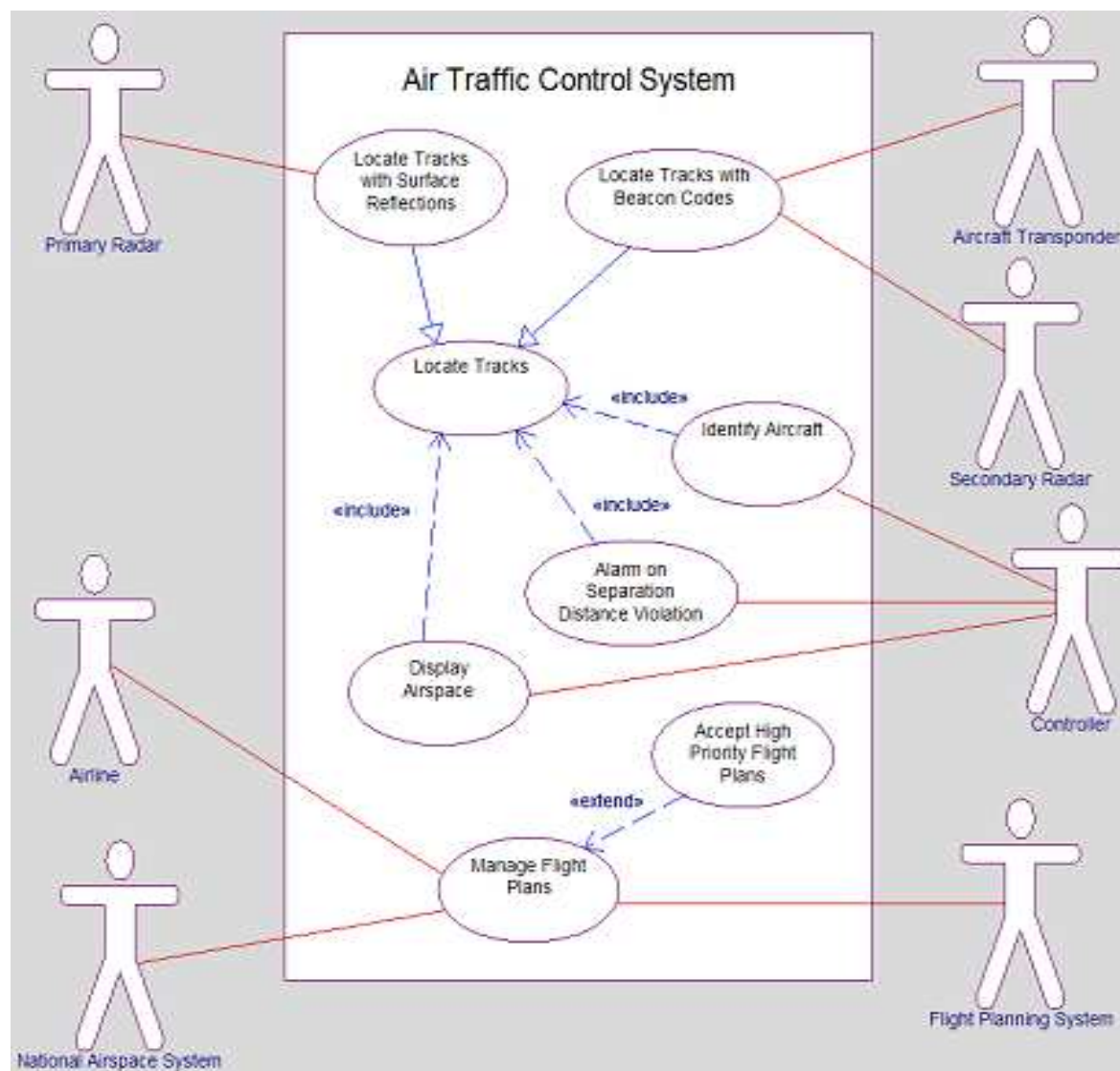
- Use case modeling is all about describing WHAT the system will do:
  - ❑ At a high level – Analysis NOT Design
  - ❑ From the perspective of the User
  
- Use cases scope the system:
  - ❑ The suppliers have to develop the functionality described by the use cases.
  - ❑ Users should not expect more/less/different functionality than is described by the use cases.
  
- Use cases organize requirements:
  - ❑ Typically 8-20 pages of requirements per use case.



# Example of Use Case Diagram

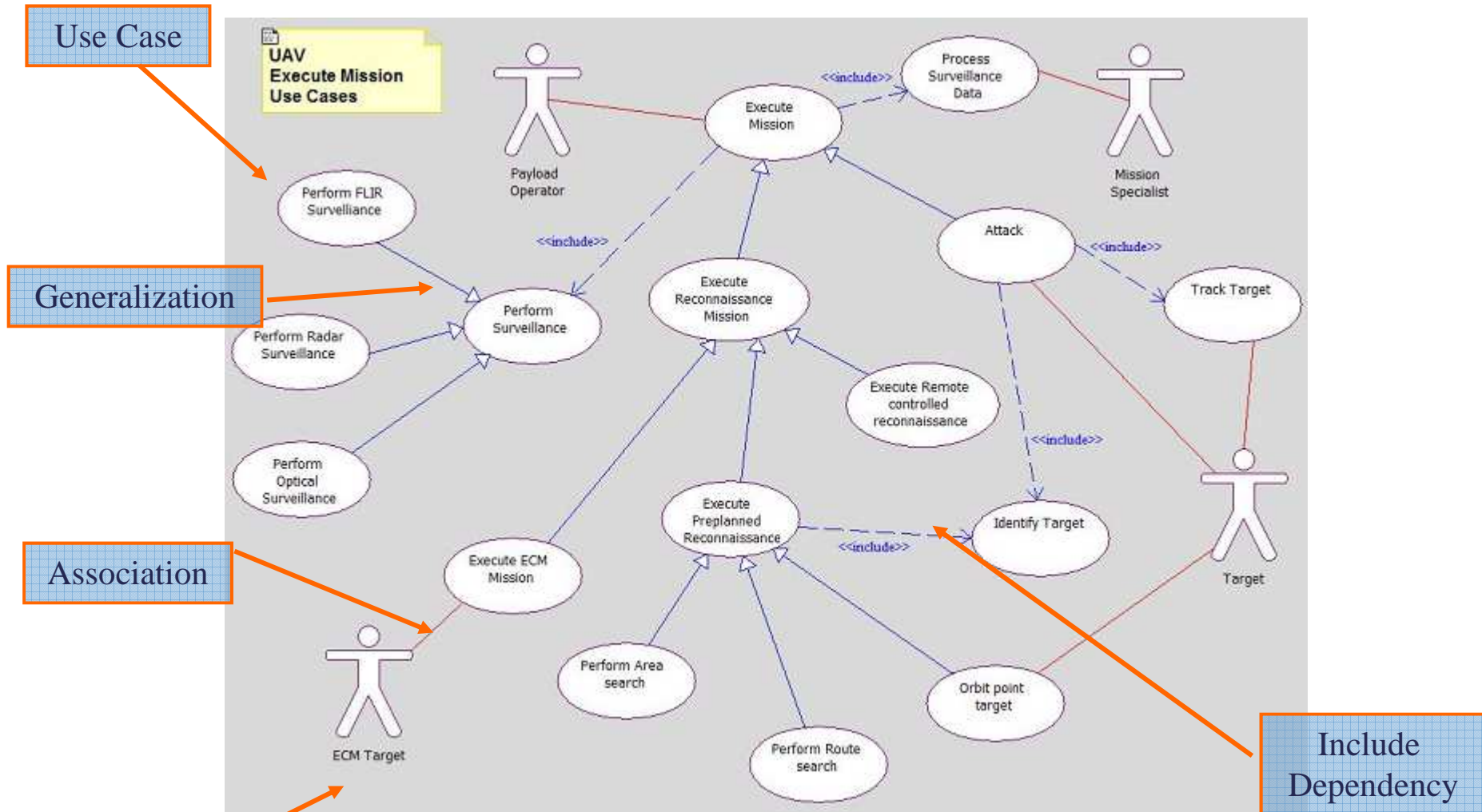


## Elaborated Use case





# Complete use case syntax





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## Sequence Diagram

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 **EADS**

 **eurocopter**  
an EADS Company

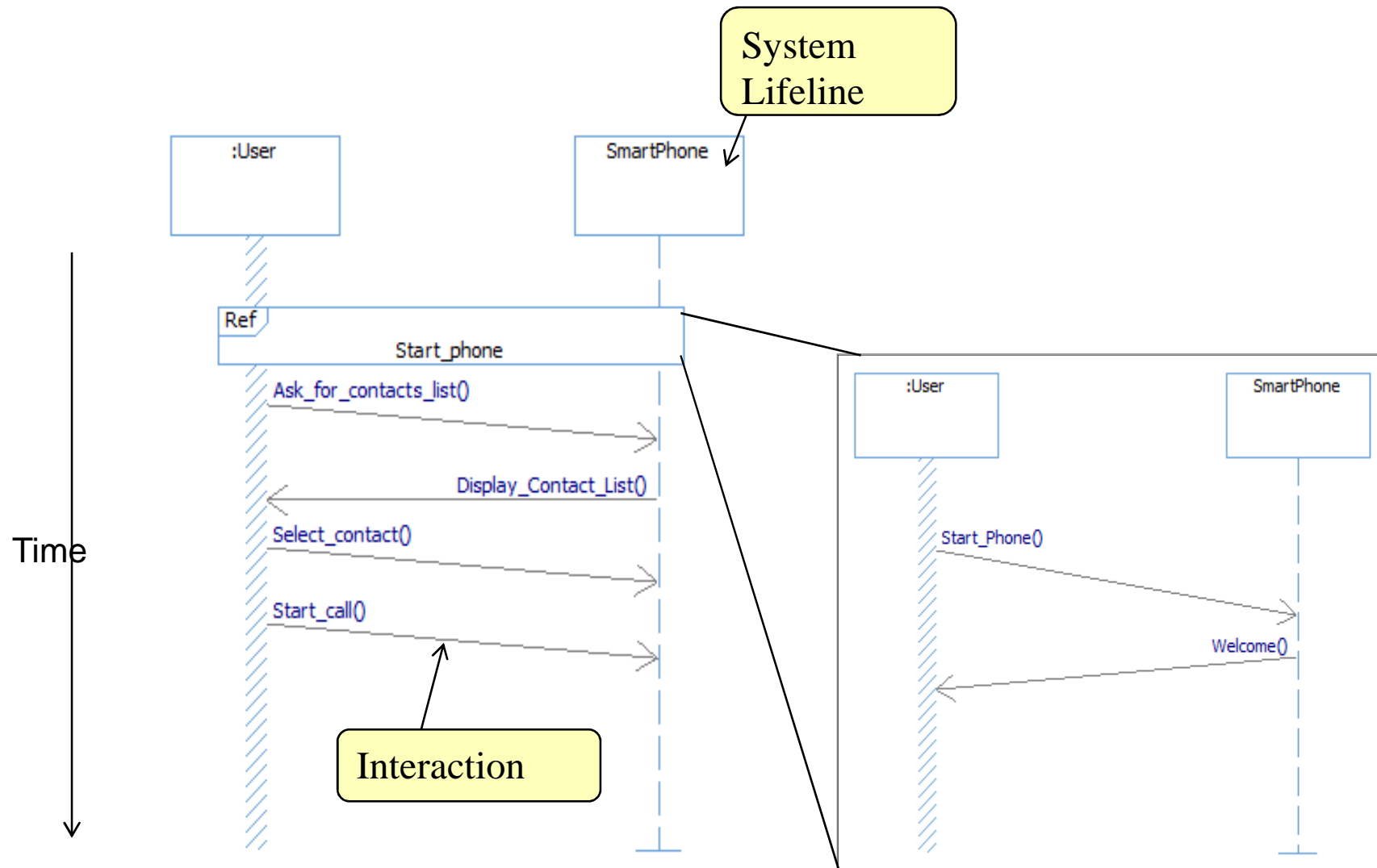
 **SAFRAN**

# SysML tool: sequence diagram

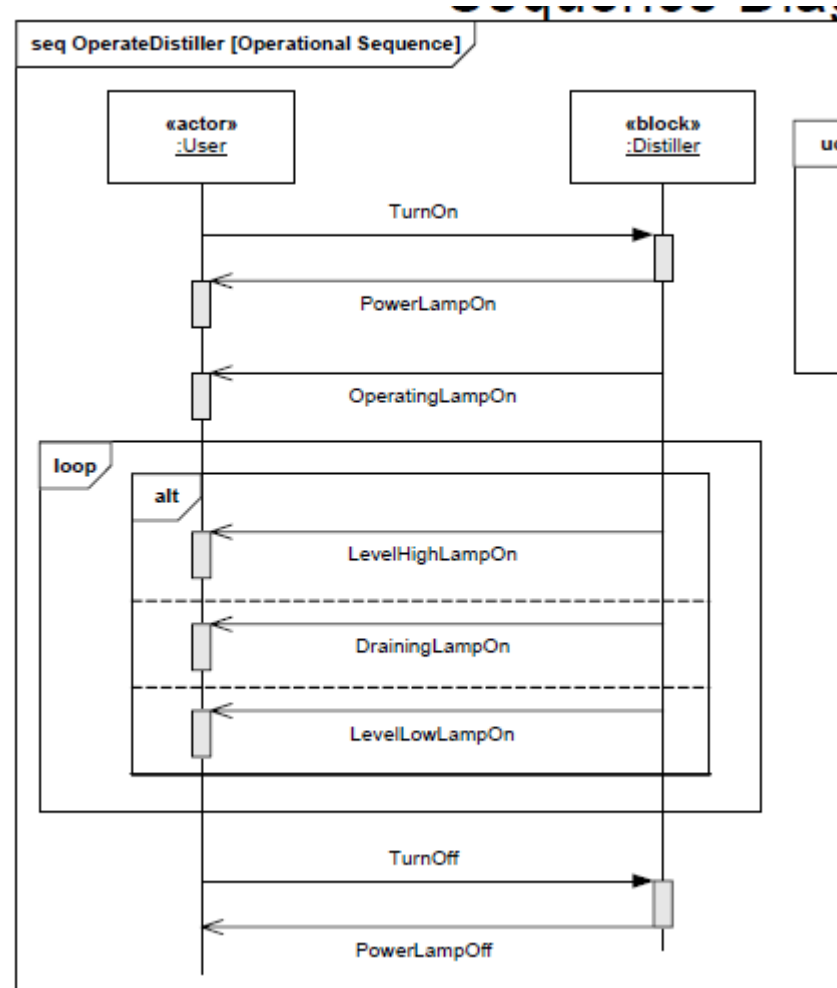
- Objective
  - Representation of a sequence of operations, progressing in time
  - Description of the interactions between the system of interest and external actors



# Sequence Diagram: example



# Distiller sequence diagram





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## State Machine Diagram

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# State Machine Diagram

- Typically used to represent the life cycle of a block
- Support event-based behavior (generally asynchronous)
  - ❑ Transition with trigger, guard, action
  - ❑ State with entry, exit, and do-activity
  - ❑ Can include nested sequential or concurrent states
  - ❑ Can send/receive signals to communicate between blocks during state transitions, etc.



## Statecharts (2)

- What is a state?



A state is a condition of existence of an object that persists for a significant period of time

- What is a transition?



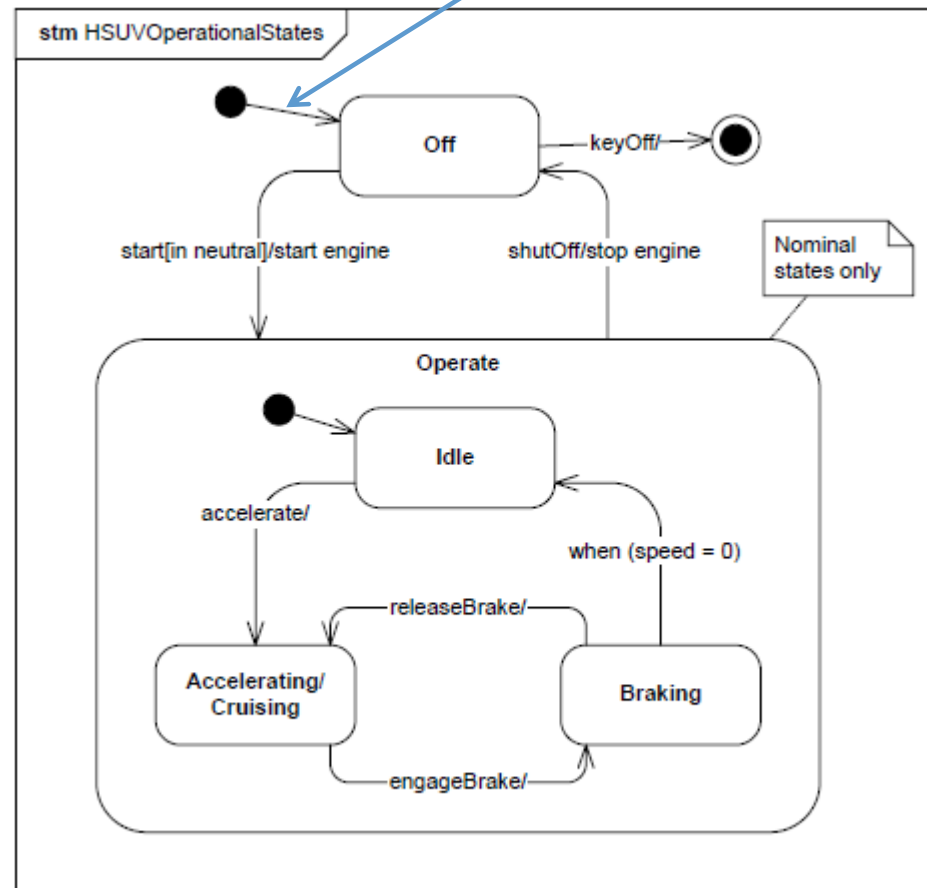
A transition is a response to an event, moving the object from a state to a state.



# State Machine Diagram example

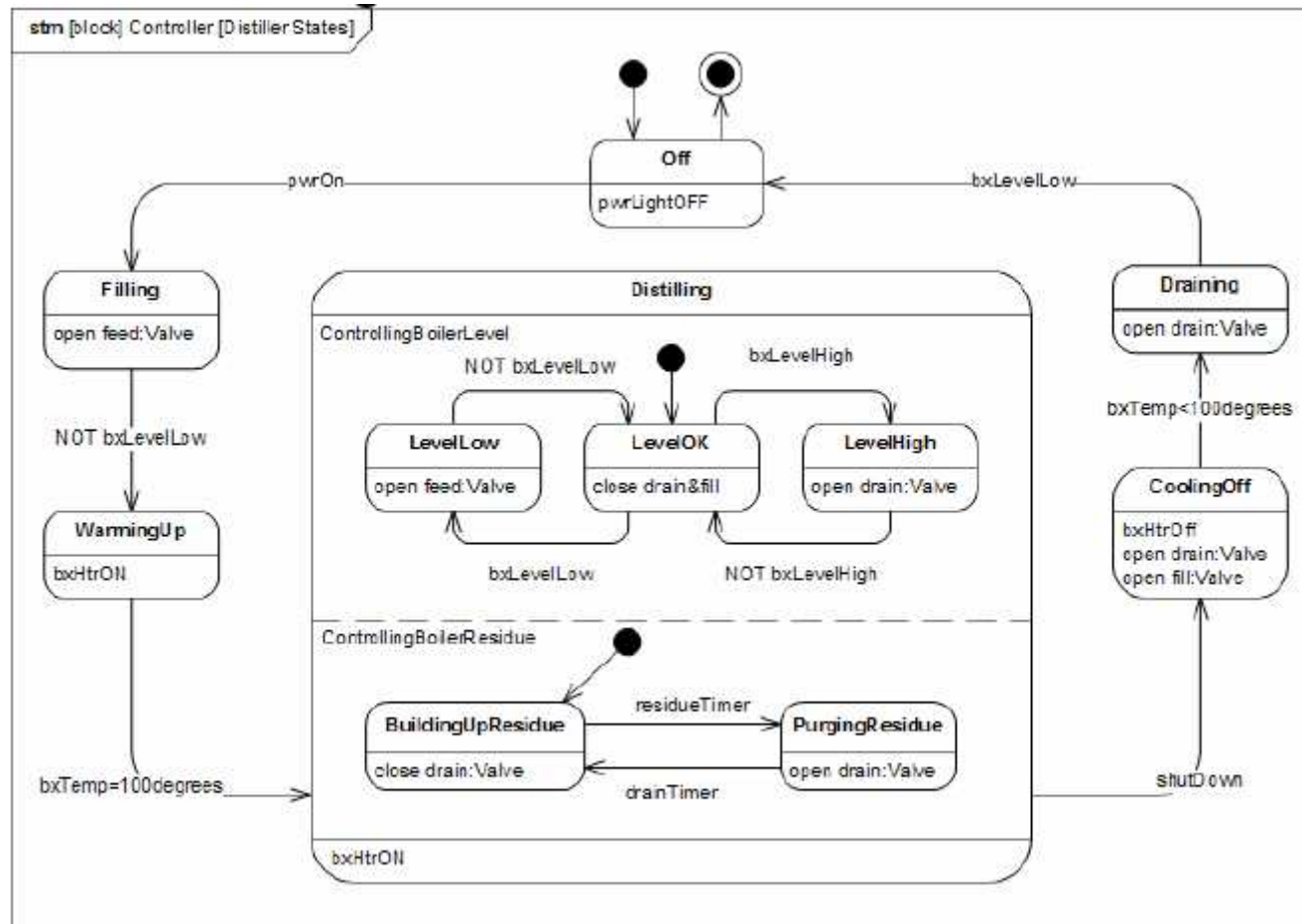
Transition Syntax:  
trigger [ guard ] / action list

Default transition



T  
ti

# State Machine example





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## Diagrams for structure

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## Basic Structuring Entity: Block

- Provides a unifying concept to describe the structure of an element or system

- Hardware
- Software
- Data
- Procedure
- Facility
- Person

<b>«block» BrakeModulator</b>
<i>allocatedFrom</i> «activity»Modulate BrakingForce
<i>values</i> DutyCycle: Percentage

- Multiple compartments can describe the block characteristics
  - Properties (parts, references, values)
  - Operations
  - Constraints
  - Allocations to the block (e.g. activities)
  - Requirements the block satisfies

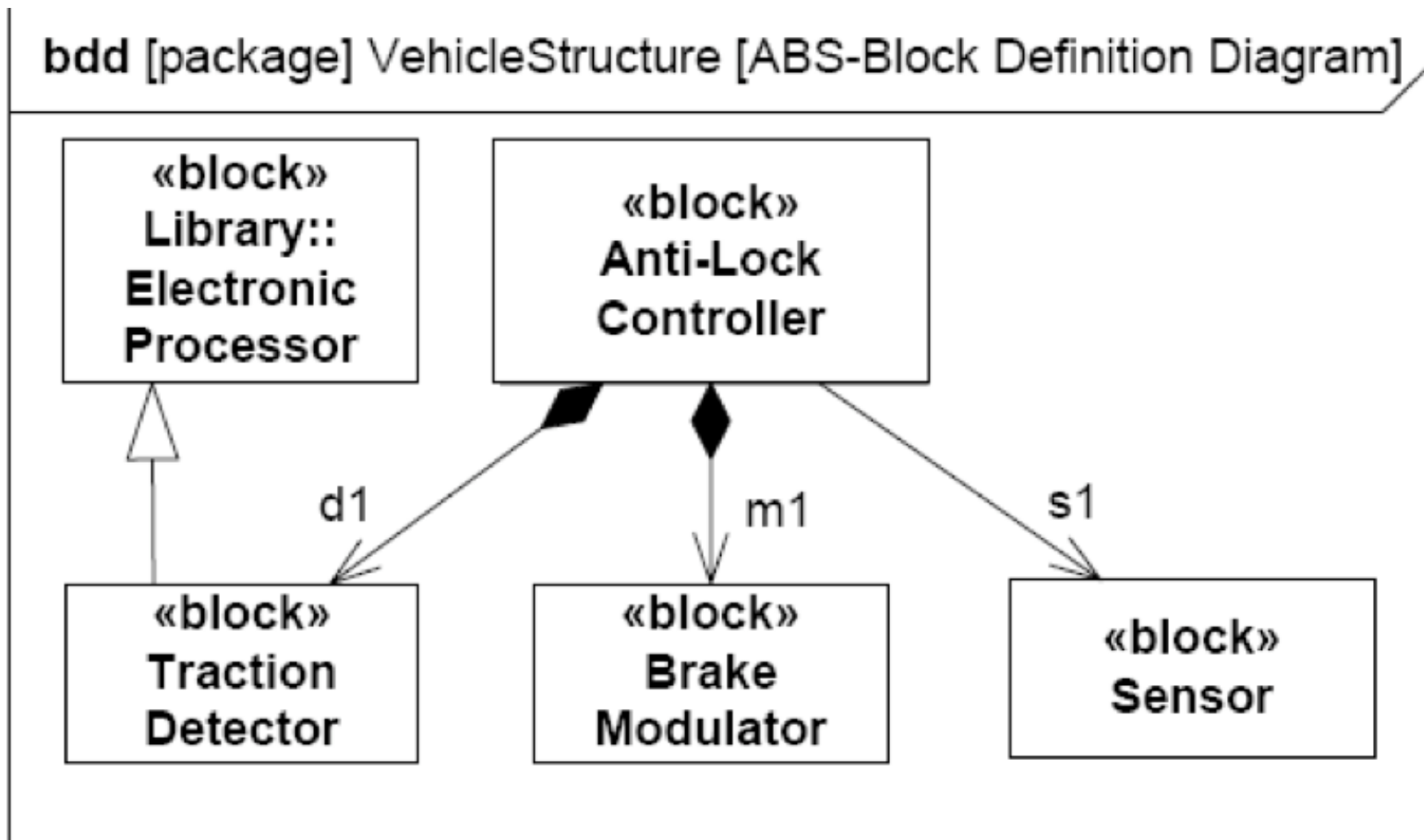
# Using blocks

- Blocks are used to specify hierarchies and interconnection
- Block definition diagram describes the relationship among blocks (e.g., composition, association, classification)
  - ❑ Behavior can be allocated to blocks

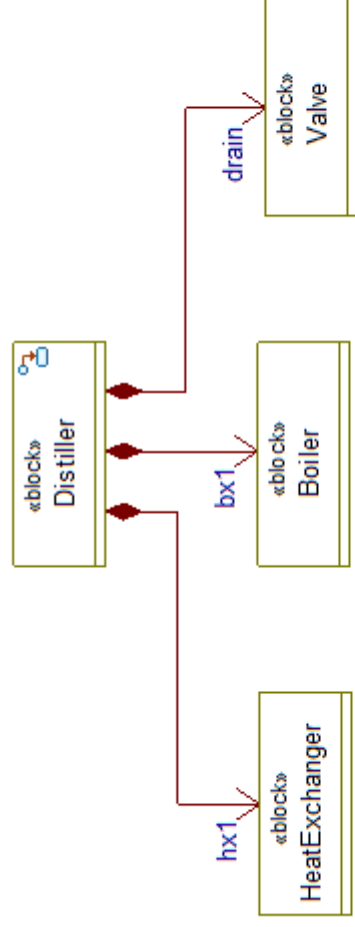


# Block Definition Diagram

## ➤ Type Definitions



bdd [Package] Structure [bdd 1 Structural Breakdown]



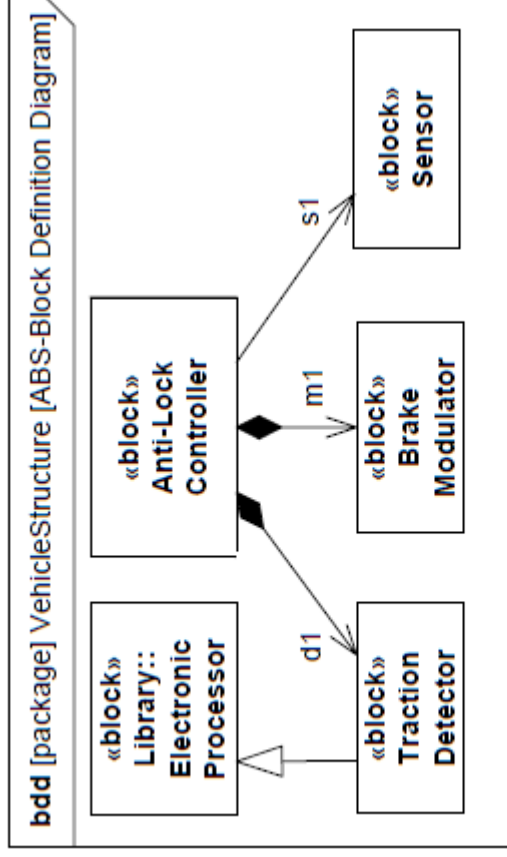
# Internal block diagram

- Internal block diagram describes the internal structure of a block in terms of its properties and connectors





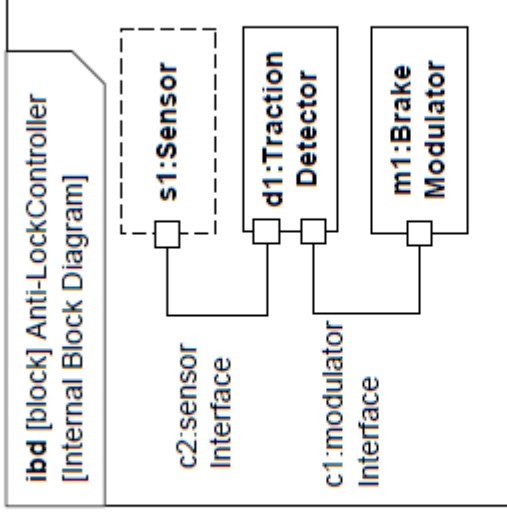
## Block Definition Diagram



## Definition

- Block is a definition/type
- Captures properties, etc.
- Reused in multiple contexts

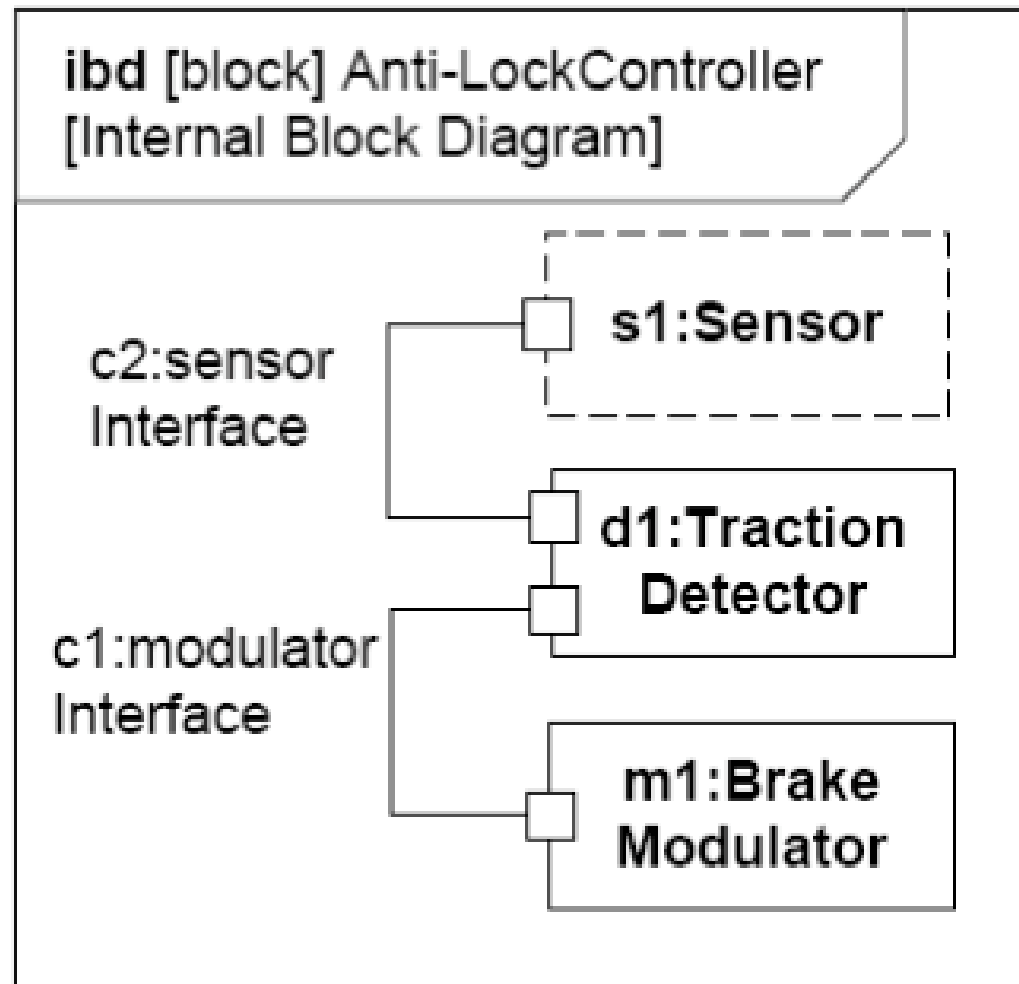
## Internal Block Diagram



## Usage

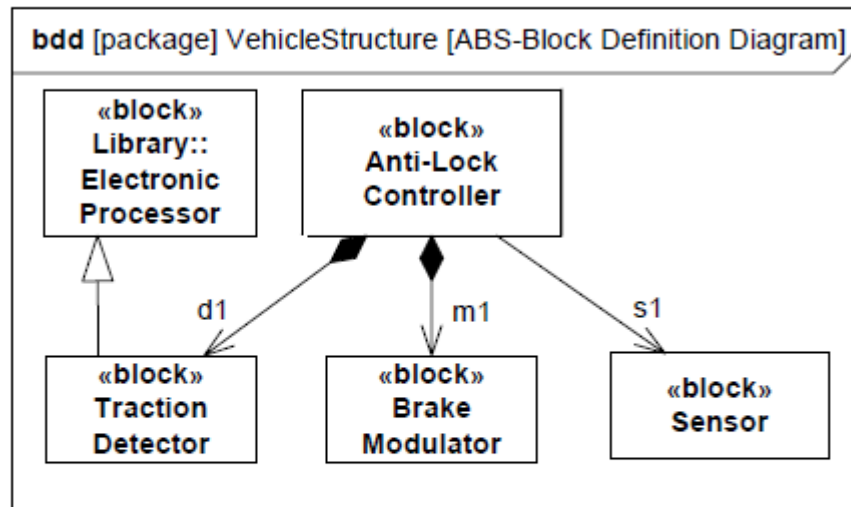
- Part is the usage in a particular context
- Typed by a block
- Also known as a role

## Internal Block Diagram



# Relation between BDD and IBD

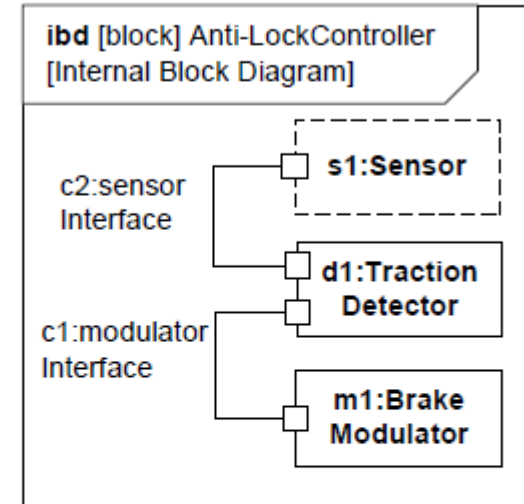
## Block Definition Diagram



## Definition

- Block is a definition/type
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## Internal Block Diagram



## Usage

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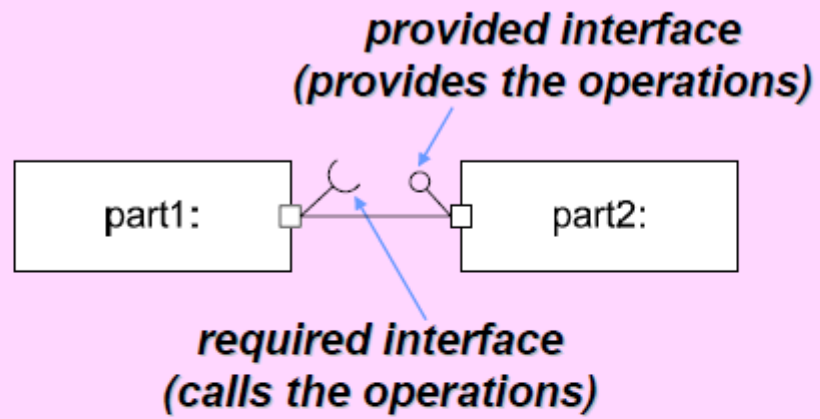
# Ports

- Specifies interaction points on blocks and parts
  - Supports integration of behavior and structure
- Port types
  - Standard (UML) Port
    - Specifies a set of operations and/or signals
    - Typed by a UML interface
  - Flow Port
    - Specifies what can flow in or out of block/part
    - Typed by a flow specification

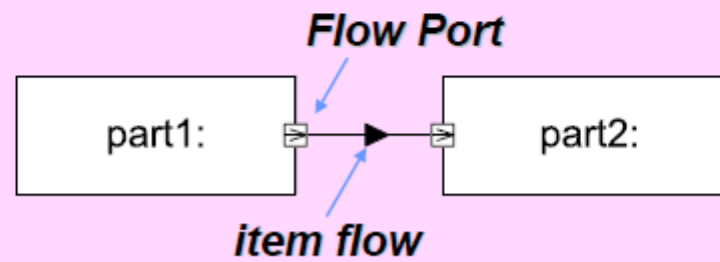


# Ports

**Standard  
Port**

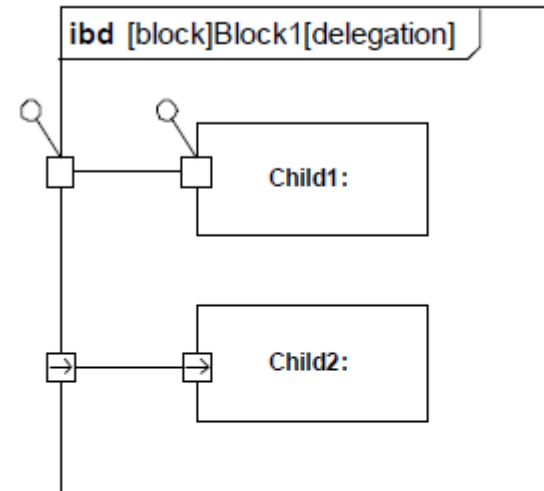


**Flow  
Port**



## Delegation through ports

- Delegation can be used to preserve encapsulation of block
- Interactions at outer ports of Block1 are delegated to ports of child parts
- Ports must match (same kind, types, direction etc.)
- (Deep-nested) Connectors can break encapsulation if required (e.g. in physical system modeling)





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## Parametric diagrams

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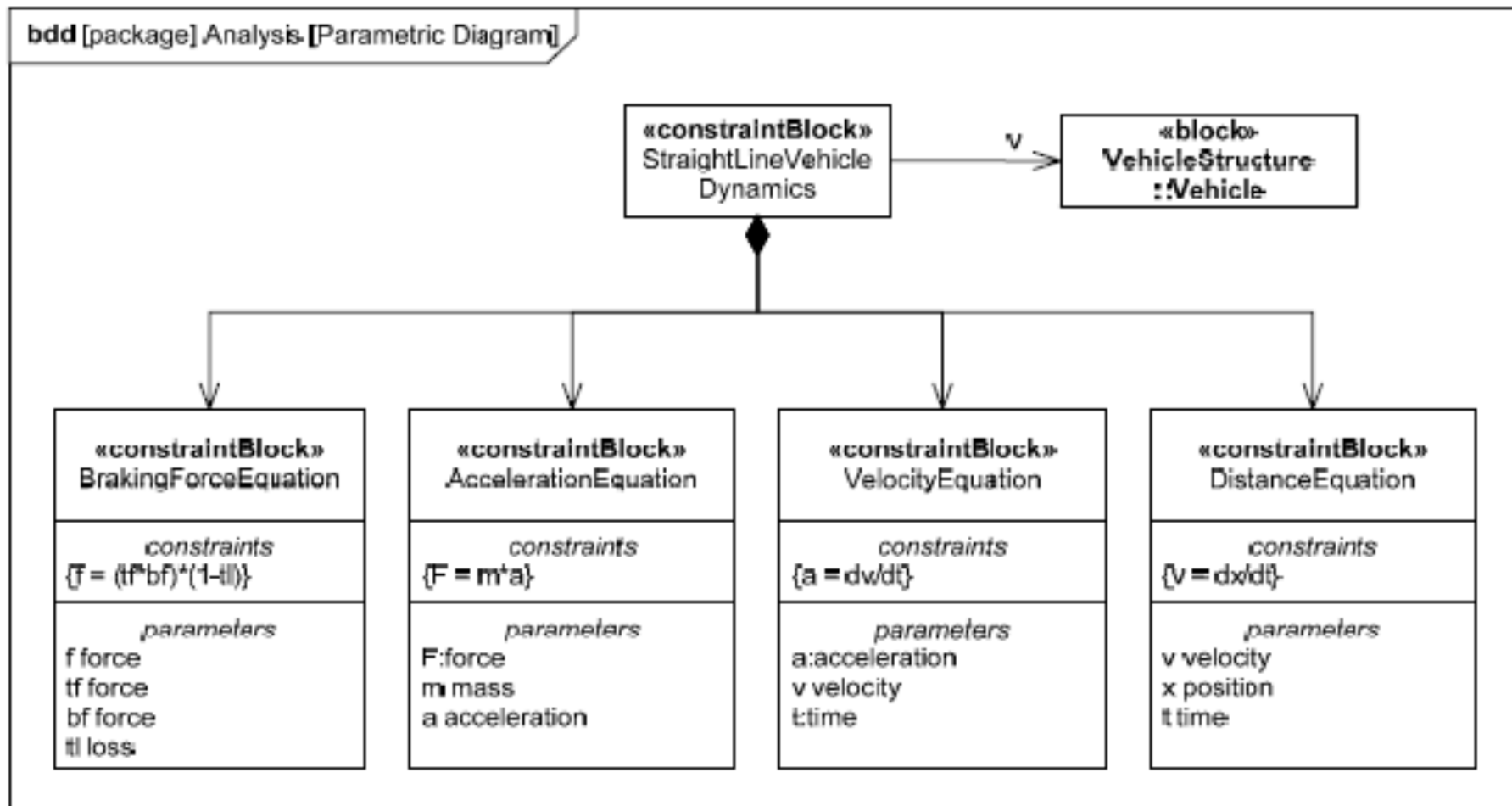
# Parametric Diagrams

- Used to express constraints (equations) between value properties
  - ❑ Provides support for engineering analysis (e.g., performance, reliability)
- Constraint block captures equations
  - ❑ Expression language can be formal (e.g., MathML, OCL) or informal
  - ❑ Computational engine is defined by applicable analysis tool and not by SysML
- Parametric diagram represents the usage of the constraints in an analysis context
  - ❑ Binding of constraint usage to value properties of blocks (e.g., vehicle mass bound to  $F = m \times a$ )
- Parametrics Enable Integration of Engineering Analysis with Design Models





# Defining Vehicle Dynamics



# Parametric Diagram

