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// 15-745 S14 Assignment 2: reaching-definitions.cpp
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/////////////////////////////////////////////////////////////////

#include "llvm/IR/Function.h"
#include "llvm/Pass.h"
#include "llvm/Support/raw_ostream.h"

#include "dataflow.h"

using namespace llvm;

namespace {

// 1-1 mapping between indices and variables
std::vector<std::string> itov;
std::map<Value*, int> vtoi;

Elem reachingDefsTransition(Instruction* instr, Elem elem)
{
    // generate defined variable
    int idx = vtoi[instr] - 1;
    if (idx != -1)
    {
        elem[idx] = true;
    }
    return elem;
}

class ReachingDefinitions : public FunctionPass {
public:
    static char ID;

    ReachingDefinitions() : FunctionPass(ID) { }

    virtual bool runOnFunction(Function& F) {
        //ExampleFunctionPrinter(errs(), F);

        itov.clear();
        vtoi.clear();
        // find variables passed as arguments
        for (ilist_iterator<Argument> AI = F.arg_begin(), AE = F.arg_end(); AI != AE; ++AI)
        {
            std::string name = "%";
            name += AI->getName();
            itov.push_back(name);
            vtoi[AI] = itov.size();
        }
        // find variables declared by instructions
        for (ilist_iterator<BasicBlock> BI = F.begin(), BE = F.end(); BI != BE; ++BI)
        for (ilist_iterator<Instruction> II = BI->begin(), IE = BI->end(); II != IE; ++II)
        {
            std::string name;
            raw_string_ostream stream(name);
            II->print(stream);
            // check if it's a variable definition
            size_t st = name.find('%');
            size_t fi = name.find('=');
            if (st < fi && fi != std::string::npos)
            {
                // if so, include its name in the lattice
                name = name.substr(st, fi-st-1);
                itov.push_back(name);
                vtoi[II] = itov.size();
            }
        }
        // define lattice and do the analysis

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Lattice lattice(itov, false);
forwardSearch(F, &lattice, &reachingDefsTransition);

// Did not modify the incoming Function.
return false;
}

virtual void getAnalysisUsage(AnalysisUsage& AU) const {
    AU.setPreservesCFG();
}

private:
};

char ReachingDefinitions::ID = 0;
RegisterPass<ReachingDefinitions> X("cd-reaching-definitions",
    "15745 ReachingDefinitions");
}
```