

MK1



1



MK2



1



MK3



1



MK4



1



U1  
HOLE



U2  
HOLE



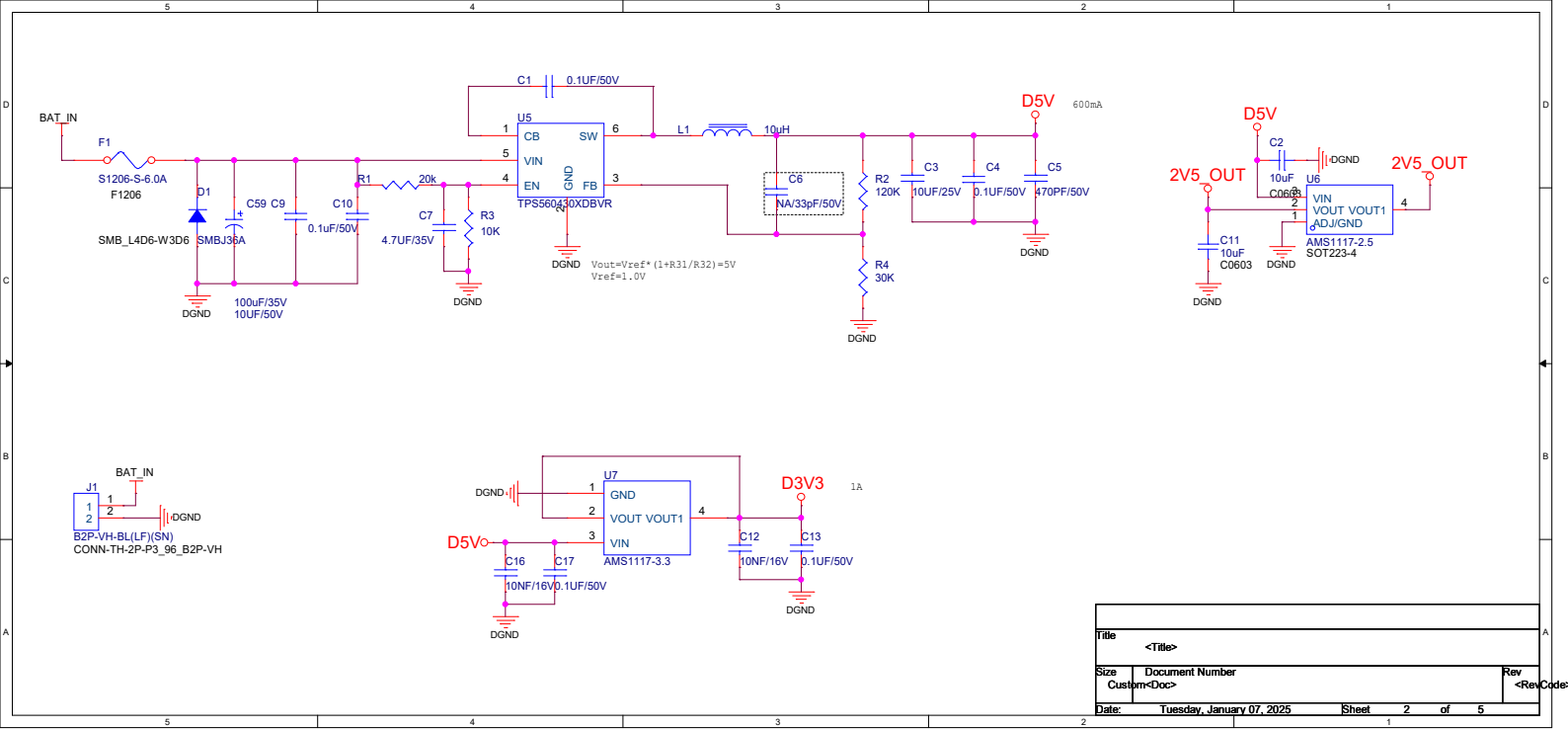
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HOLE



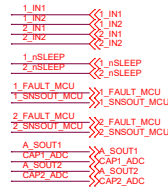
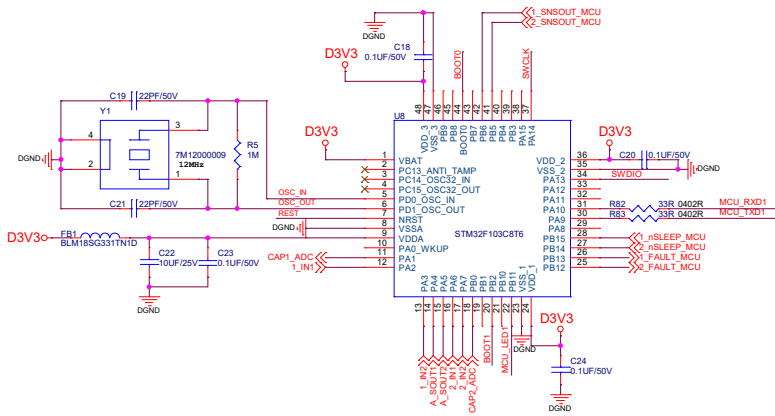
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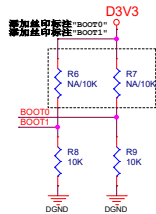


MCU

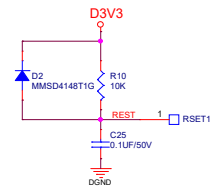


BOOT

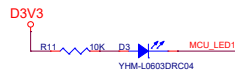
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1	1	SRAM
1	0	ISP



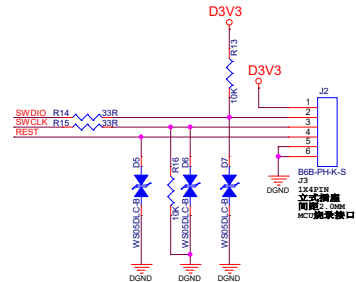
REST



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