

Bitwise AND



and,

andi

Bitwise OR



or,

ori

Bitwise NOT



nor

R type

I-type

format
 register and data -
 int and and 2

and \$t0, \$t1, \$t2

rd

rs

rt

\$t1 = 1110

\$t2 = 0101

∴ \$t0 = 0100

andi \$t0, \$t1, 2

\$t1 = 1011

\$t2 = 0010

∴ \$t0 = 0010

or \$t0, \$t1, \$t2

\$t1 = 1110

\$t2 = 0100

∴ \$t0 = 1110

ori \$t0, \$t1, 2

\$t1 = 1011

\$t2 = 0010

\$t0 = 1011

a nor b = Not (a or b)

অর্থাৎ a or b
 এর সাথে or operation
 এর then এর
 complement দে- হবে

Not করে হবে

nor \$t1, \$t2, \$zero.

\$t2 = 1011

\$zero = 0000

\therefore OR operation $\$t2 \vee \$zero = 1011$

2nd 2nd not 2nd.

\therefore \$t1 = 0100

now \$t1, \$t2, \$t3

\$t2 = 0100

\$t3 = 1000

OR operation $\$t2 \vee \$t3 = 1100$

NOT $\$t2 \vee \$t3$, \$t1 = 0011

Loops

while loop:



C-code:

```
while (save[i] == k) {
```

```
    a = a + 2;
```

```
    i++;
```

```
}
```

i, k, a are stored in \$s3, \$s5 and \$s4 respectively and base address of save

is \$s6.

MIPS Code:

Loop:

sll \$t0, \$s3, 2

add \$t0, \$t0, \$s6 # Memory address of save [i]

lw \$t1, 0(\$t0)

bne \$t1, \$s5, Exit

addi \$s4, \$s4, 2

addi \$s3, \$s3, 1

j loop # loop iterate again and again

Exit:

■ For loop:

for (int i = 0 ; save [i] > k ; i++) {

 a = a + 2

}

i, k, a are stored in \$s3, \$s5 and \$s4.

respectively. Base address of save is in \$s6.

MIPS Code:

add \$s3, \$zero, \$zero # i=0 assign করা হচ্ছে initially

loop:

· sll \$t0, \$s3, 2

add \$t0, \$t0, \$s6

lw \$t1, 0(\$t0)

slt \$t2, \$t1, \$s5

bne \$t2, \$zero, Exit

addi \$s4, \$s4, 2

addi \$s3, \$s3, 1

j loop

ব্যবহার, main code এ

> then < এর সাথে

compare করা, less than

হলে \$t2 তে 1 store

হবে।

Exit: