

Chapter-3

Arithmetic for Computers

Integer Addition: $7+6$

$$\begin{array}{r}
 \\
 \\
 + \\
 \hline

 \end{array}$$

Overflow if result out of range

Add (+ve, -ve) \longrightarrow No overflow

Add (+ve, +ve) \longrightarrow overflow if result sign 1

Add (-ve, -ve) \longrightarrow overflow if result sign 0

$$-2^{(n-1)} \text{ to } +2^{(n-1)} - 1$$

$$\Rightarrow -16 \text{ to } 15 \quad \{ \text{5 bit} \}$$

$$(+11) + (-1) = 10 \quad 1 \longrightarrow \begin{array}{l} 00001 \\ 11110 \\ 11111 \end{array}$$

$$\begin{array}{r}
 +11 \longrightarrow 01011 \\
 -1 \longrightarrow 11111 \\
 \hline
 \underline{01010}
 \end{array}$$

$$(-11) + (-15) = -26 \text{ (Overflow)}$$

$$\begin{array}{r}
 -11 \longrightarrow 10101 \\
 -15 \longrightarrow 10001 \\
 \hline
 \cancel{1} 00110 \\
 \uparrow
 \end{array}$$

$$(+11) + (+15) = 26$$

$$\begin{array}{r}
 +11 \longrightarrow 01011 \\
 +15 \longrightarrow 01111 \\
 \hline
 \underline{11010} \\
 \underline{\underline{4 \quad 3 \quad 2 \quad 1 \quad 0}} \\
 -2^4 + 2^3 + 2^1 = -6
 \end{array}$$

$$(+8) - (-4) = +12$$

$$(-8) - (+4) = -12$$

$$3 - (-15) = 18 \rightarrow \text{overflow}$$

↙ $3 + 15$

$$\begin{array}{r} 00011 \\ 01111 \\ \hline 10010 \\ \hline \end{array}$$

$$-8 - (+10) = -18 \rightarrow \text{overflow}$$

↙ $-8 - 10$

$$\begin{array}{r} 11000 \\ 10110 \\ \hline 01110 \\ \downarrow \\ \text{+ve} \end{array} \left. \vphantom{\begin{array}{r} 11000 \\ 10110 \\ \hline 01110 \\ \downarrow \\ \text{+ve} \end{array}} \right\} \text{Overflow}$$

Dealing with Overflow

addu, addui, subu (C program)

Step 1: If the current instruction has overflow, then the program counter holds the address of that instruction.

Step 2: Then the program counter saves the address of that instruction in exception pc register.

Step 3: Then the program counter jumps to the function, which handles overflow.

This function contains the set of instructions to overcome overflow.

Step 4: Lastly, the program counter retrieves EPC address using mfc0 instruction and resume instruction.

Multiplication

Length of product is the sum of operand lengths:

$$\begin{array}{r}
 1000 \rightarrow \text{multiplicand} \\
 \times 1001 \rightarrow \text{multiplier} \\
 \hline
 1000 \\
 0000 \\
 0000 \\
 1000 \\
 \hline
 1001000 \rightarrow \text{Product}
 \end{array}$$

- Long Multiplication Approach
- 4-bit architecture
- Multiplicand = 8 → 0000 1000
- Multiplier = 9 → 1001

Iteration	Multiplicand 0000 1000	Multiplier 1001	Product 0000 0000
1	0000 1000 000 10000 000 10000	1001 1001 0100	0000 1000 0000 1000 0000 1000
2	0010 0000 001 00000	0100 0010	0000 1000 0000 1000
3	0100 0000 0100 0000	0010 0001	0000 1000 0000 1000
4	0100 0000 1000 0000 1000 0000	0001 0001 0000	0100 1000 0100 1000 0100 1000

$$8 * 9 = 72$$