## Remi CHIERCHIA

2 George St, Brisbane City QLD 4000

e-mail: remi.chierchia@hdr.qut.edu.au website: remichierchia.github.io

#### **Education**

**Queensland University of Technology** 

CSIRO

Brisbane, Australia Canberra, Australia

PhD Student Feb-23 – Feb-27

**Topic:** 3D Surface Reconstruction of Wounds, NeRF, SDF

KTH Royal Institute of Technology

Stockholm, Sweden

**University of Trento** 

Trento, Italy

EIT Digital – Double Master's Degree in Visual Computing Communication – 110 cum laude/110 Sep-20 – June-22 cum laude/110 Sep-20

Thesis: Sub-frame Synchronisation and Motion Interpolation for Panoramic Video Stitching

Coursework: Signal Processing, Computer Vision, Recognition Systems, Image Forensics, Computer Graphics

University of Trento Trento, Italy

Bachelor's Degree in Information and Communications Engineering – 100/110

Sep-17 – Sep-20

**Thesis:** Tennis Player Analysis – Bio-mechanical Model and Computer Analysis

Coursework: Signal Processing, Electronics, Electromagnetic Waves, Computer Vision, Statistics/Probability

### **Experience**

Tracab Stockholm, Sweden

Junior Computer Vision Developer - Freelancer

Nov-21 - Nov-22

May-16 - July-16

- Comparison of video encoding standards H264-H265 for systems upgrade
- FFMpeg and OpenCV to assess the accuracy of qualitative, perceptual, and tracking systems (DLLs building)

EVCO S.p.a. Sedico, Italy

Electronics InternSpreadsheet softwares and manual duties

- Electronic components soldering and computer tasks

## **Projects**

#### Analysis and Search of Visual Data

Sep-21 - Oct-21

- Image feature matching, hierarchical tree search, image classification
- SIFT, SURF, TF-IDF, CNN were some of the methods used

Skeletal Animation Nov-20 – Jan-21

- C++ game engine for skeletal animation
- JSON pose information files and OpenGL libraries were used

## Image Forensics lab

Sep-20 - Nov-20

- Forensic digital watermarking in MATLAB
- Analysis focused on embedding quality and robustness methods

# Mouse and VGA drivers - VHDL mouse and VGA drivers for FPGA

June-18 - Nov-18

- The project was developed on a Nexys-4 ddr board

#### Additional Information

**Achievement**: EIT Digital Master School Scholarship **Technical skills**: C++, OpenCV, Python, MATLAB

Language: Italian (native), English

**Interests**: climbing and mountain sports – applied for the alpine rescue team, football – several years of playing, motorcycles